

DATA SHEET

**ELECTROSTATIC DISCHARGE
PROTECTION DEVICES**

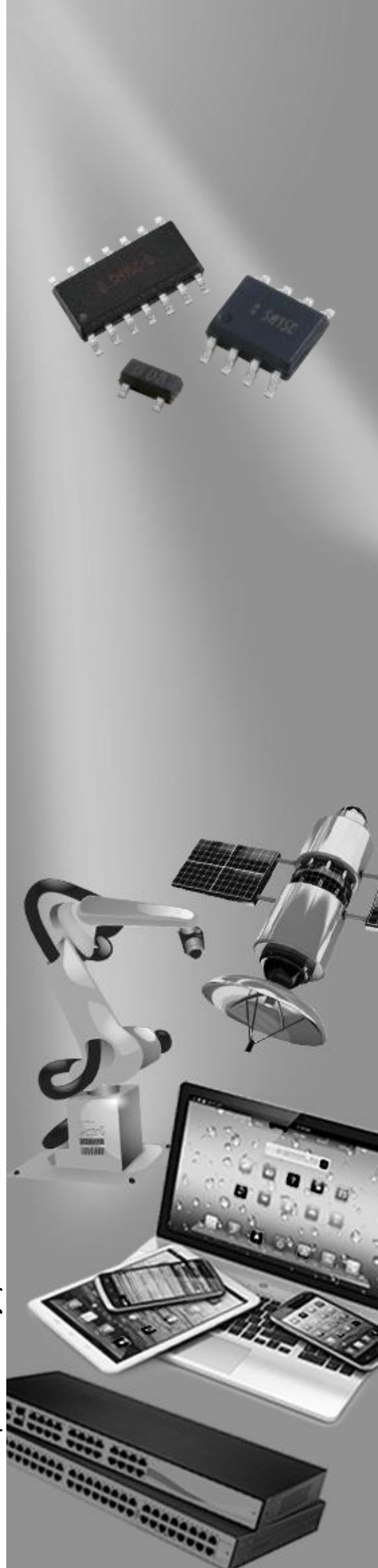
INDUSTRIAL / CONSUMER

UAD8C12L01

RoHS compliant & Halogen free



Product specification— July 04, 2023 V.2



Electrostatic Discharged Protection Devices (ESD) Data Sheet

Description

The UAD8C12L01 of Transient Voltage Suppressors is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computer, and PDAs.

It offer superior electrical characteristics such as lower clamping voltage and no device degradation when compared to MLVs. It is designed to protect sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD), lightning, electrical fast transients (EFT), and cable discharge events (CDE).



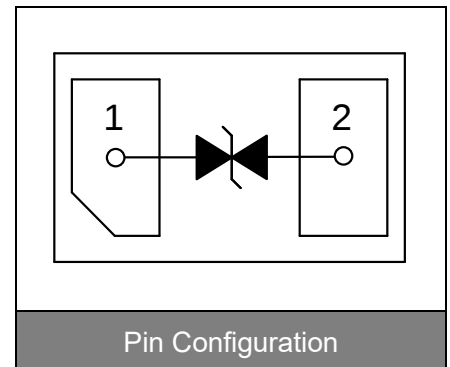
Contact : $\pm 20\text{kV}$

Air : $\pm 20\text{kV}$



Features

- IEC61000-4-2 ESD 20KV Air, 20KV contact compliance
- SOD882 surface mount package
- Working voltage: 12V
- Low leakage current
- Low operating and clamping voltages
- Solid-state silicon avalanche technology
- RoHS compliant
- Solder reflow temperature: Pure Tin-Sn, 260~270°C
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020
- Marking: B12



Applications

- MP3 players
- Touch Panels
- Personal digital assistants (PDA)
- Serial ATA protection
- Wireless system devices
- Handhelds and notebooks
- Digital cameras
- Portable Devices

Maximum Ratings

Rating	Symbol	Value	Unit
ESD voltage (Contact discharge)	V_{ESD}	± 20	kV
ESD voltage (Air discharge)		± 20	
Storage & operating temperature range	T_{STG}, T_J	-55~+150	°C

Electrical Characteristics ($T_J=25^{\circ}\text{C}$)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V_{RWM}				12	V
Reverse breakdown voltage	V_{BR}	$I_{BR}=1\text{mA}$	13.7			V
Reverse leakage current	I_R	$V_R=12\text{V}$			0.5	μA
Clamping voltage ($t_p=8/20\mu\text{s}$)	V_C	$I_{PP}=3\text{A}$		18		V
Peak pulse current ($t_p=8/20\mu\text{s}$)	I_{PP}				3	A
ESD Clamping voltage (TLP)	V_C	$I_{PP}=8\text{A}$		16.4		V
ESD Clamping voltage (TLP)	V_C	$I_{PP}=16\text{A}$		18		V
ESD Dynamic Turn-on Resistance	$R_{dynamic}$			0.25		Ω
Off state junction capacitance	C_J	0Vdc, f=1MHz		3.5	5.0	pF

Typical Characteristics Curves

Figure 1. Pulse Waveforms

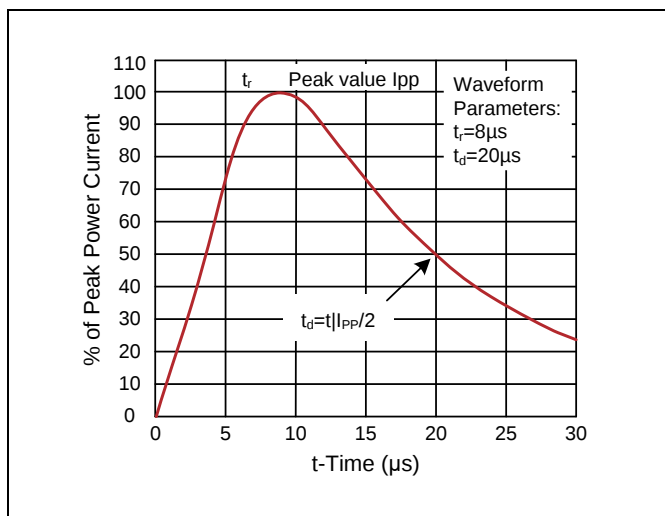


Figure 2. Clamping Voltage vs. Peak Pulse Current

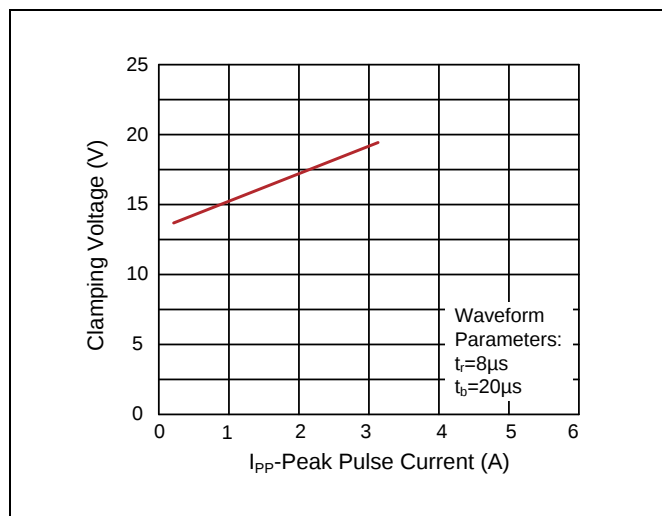


Figure 3. Capacitance vs. Reverse Voltage

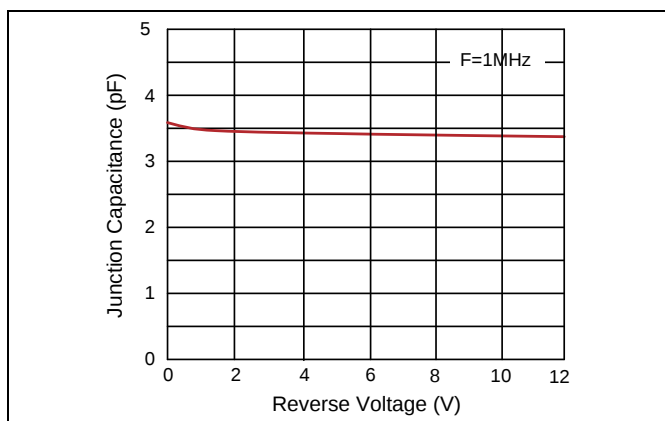


Figure 4. Power Derating Curve

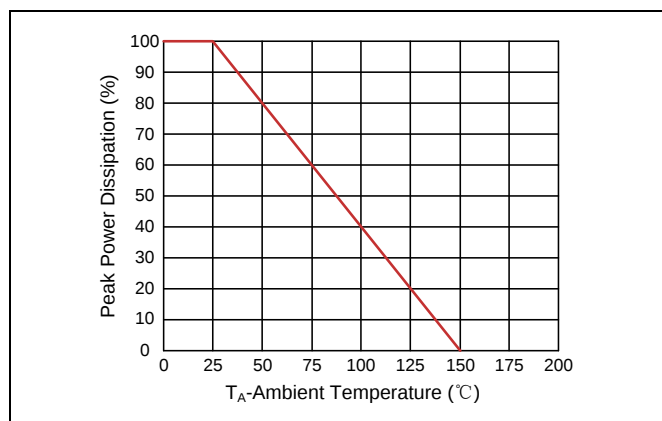


Figure 5. ESD Clamping (8kV Contact IEC61000-4-2)

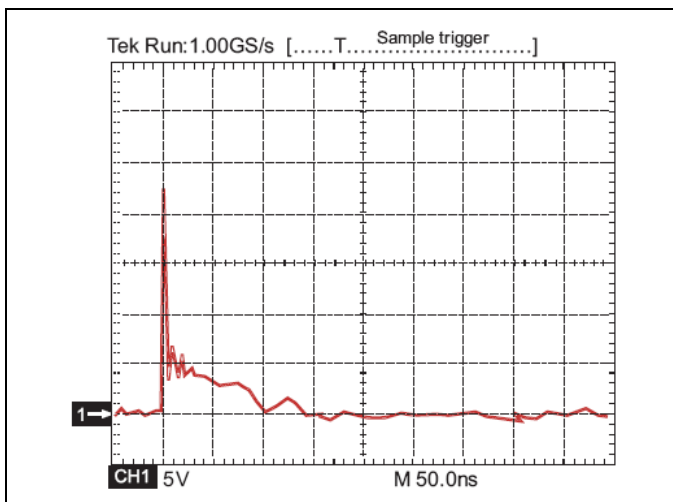
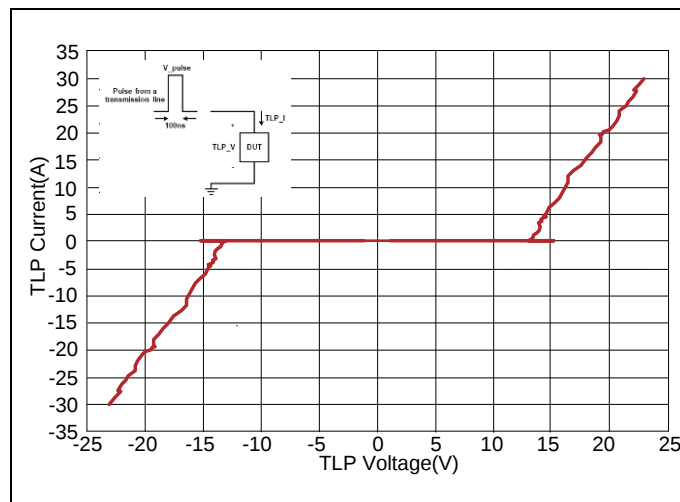
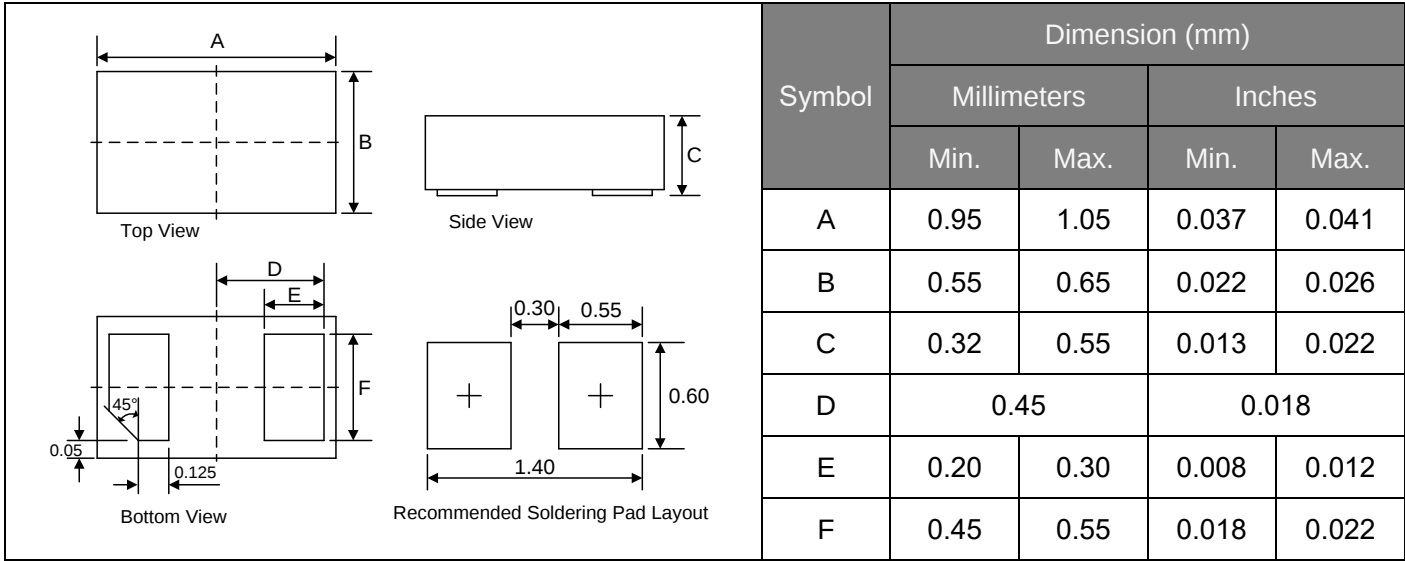


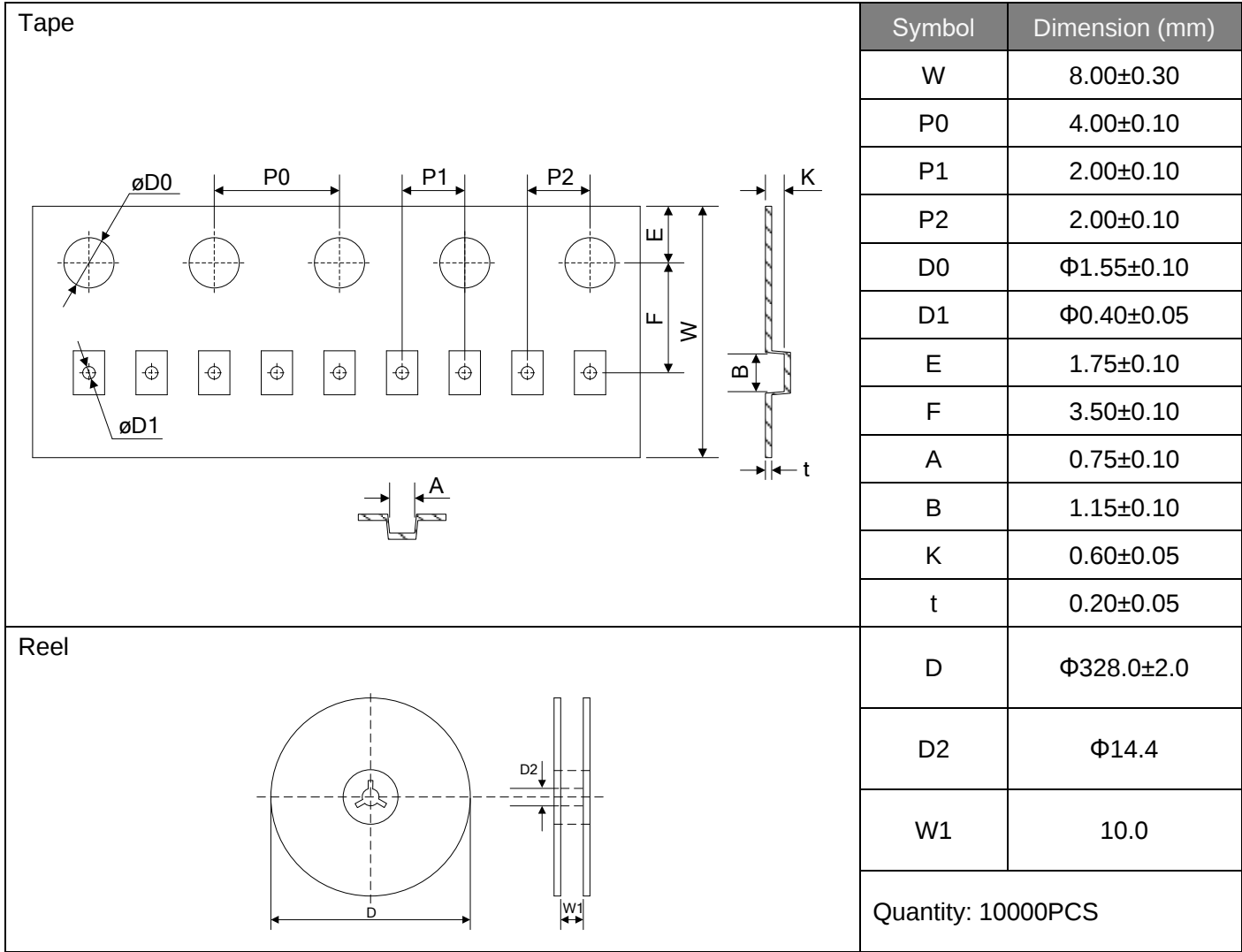
Figure 6. Transmission Line Pulsing (TLP) Measurement



Dimensions (SOD882)



Packaging



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