

The XDXL62004 Series are high-voltage, high-current darlington drivers comprised of seven NPN darlington pairs. All units feature integral clamp diodes for switching inductive loads.

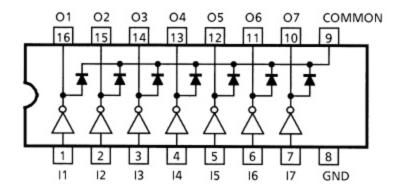
Applications include relay, hammer, lamp and display (LED) drivers.

The suffix (G) appended to the part number represents a Lead (Pb)-Free product.

Features

- Output current (single output): 500 mA max
- High sustaining voltage output: 50 V min
- Output clamp diodes
- Inputs compatible with various types of logic

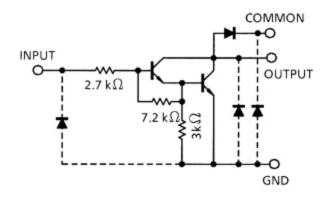
Pin Connection (top view)



Type	Input Base Resistor	Designation
XDXL62004	2.7 kΩ	TTL, 5 V CMOS

Schematics (each driver)

XDXL62004



Note: The input and output parasitic diodes cannot be used as clamp diodes.

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit		
Output sustaining voltage	V _{CE} (SUS)	- 0.5~50	V		
Output current	lout	500	mA/ch		
Input voltage	V _{IN}	-0.5~30	V		
Clamp diode reverse voltage	V_{R}	50	V		
Clamp diode forward currer	l _F	500	mA		
Power dissipation	XD62004	P _D	1.47	W	
rower dissipation	XL62004	טי	1.25 (Note)		
Operating temperature	T _{opr}	- 40~85	°C		
Storage temperature	T _{stg}	- 55~150	°C		

Note: On PCB (Test Board: JEDEC 2s2p)

Recommended Operating Conditions ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Charac	cteristic	Symbol	Test Condition		Min	Тур.	Max	Unit
Output sustaining v	voltage	V _{CE (SUS)}			0	_	50	V
Output current	XD62004	Гоит	T_{pw} = 25 ms 7 Circuits Ta = 85°C T_j = 120°C	Duty = 10%	0	_	350	- mA/ch
				Duty = 50%	0	_	100	
	XL62004			Duty = 10%	0	-	300	
				Duty = 50%	0	_	90	
Input voltage		V _{IN}			0	_	24	V
Input voltage		Various	I _{OUT} = 400 mA		2.8	_	24	V
(output on)	XDXL62004	V _{IN (ON)}	h _{FE} = 800			_		V
Input voltage XDXL62004		VINTORE			0	_	0.7	V
(output off)	ADALOZOU4 VIN (OFF	V _{IN} (OFF)				_		·
Clamp diode reverse voltage		V_{R}			I	_	50	V
Clamp diode forwa	rd current	I _F			_	_	350	mA
Power dissipation	XD62004	P _D	Ta = 85°C		_	_	0.76	w
	XL62004	רט	Ta = 85°C	(Note)	_	_	0.65	VV

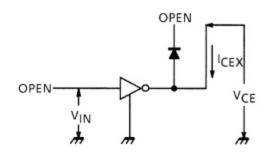
Note: On PCB (Test Board: JEDEC 2s2p)

Electrical Characteristics (Ta = 25°C unless otherwise noted)

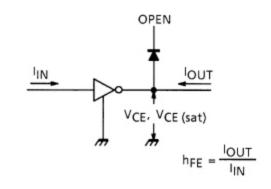
Characteri	stic	Symbol	Test Circuit			Min	Тур.	Max	Unit	
Output leakage current		1	1	V _{CE} = 50 V, Ta = 25°C		_	_	50	μА	
		I _{CEX}		$V_{CE} = 50 \text{ V},$	V _{CE} = 50 V, Ta = 85°C		_	100		
Collector-emitter saturation voltage		V _{CE} (sat)	2	$I_{OUT} = 350 \text{ mA}, I_{IN} = 500 \mu\text{A}$		_	1.3	1.6	٧	
				I _{OUT} = 200 mA, I _{IN} = 350 μA		_	1.1	1.3		
				I _{OUT} = 100 mA, I _{IN} = 250 μA		_	0.9	1.1		
DC Current transfer ratio		h _{FE}	2	V _{CE} = 2 V, I _{OUT} = 350 mA		1000	_	_		
Input current	VDVI 6200.4		3	$V_{IN} = 2.4 V$,	V _{IN} = 2.4 V, I _{OUT} = 350 mA		0.4	0.7	mA	
(output on)	XDXL62004	IIN (ON)	3							
Input current (output off)		I _{IN (OFF)}	4	I _{OUT} = 500 μA, Ta = 85°C		50	65	_	μА	
	XDXL62004	V	5	V _{CE} = 2 V	I _{OUT} = 350 mA	1	_	2.6	V	
Input voltage					I _{OUT} = 200 mA	1	_	2.0		
(output on)	ADAL62004	V _{IN} (ON)	3	h _{FE} = 800						
Clamp diode reverse current		I _R 6	V _R = 50 V, Ta = 25°C		_	_	50			
			6	V _R = 50 V, Ta = 85°C		_	_	100	μА	
Clamp diode forward voltage		V _F	7	I _F = 350 mA			_	2.0	V	
Input capacitance		C _{IN}	_			_	15	_	pF	
Tum-on delay		t _{ON}	8	$V_{OUT} = 50 \text{ V}, R_L = 125 \Omega$ $C_L = 15 \text{ pF}$		_	0.1	-	6	
Tum-off delay		t _{OFF}	8	$V_{OUT} = 50 \text{ V}, R_L = 125 \Omega$ $C_L = 15 \text{ pF}$		_	0.2	-	μS	

Test Circuit

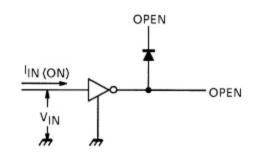
1. I_{CEX}



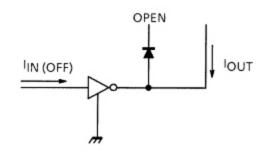
2. V_{CE (sat),} h_{FE}



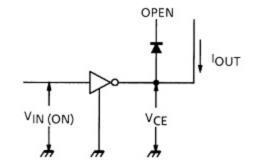
3. I_{IN (ON)}



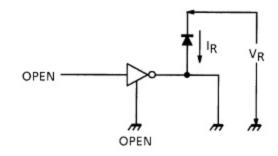
4. I_{IN (OFF)}



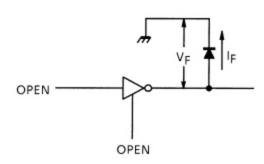
5. V_{IN (ON)}



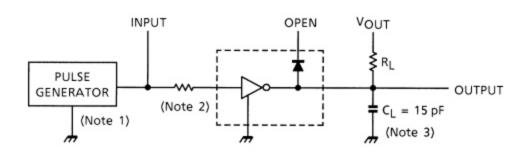
6. I_R

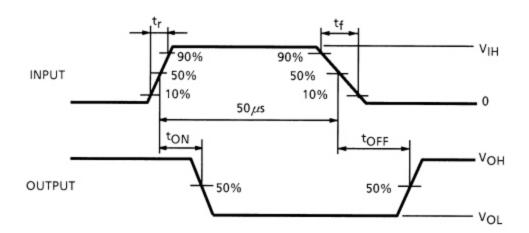


7. V_F



8. t_{ON}, t_{OFF}





Note 1: Pulse width 50 µs, duty cycle 10%

Output impedance 50 Ω , $t_f \le 5$ ns, $t_f \le 10$ ns

Note 2: See below

Input Condition

Type Number	R1	V _{IH}
XDXL62004	0	3 V

Note 3: CL includes probe and jig capacitance.

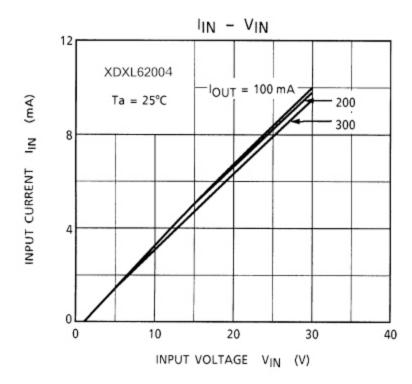
Precautions for Using

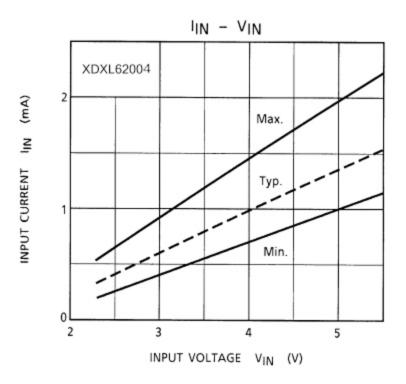
This IC does not include built-in protection circuits for excess current or overvoltage.

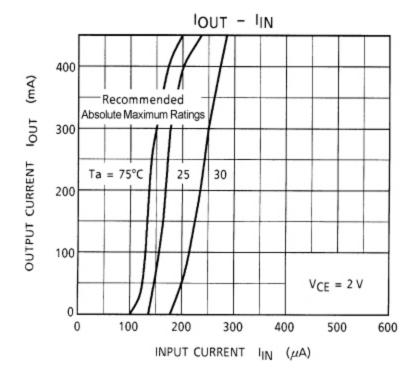
If this IC is subjected to excess current or overvoltage, it may be destroyed.

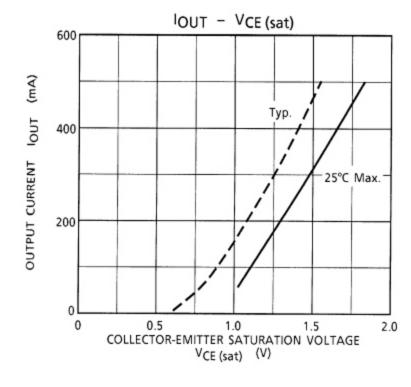
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

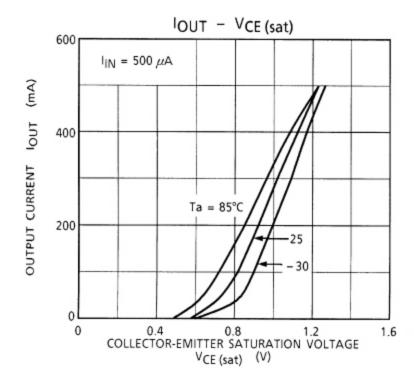
Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

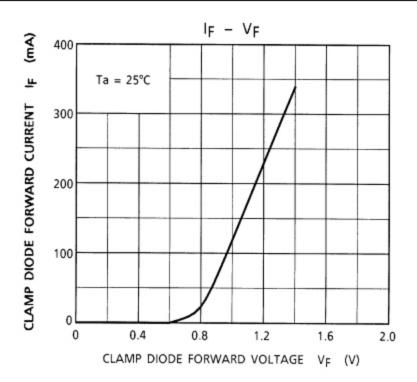


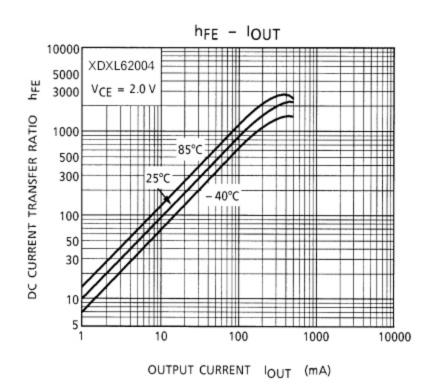


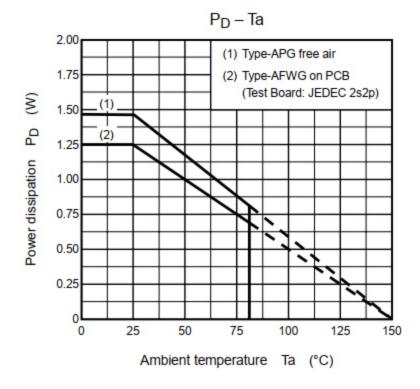




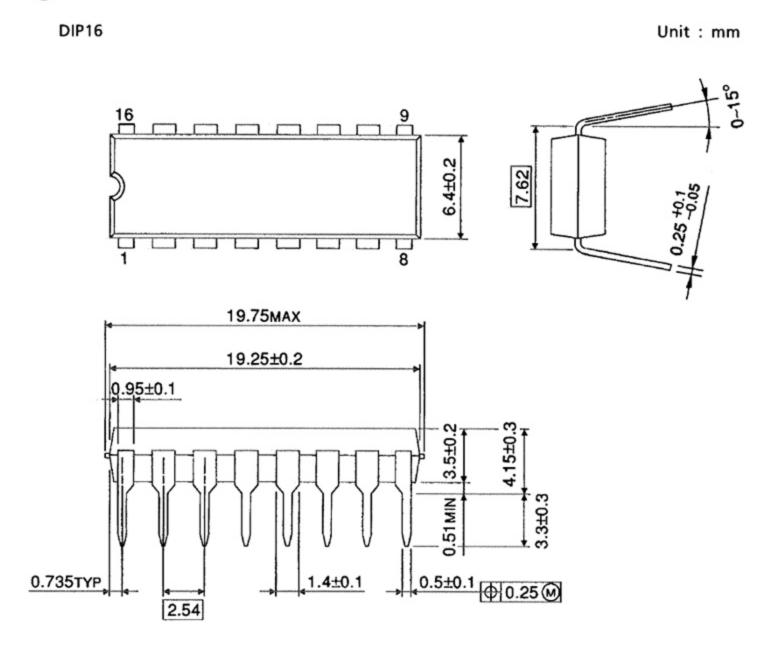






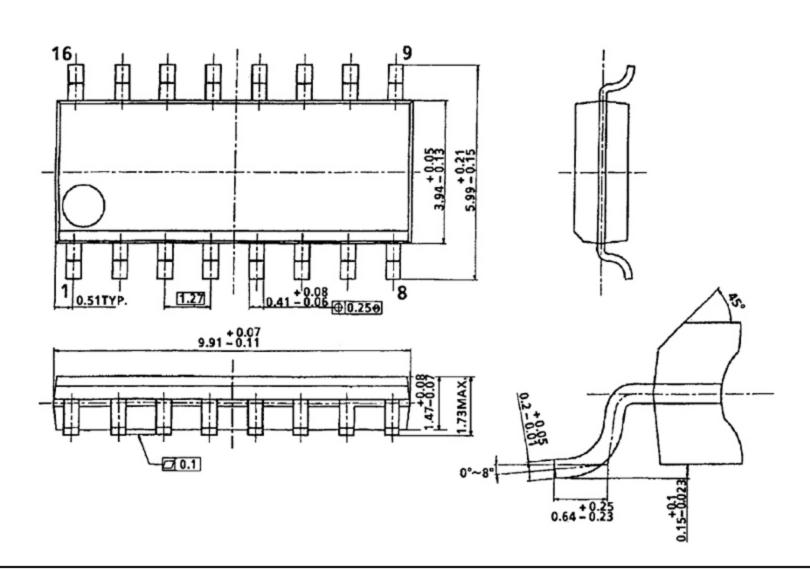


Package Dimensions



Package Dimensions

SOP16 Unit: mm



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