



General Description

The WSD100N15DN56G is the highest performance trench N-ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSD100N15DN56 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

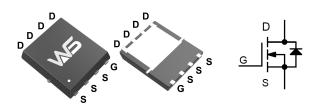
Product Summery

| BVDSS | RDSON | ID |
|-------|-------|------|
| 150V | 9mΩ | 100A |

Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System

DFN5X6-8L Pin Configuration



Absolute Maximum Ratings

| Symbol | Parameter | Rating | Units | |
|------------------|---|------------|---------------|--|
| V _{DS} | Drain-Source Voltage | 150 | V | |
| V_{GS} | Gate-Source Voltage | ±20 | V | |
| I _D | Continuous Drain Current, V _{GS} @ 10V(T _C =25 ℃) | 100 | Α | |
| I _{DM} | Pulsed Drain Current | 360 | Α | |
| EAS | Single Pulse Avalanche Energy | 400 | mJ | |
| P _D | Total Power Dissipation _C =25 ^C) | 160 | W | |
| RθJA | Thermal resistance, junction-ambient | 62 | °C/W | |
| RθJC | Thermal resistance, junction-case | 0.78 | °C/W | |
| T _{STG} | Storage Temperature Range -55 to 175 | | $^{\circ}$ | |
| T _J | Operating Junction Temperature Range | -55 to 175 | ${\mathbb C}$ | |



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Unit |
|---------------------|--|---|------|------|------|------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V , I _D =250uA | 150 | | | V |
| R _{DS(ON)} | Static Drain-Source On-Resistance ² | V _{GS} =10V , I _D =20A | | 9 | 12 | mΩ |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{GS}=V_{DS}$, $I_D=250uA$ | 2.0 | 3.0 | 4.0 | V |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =100V , V _{GS} =0V , T _J =25℃ | | | 1 | uA |
| I _{GSS} | Gate-Source Leakage Current | $V_{GS}=\pm 20V$, $V_{DS}=0V$ | | | ±100 | nA |
| Qg | Total Gate Charge | | | 66 | | |
| Q _{gs} | Gate-Source Charge | V _{DS} =50V , V _{GS} =10V , I _D =20A | | 26 | | nC |
| Q _{gd} | Gate-Drain Charge | _ | | 18 | | |
| T _{d(on)} | Turn-On Delay Time | V _{DD} =50V , | | 37 | | |
| Tr | Rise Time | V _{GS} =10V | | 98 | | 20 |
| T _{d(off)} | Turn-Off Delay Time | $R_G=2\Omega$, | | 55 | | ns |
| T _f | Fall Time | I _D =20A | | 20 | | |
| C _{iss} | Input Capacitance | | | 5450 | | |
| Coss | Output Capacitance | V _{DS} =30V , V _{GS} =0V , f=1MHz | | 1730 | | pF |
| C _{rss} | Reverse Transfer Capacitance | | | 195 | | |

Diode Characteristics

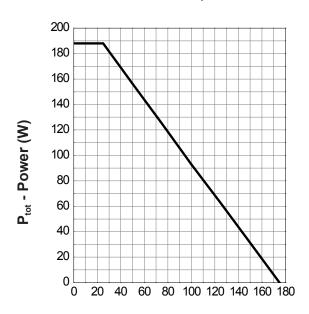
| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Unit |
|-----------------|--|---|------|------|------|------|
| I _S | Continuous Source Current ^{1,6} | V =V =0V Force Current | | | 100 | Α |
| I _{SM} | Pulsed Source Current ^{2,6} | V _G =V _D =0V , Force Current | | | 360 | Α |
| V_{SD} | Diode Forward Voltage ² | V _{GS} =0V , I _S =20A , T _J =25℃ | | | 1.3 | V |

■ Note

- ¹) Repetitive rating; pulse width limited by max. junction temperature.
- 2) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 3) The value of R0JA is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 °C.
- 4) VDD=50 V, RG=25 Ω , L=0.5 mH, starting Tj=25 °C.
- ⁵) Calculated continuous current based on maximum allowable junction temperature.

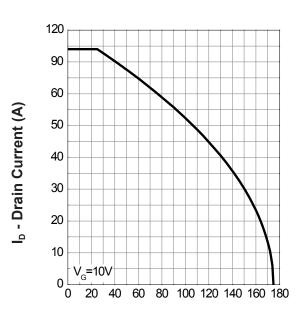


Power Dissipation



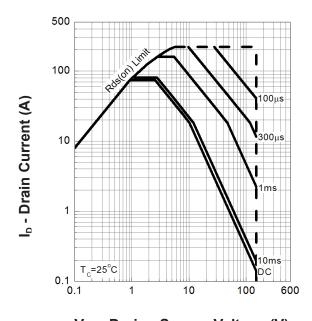
T_c - Case Temperature (°C)

Drain Current



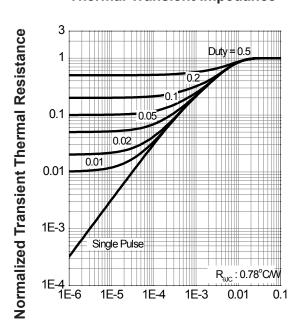
T_c - Case Temperature (°C)

Safe Operation Area



V_{DS} - Drain - Source Voltage (V)

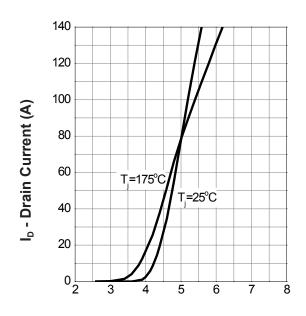
Thermal Transient Impedance



Square Wave Pulse Duration (sec)

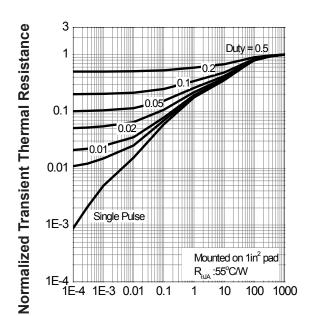


Transfer Characteristics



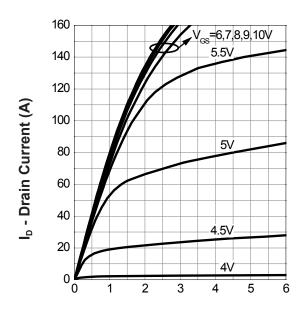
V_{GS} - Gate-Source Voltage (V)

Thermal Transient Impedance



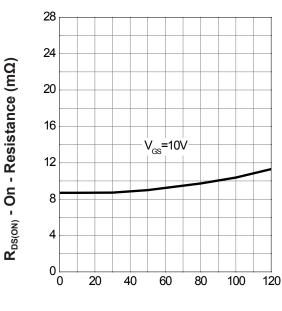
Square Wave Pulse Duration (sec)

Output Characteristics



V_{DS} - Drain - Source Voltage (V)

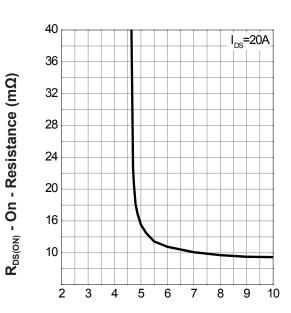
Drain-Source On Resistance



I_D - Drain Current (A)

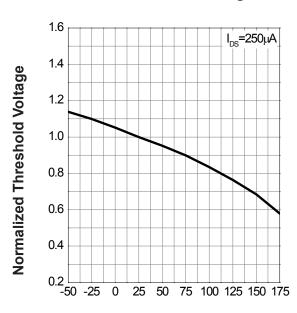


Gate-Source On Resistance



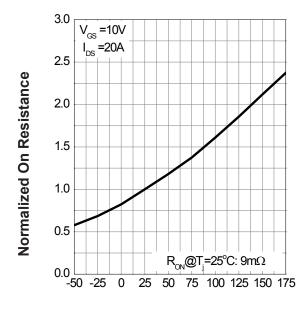
V_{GS} - Gate - Source Voltage (V)

Gate Threshold Voltage



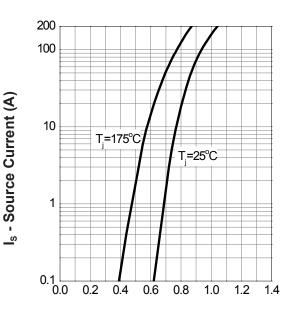
T_j - Junction Temperature (°C)

Drain-Source On Resistance



T_j - Junction Temperature (°C)

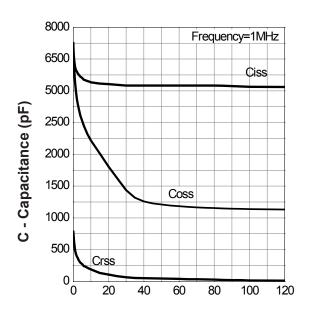
Source-Drain Diode Forward



V_{SD} - Source - Drain Voltage (V)

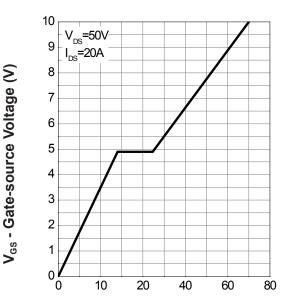


Capacitance



V_{DS} - Drain-Source Voltage (V)

Gate Charge



Q_G - Gate Charge (nC)



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