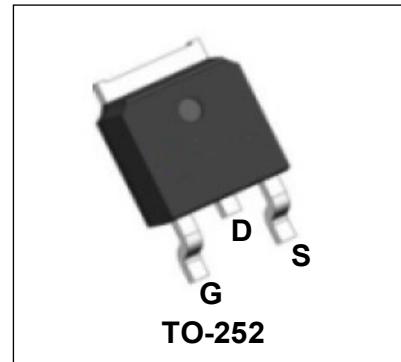


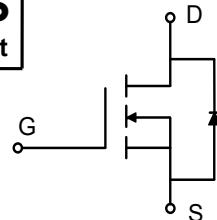
## 60V N-Channel Enhancement Mode Power MOSFET

**Description**

WMO50N06TS uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

**Features**

- $V_{DS} = 60V$ ,  $I_D = 50A$
- $R_{DS(on)} < 16m\Omega$  @  $V_{GS} = 10V$
- $R_{DS(on)} < 20m\Omega$  @  $V_{GS} = 4.5V$
- Green Device Available
- 100% EAS Guaranteed
- Optimized for High Speed Smooth Switching

**Applications**

- Synchronous Rectification
- DC/DC Converter

**Absolute Maximum Ratings ( $T_A = 25^\circ C$ , unless otherwise noted)**

| Parameter  | Symbol            | Value      | Unit |
|--|-------------------|------------|------|
| Drain-Source Voltage                             | $V_{DS}$          | 60         | V    |
| Gate-Source Voltage                              | $V_{GS}$          | $\pm 20$   | V    |
| Continuous Drain Current<br>$T_C=25^\circ C$     | $I_D$             | 50         | A    |
|  |                   | 31.6       |      |
| Pulsed Drain Current <sup>1</sup>                | $I_{DM}$          | 200        | A    |
| Single Pulse Avalanche Energy <sup>2</sup>       | $EAS$             | 96.8       | mJ   |
| Total Power Dissipation                          | $P_D$             | 69.4       | W    |
| Operating Junction and Storage Temperature Range | $T_J$ , $T_{STG}$ | -55 to 150 | °C   |

**Thermal Characteristics**

| Parameter  | Symbol          | Value | Unit |
|--|-----------------|-------|------|
| Thermal Resistance from Junction-to-Ambient <sup>3</sup> | $R_{\theta JA}$ | 48    | °C/W |
| Thermal Resistance from Junction-to-Case                 | $R_{\theta JC}$ | 1.8   | °C/W |

**Electrical Characteristics (T<sub>J</sub> = 25°C, unless otherwise noted)**

| Parameter   | Symbol               | Test Conditions  | Min. | Typ. | Max. | Unit |
|---|----------------------|--|------|------|------|------|
| <b>Static Characteristics</b>                           |                      |  |      |      |      |      |
| Drain-Source Breakdown Voltage                          | V <sub>(BR)DSS</sub> | V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA   | 60   | -    | -    | V    |
| Gate-Body Leakage Current                               | I <sub>GSS</sub>     | V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V   | -    | -    | ±100 | nA   |
| Zero Gate Voltage Drain Current<br>T <sub>J</sub> =25°C | I <sub>DSS</sub>     | V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V  | -    | -    | 1    | μA   |
| T <sub>J</sub> =100°C                                   |                      |  | -    | -    | 100  |      |
| Gate-Threshold Voltage                                  | V <sub>GS(th)</sub>  | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA                                 | 1.2  | 1.9  | 2.5  | V    |
| Drain-Source on-Resistance <sup>4</sup>                 | R <sub>DSS(on)</sub> | V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A  | -    | 12.5 | 16   | mΩ   |
|   |                      | V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5A  | -    | 16   | 20   |      |
| Forward Transconductance <sup>4</sup>                   | g <sub>fs</sub>      | V <sub>DS</sub> = 10V, I <sub>D</sub> = 20A  | -    | 36   | -    | S    |
| <b>Dynamic Characteristics<sup>5</sup></b>              |                      |  |      |      |      |      |
| Input Capacitance                                       | C <sub>iss</sub>     | V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V,<br>f = 1MHz                                   | -    | 2690 | -    | pF   |
| Output Capacitance                                      | C <sub>oss</sub>     |  | -    | 120  | -    |      |
| Reverse Transfer Capacitance                            | C <sub>rss</sub>     |  | -    | 98   | -    |      |
| Gate Resistance   | R <sub>G</sub>       | f = 1MHz   | -    | 0.6  | -    | Ω    |
| <b>Switching Characteristics<sup>5</sup></b>            |                      |  |      |      |      |      |
| Total Gate Charge                                       | Q <sub>g</sub>       | V <sub>GS</sub> = 10V, V <sub>DD</sub> = 30V,<br>I <sub>D</sub> = 20A                      | -    | 46   | -    | nC   |
| Gate-Source Charge                                      | Q <sub>gs</sub>      |  | -    | 8.5  | -    |      |
| Gate-Drain Charge                                       | Q <sub>gd</sub>      |  | -    | 7.5  | -    |      |
| Turn-on Delay Time                                      | t <sub>d(on)</sub>   | V <sub>GS</sub> = 10V, V <sub>DD</sub> = 30V,<br>R <sub>G</sub> = 3Ω, I <sub>D</sub> = 20A | -    | 10.5 | -    | ns   |
| Rise Time   | t <sub>r</sub>       |  | -    | 21.2 | -    |      |
| Turn-off Delay Time                                     | t <sub>d(off)</sub>  |  | -    | 35   | -    |      |
| Fall Time   | t <sub>f</sub>       |  | -    | 13   | -    |      |
| Body Diode Reverse Recovery Time                        | t <sub>rr</sub>      | I <sub>F</sub> = 20A, dI <sub>F</sub> /dt = 100A/μs  | -    | 38   | -    | ns   |
| Body Diode Reverse Recovery Charge                      | Q <sub>rr</sub>      |  | -    | 72   | -    | nC   |
| <b>Drain-Source Body Diode Characteristics</b>          |                      |  |      |      |      |      |
| Diode Forward Voltage <sup>4</sup>                      | V <sub>SD</sub>      | I <sub>S</sub> = 20A, V <sub>GS</sub> = 0V   | -    | -    | 1.2  | V    |
| Continuous Source Current                               | T <sub>c</sub> =25°C | I <sub>S</sub>   | -    | -    | 50   | A    |

Notes:

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C.
2. The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.4mH, I<sub>AS</sub>=22A
3. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.

## Typical Characteristics

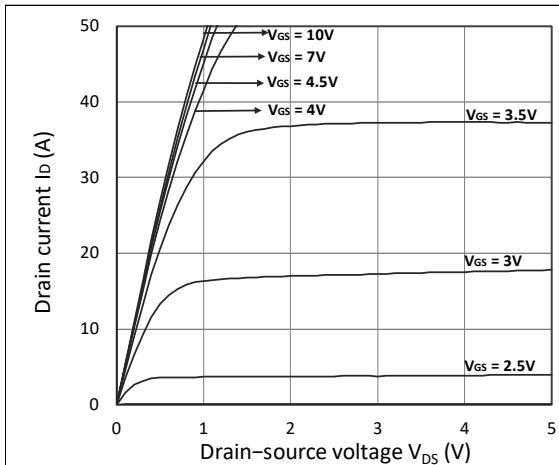


Figure 1. Output Characteristics

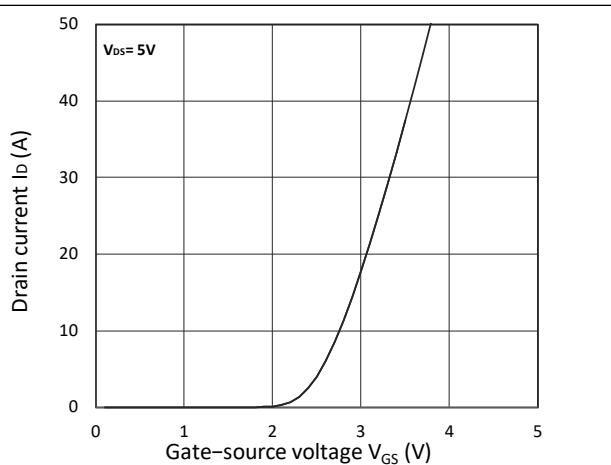


Figure 2. Transfer Characteristics

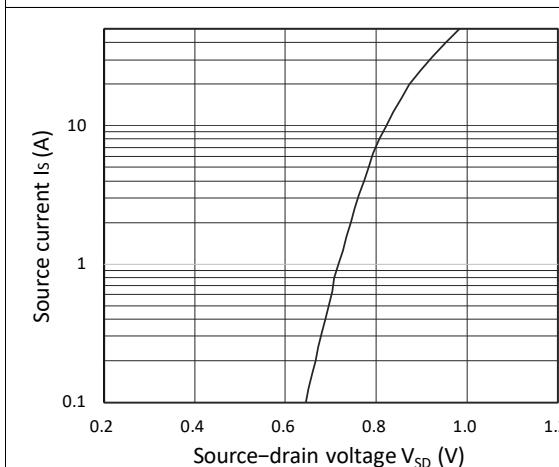
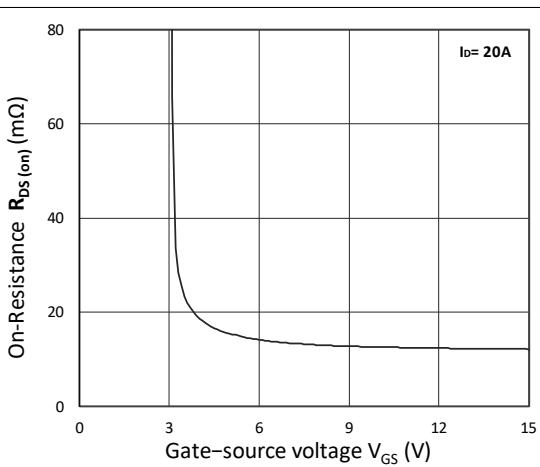
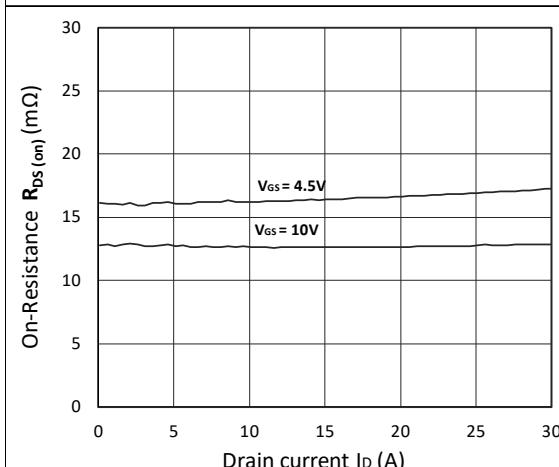
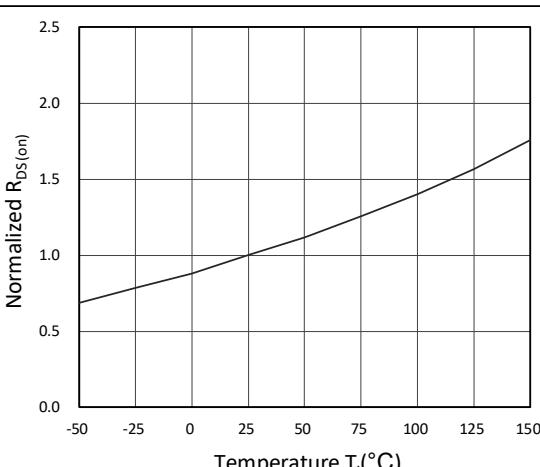


Figure 3. Forward Characteristics of Reverse

Figure 4.  $R_{DS(ON)}$  vs.  $V_{GS}$ Figure 5.  $R_{DS(ON)}$  vs.  $I_D$ Figure 6. Normalized  $R_{DS(ON)}$  vs. Temperature

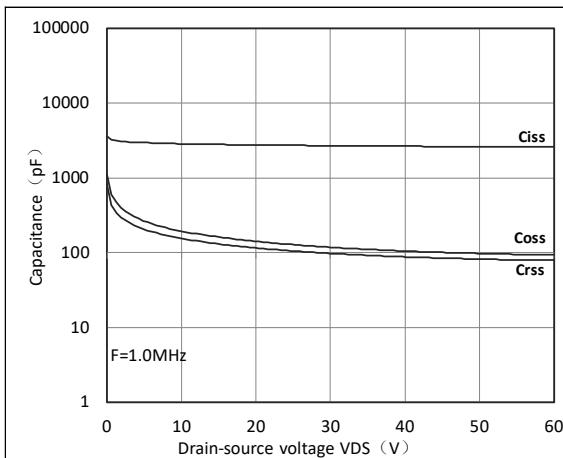


Figure 7. Capacitance Characteristics

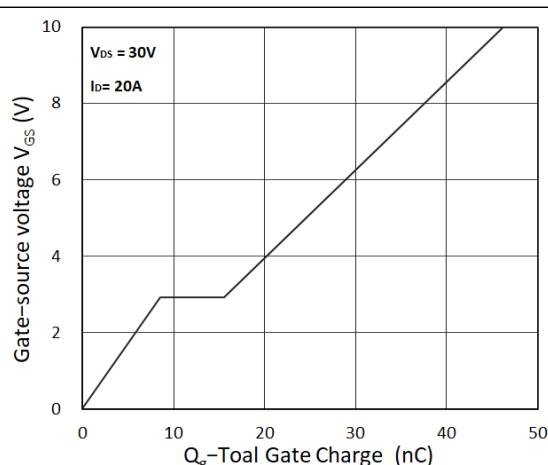


Figure 8. Gate Charge Characteristics

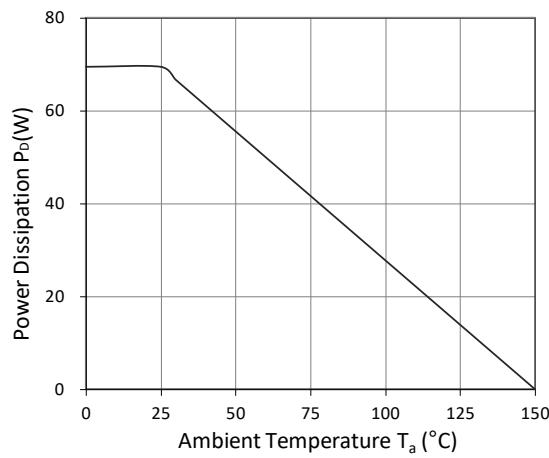


Figure 9. Power Dissipation

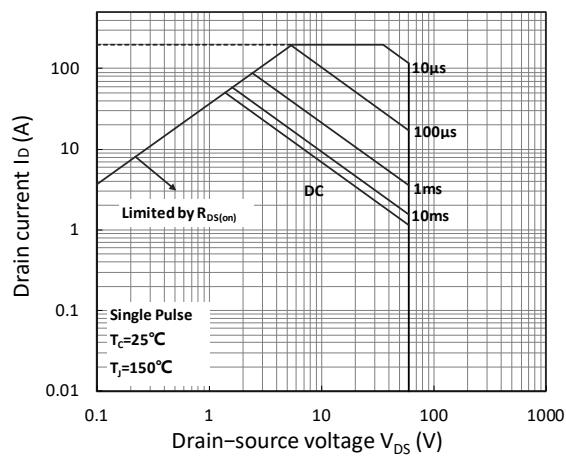


Figure 10. Safe Operating Area

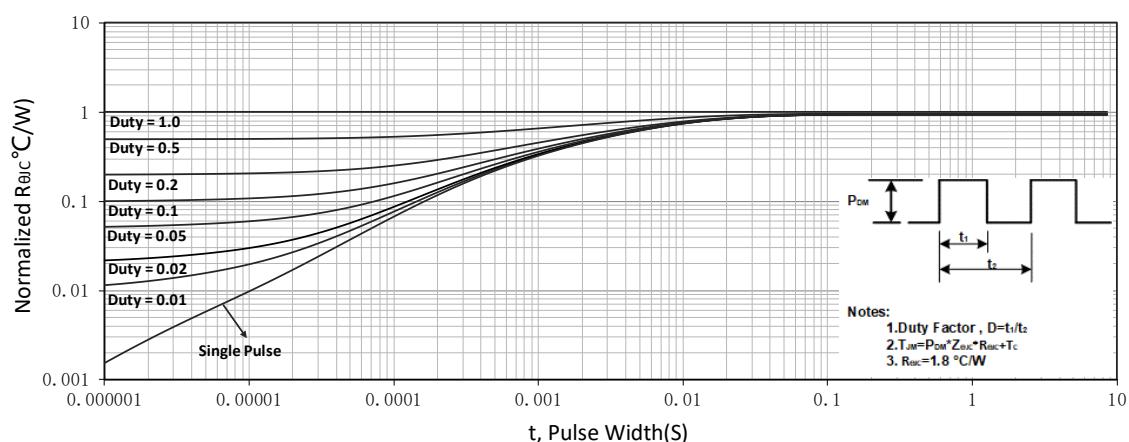
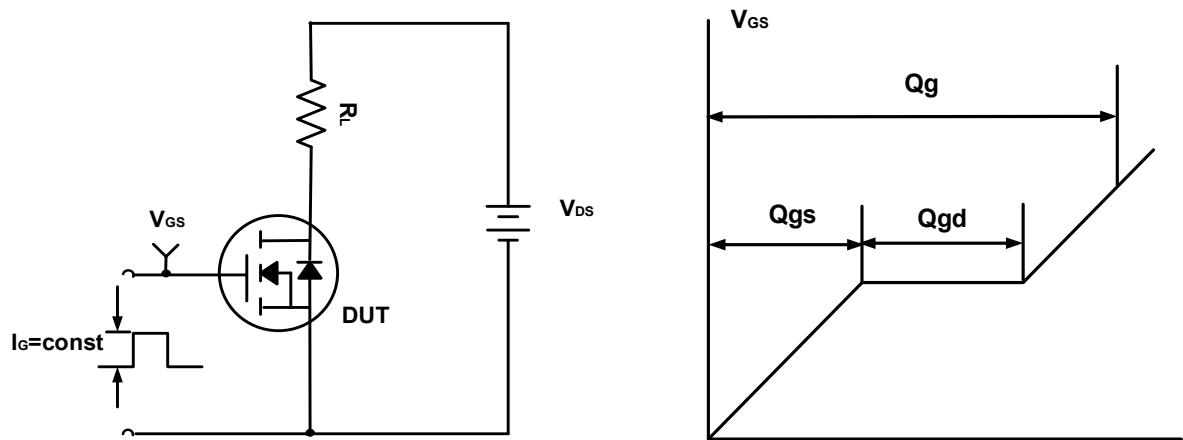
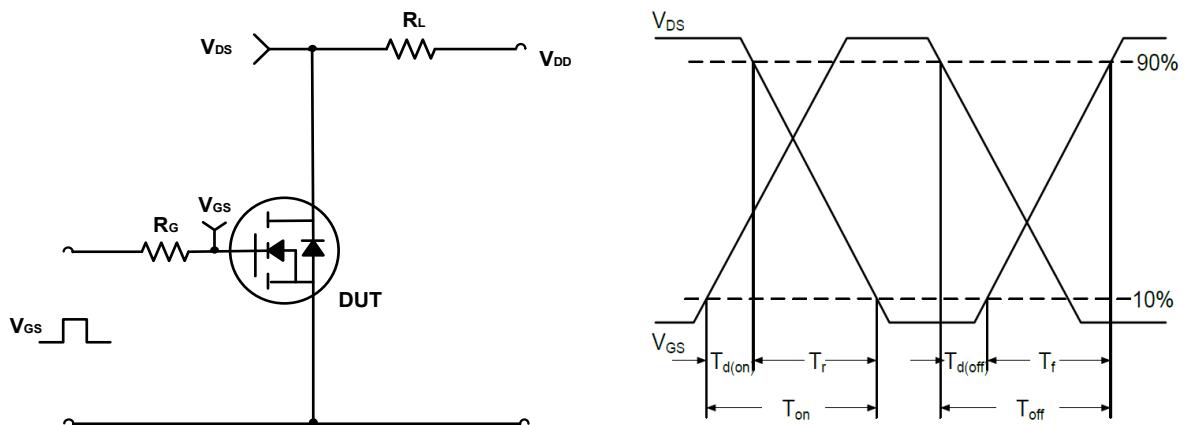
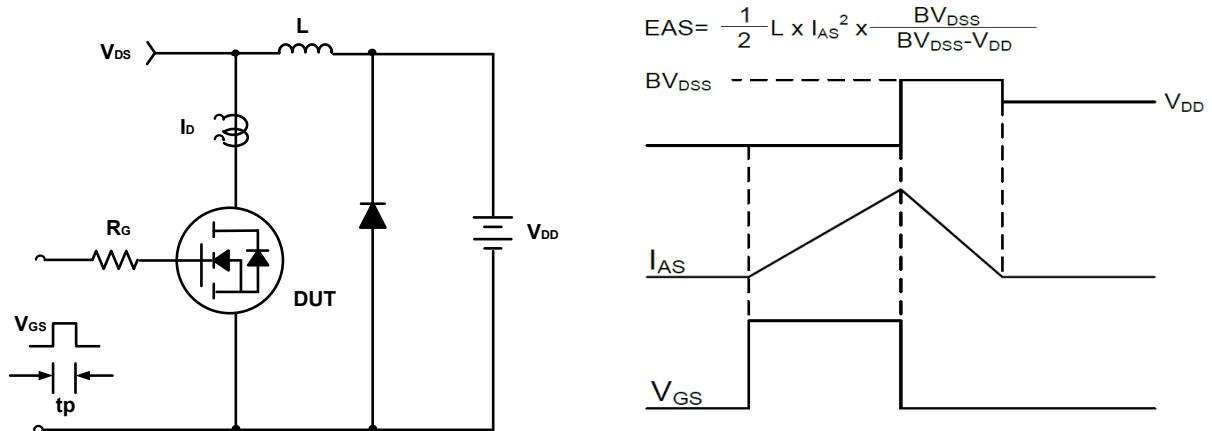
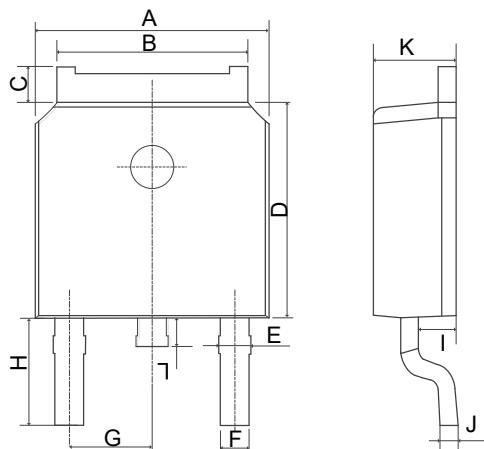


Figure 11. Normalized Maximum Transient Thermal Impedance

**Test Circuit****Figure A. Gate Charge Test Circuit & Waveforms****Figure B. Switching Test Circuit & Waveforms****Figure C. Unclamped Inductive Switching Circuit & Waveforms**

## Mechanical Dimensions for TO-252



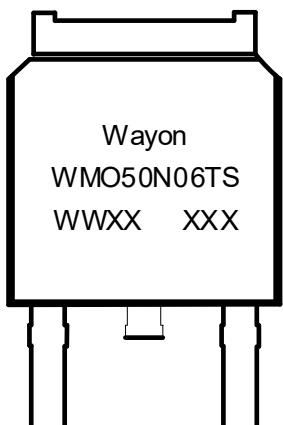
## COMMON DIMENSIONS

| SYMBOL | MM      |      |
|--------|---------|------|
|        | MIN     | MAX  |
| A      | 6.40    | 6.80 |
| B      | 5.13    | 5.50 |
| C      | 0.88    | 1.28 |
| D      | 5.90    | 6.22 |
| E      | 0.68    | 1.10 |
| F      | 0.68    | 0.91 |
| G      | 2.29REF |      |
| H      | 2.90REF |      |
| I      | 0.85    | 1.17 |
| J      | 0.51REF |      |
| K      | 2.10    | 2.50 |
| L      | 0.40    | 1.00 |

## Ordering Information

| Part       | Package | Marking    | Packing method |
|------------|---------|------------|----------------|
| WMO50N06TS | TO-252  | WMO50N06TS | Tape and Reel  |

## Marking Information



WMO50N06TS = Device code

WWXX XXX= Date code

## Contact Information

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WAYON website: <http://www.way-on.com>

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[IPS60R360PFD7SAKMA1](#) [IPS60R600PFD7SAKMA1](#)