

- **Main Characteristics**
SMD Fuse; Fast Acting(F);0603
Designed to UL 248-14
- **Materials**
Body: Ceramic
Termination: Silver over-plated with nickel and Tin
- **Environment Standard**
Lead-free, Halogen-free, RoHS
- **Vibration Resistance**
10-60Hz at 0.75mm amplitude
60-2000Hz at 10g acceleration
- **Stock Temperature**
+10°C to +60°C
Relative humidity: ≤75% yearly average
Without dew, maximum 30 days at 95%
- **Operating temperature:**
-55°C to +150°C (with de-rating)

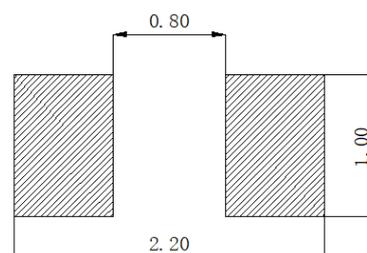
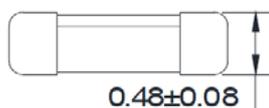
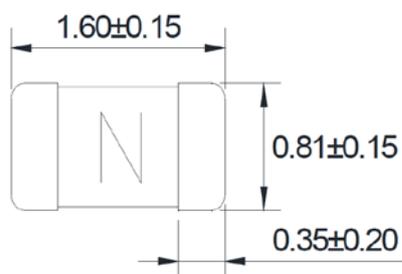
0603WNF series (Rev:B)

SMD chip fuse

■ **Applications**

Circuit Protecting in notebook PC, telecom system, LCD/PDP TV, wireless goods,LCD monitor, white goods, LCD/PDP panel, game console, power supply, net working and other electronics products.

■ **Product Dimensions and Recommended Land Pattern (mm)**



Dimensions

Recommended Land Pattern

■ **Clear-Time Characteristics (Fast Acting)**

% of current rating	Clear-time at 25 °C
	0.25A-8A
100%	4 hours min.
200%	60 seconds max.

■ **Part Numbering**

0603 W NF 150A 032V (A)

(1) (2) (3) (4) (5) (6)

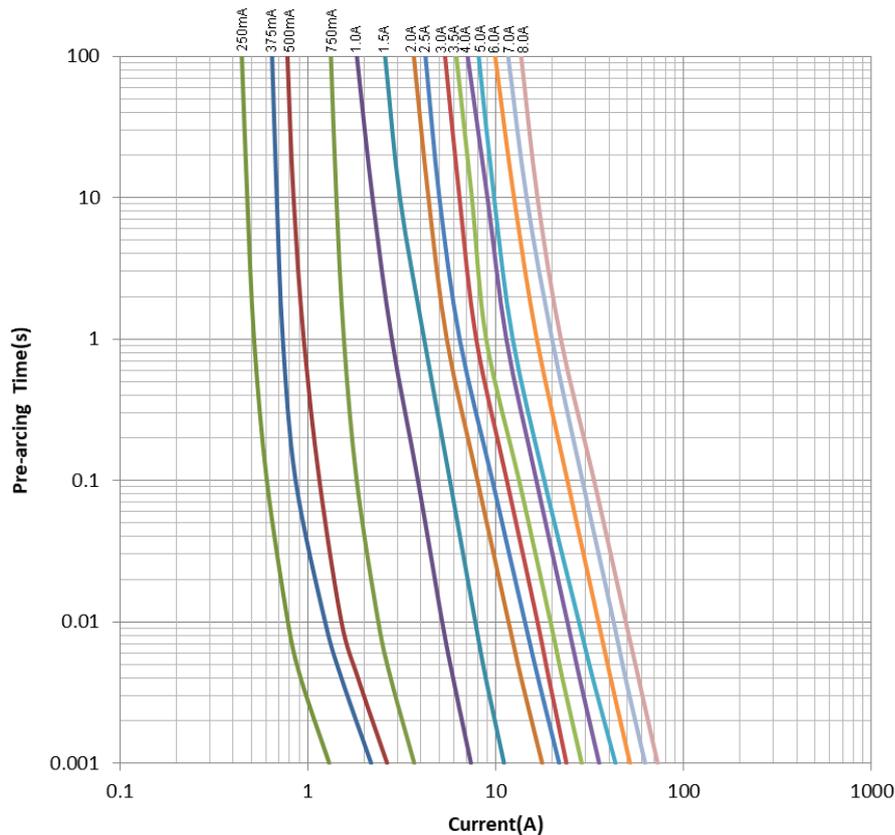
- (1) Size code: Standard EIA Chip Size
- (2) Company code: Wayon Chip Fuse
- (3) Series code: NF series
- (4) Current rating code:150A-1.5A
- (5) Voltage rating code:032V-32VDC
- (6) Supplementary Code: The default is empty,alphabet and number denoting customer code or package code.

Electrical Characteristics

Part number	Current Rating	Voltage Rating	Interrupting Ratings	Nominal Cold DCR	Nominal I ² t	Marking Code
	A	V/DC	A	mΩ	(A ² s)	
0603WNF025A032V	0.25	32	50A@32V	3100	0.00046	D
0603WNF037A032V	0.375			1650	0.00095	E
0603WNF050A032V	0.5			1000	0.0012	F
0603WNF075A032V	0.75			450	0.0095	G
0603WNF100A032V	1			249.5	0.012	B
0603WNF150A032V	1.5			149	0.047	H
0603WNF200A032V	2			74.5	0.118	K
0603WNF250A032V	2.5			46.5	0.145	L
0603WNF300A032V	3			34.5	0.25	O
0603WNF350A032V	3.5			26.5	0.55	R
0603WNF400A032V	4			20.5	0.59	S
0603WNF500A032V	5			13	1.25	T
0603WNF600A032V	6			11	1.75	V
0603WNF700A032V	7			9	2.35	X
0603WNF800A032V	8			7	3.1	Z

- Notice: 1. DC interrupting rating (measured at rated voltage, time constant of less than 50 microseconds, battery source)
 2. DC cold resistance are measured at <10% of rated current in ambient temperature of 25°C
 3. The current values used for calculating I²t should be 10I_n current.
 4. For 1A-5A, the color of glass coating is green; for others, it's blue

Average Clear-Time Curves And I²t vs. t Curves



Specifications are subject to change without notice

Wayon Electronics Co.,Ltd.

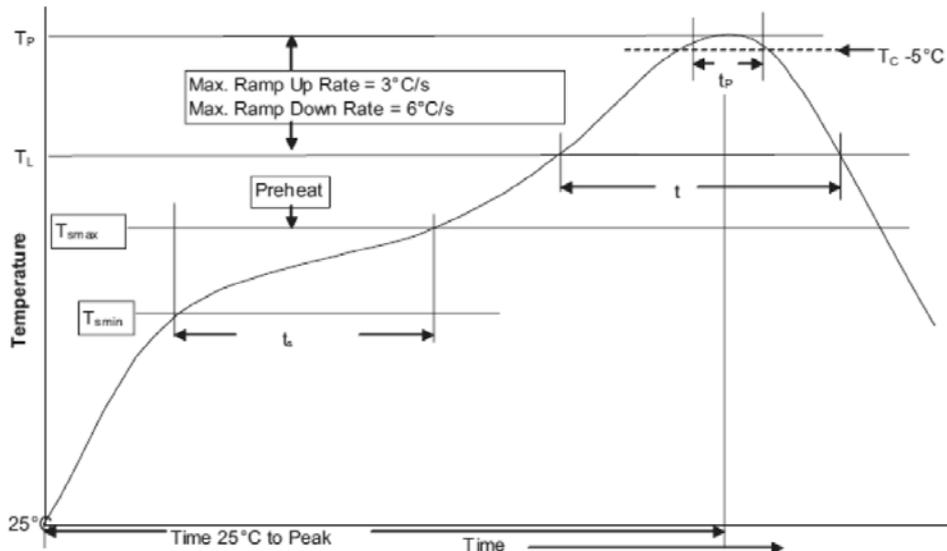
No. 1001, Shivan Qi Road, Shanghai 201202, P. R. China

Tel: 86-21- 50968322

E-mail: hanql@way-on.com

[Http://www.way-on.com](http://www.way-on.com)

Solder Reflow Recommendations



Recommended conditions for hand soldering:

1. Infrared Reflow:

Temperature: 260 °C

Time: 5sec Max.

Recommend reflow profile

2. Wave Soldering

Reservoir Temperature: 260 °C

Time in Reservoir: 10sec Max.

Profile Feature		Pb-Free Assembly
Average Ramp-UP Rate(T_{smax} to T_P)		3°C/s Max.
Preheat	Temperature Min(T_{smin})	150°C
	Temperature Max(T_{smax})	200°C
	Time(T_{smin} to T_{smax})	60sec ~ 120sec
Liquidous temperature(T_L)		217°C
Time at liquidous(t_L)		60 ~ 150 sec.
Peak package body temperature(T_P)		260°C
Time within 5°C of actual Peak Temperature(T_P)		30sec
Average ramp-down rate(T_P to T_{smax})		6°C/s Max.
Time(25°C to Peak Temperature)		8 minutes Max.

Reliability Tests:

No.	Test	Requirement	Test condition	Test reference
1	Resistance to Solder Heat	DCR change $\leq \pm 10\%$ No mechanical damage	One dip at 260°C for 60sec	MIL-STD-202 Method 210
2	Solderability	Minimum 95% coverage	One dip at 235°C for 5sec	MIL-STD-202 Method 208
3	Mechanical Shock	DCR change $\leq \pm 10\%$ No mechanical damage	Figure 1 of Method 213. Condition C 100g 6ms	MIL-STD-202 Method 213
4	Humidity Bias	DCR change $\leq \pm 10\%$ No mechanical damage	1000 hours 85°C/85%RH	MIL-STD-202 Method 103
5	Thermal shock	DCR change $\leq \pm 10\%$ No mechanical damage	300 cycles between -55°C and +125°C	MIL-STD-202 Method 107
6	Moisture resistance	DCR change $\leq \pm 15\%$ No mechanical damage	50 cycles	MIL-STD-202 Method 106
7	High Temperature Exposure	DCR change $\leq \pm 10\%$ No mechanical damage	degree C without power, 1000h	MIL-STD-202 Method 108

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■ Electrical Specifications:

Clear-Time Characteristics:

Same as specified on the Short Form Data Sheet

Insulation Resistance after Opening:

20,000 ohms minimum when cleared with rated voltage applied. Fuse clearing under low voltage conditions may result in lower after clearing insulation resistance values. (Note: Under normal fault conditions (low or rated voltage conditions), WAYON chip fuses provide sufficient after clearing insulation resistance values for circuit protection.)

Current Carrying Capacity:

100% rated current at +25°C ambient for 4 hours minimum when evaluated per MIL-PRF-23419

Interrupt Ratings:

Same as specified on the Short Form Data Sheet

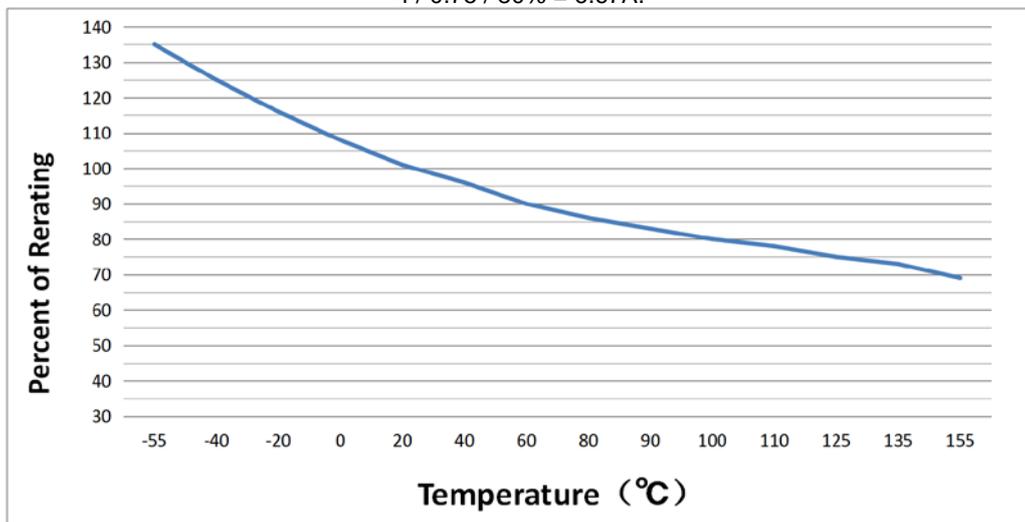
Fuse Selection and Temperature De-rating Guideline:

The ambient temperature affects the current carrying capacity of fuses. When a fuse is operating at a temperature higher than 25°C, the fuse shall be "de-rated".

To select a fuse from the catalog, the following rule may be followed: Catalog Fuse Current Rating = Nominal Operating Current / 0.75 / % De-rating at the maximum operating temperature.

Example: At 100°C, % De-rating is 80%. The nominal operating current is 4A. The current rating for fuse selected from the catalog shall be:

$$4 / 0.75 / 80\% = 6.67A.$$



■ Packaging and Storage:

Storage

The maximum ambient temperature shall not exceed 40°C. Storage temperatures higher than 40°C could result in the deformation of packaging materials. The maximum relative humidity recommended for storage is 70%. High humidity with high temperature can accelerate the oxidation of the solder plating on the termination and reduce the solderability of the components. Sealed plastic bags with desiccant shall be used to reduce the oxidation of the termination and shall only be opened prior to use. The products shall not be stored in areas where harmful gases containing sulfur or chlorine are present.

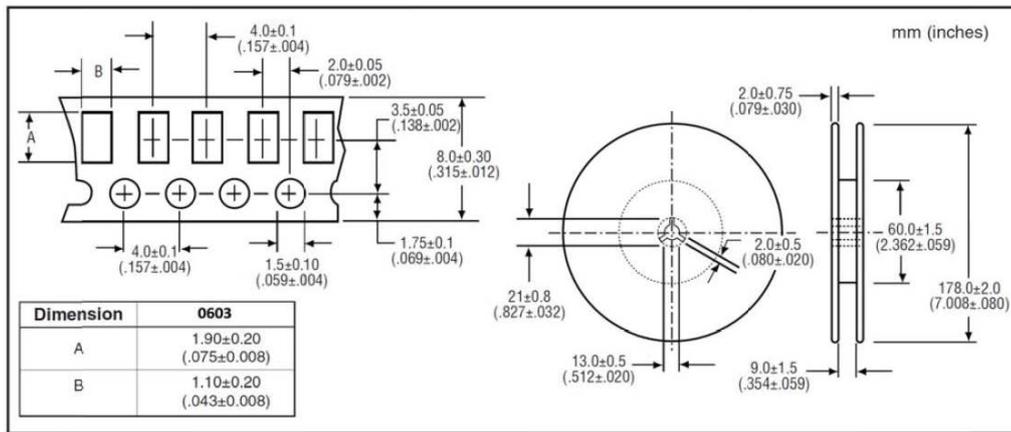
Packaging

WAYON's chip fuse are provided on tape-and-reel for use in pick-and-place machines or in bulk for special applications. Both tape-and-reel and bulk products are sealed in plastic bags with desiccant.

Packaging Data

Quantity per reel 5,000pcs

Specifications are subject to change without notice



■ Cautions and warnings:

1、 Handling

- CHIP FUSE must not be dropped. Chip-offs must not be caused during handling of FUSEs.
- Components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of fuse surface during handling.

2、 Soldering

- Use resin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.

3、 Mounting

- Electrode must not be scratched before/during/after the mounting process.
- Contacts and housings used for assembly with fuses have to be clean before mounting.
- During operation, the fuse's surface temperature can be very high (ICL). Ensure that adjacent components are placed at a sufficient distance from the fuse to allow for proper cooling of the fuses.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of the fuse. Be sure that surrounding parts and materials can withstand this temperature.
- Avoid contamination of fuse surface during processing.

4、 Operation

- Use fuses only within the specified operating temperature range.
- Environmental conditions must not harm the fuses. Use fuses only in normal atmospheric conditions.
- Contact of chip fuses with any liquids and solvents should be prevented. It must be ensured that no water enters the chip fuse (e.g. through plug terminals). For measurement purposes (checking the specified resistance vs. temperature), the component must not be immersed in water but in suitable liquids (e.g. Galden).
- Avoid dewing and condensation.

■ Notice:

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