

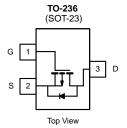
SQ2319ADS-T1_GE3-VB Datasheet P-Channel 40 V (D-S) MOSFET

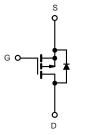
PRODUCT SUMMARY	
V _{DS} (V)	- 40
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	0.071
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.100
I _D (A)	- 3.6
Configuration	Single

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_q and UIS Tested
- Compliant to RoHS Directive 2002/95/EC







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$(T_C = 25 ^{\circ}C, \text{ unles})$	s otherwise noted	l)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	- 40	V
Gate-Source Voltage		V _{GS}	± 20	V
Continuous Drain Current	T _C = 25 °C	- I _D	- 3.6	
Continuous Drain Current	T _C = 125 °C		- 2.6	
Continuous Source Current (Diode Conduction)		I _S	- 3.6	Α
Pulsed Drain Current ^a		I _{DM}	- 18	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	- 12	
Single Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	7.2	mJ
Maximum Dayyar Dissinations	T _C = 25 °C	D	3	W
Maximum Power Dissipation ^a	T _C = 125 °C	- P _D	1	VV
Operating Junction and Storage Temperature F	Range	T _J , T _{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^b	R_{thJA}	166	°C/W
Junction-to-Foot (Drain)		R _{thJF}	50	G/W

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. When mounted on 1" square PCB (FR-4 material).
- c. Parametric verification ongoing.

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PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static	•	•					
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0, I _D = - 250 μA		- 40	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	V _{GS} , I _D = - 250 μA	- 1.0	-	- 2.5	V
Gate-Source Leakage	I _{GSS}	V _{DS} =	0 V, V _{GS} = ± 20 V	-	-	± 100	nA
		V _{GS} = 0 V	V _{DS} = - 40 V	-	-	- 1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = - 40 V, T _J = 125 °C	-	-	- 50	μΑ
		$V_{GS} = 0 V$	V _{DS} = - 40 V, T _J = 175 °C	-	-	- 150	1
On-State Drain Current ^a	I _{D(on)}	V _{GS} = - 10 V	V _{DS} ≤ - 5 V	- 10	-	-	Α
		V _{GS} = - 10 V	I _D = - 3 A	-	0.071	-	V nA μA A Ω S pF nC Ω ns
Drain Sauras On State Besistance	В	V _{GS} = - 10 V	I _D = - 3 A, T _J = 125 °C	-	0.116	-	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V	I _D = - 3 A, T _J = 175 °C	-	0.139	-	
		V _{GS} = - 4.5 V	I _D = - 2.4 A	-	0.100	-	
Forward Transconductanceb	9 _{fs}	V _{DS} = - 5 V, I _D = - 3 A		-	8	-	S
Dynamic ^b							
Input Capacitance	C _{iss}			-	493	620	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = - 25 V, f = 1 MHz	-	76	95	pF
Reverse Transfer Capacitance	C _{rss}			-	51	65	
Total Gate Charge ^c	Qg			-	10.5	16	
Gate-Source Charge ^c	Q_{gs}	V _{GS} = - 10 V	$V_{DS} = -20 \text{ V}, I_{D} = -3 \text{ A}$	-	1.8	-	nC
Gate-Drain Charge ^c	Q_{gd}			-	2.6	-	
Gate Resistance	R_{g}	f = 1 MHz		5	10	15	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	5	8	
Rise Time ^c	t _r	V_{DD} = - 20 V, R_L = 6.7 Ω $I_D \cong$ - 3 A, V_{GEN} = - 10 V, R_g = 1 Ω		-	11	17	ns
Turn-Off Delay Time ^c	t _{d(off)}			-	19	29	
Fall Time ^c	t _f			-	8	12	
Source-Drain Diode Ratings and Chara	acteristics ^b						
Pulsed Current ^a	I _{SM}			-	-	- 18	А
Forward Voltage	V_{SD}	I I -	- 1.5 A, V _{GS} = 0	-	- 0.8	- 1.2	V

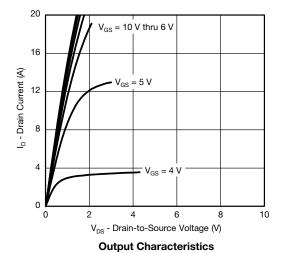
Notes

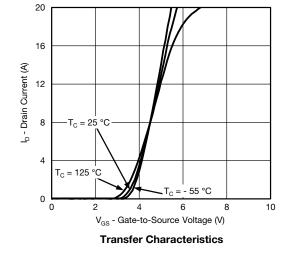
- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

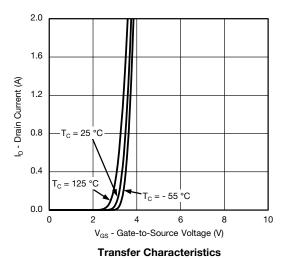
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

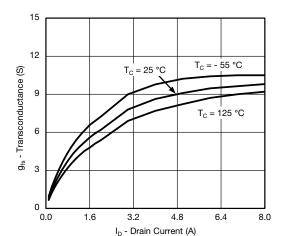


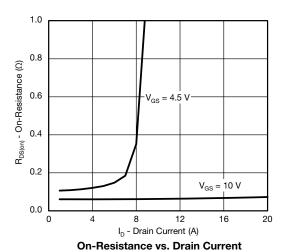
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)





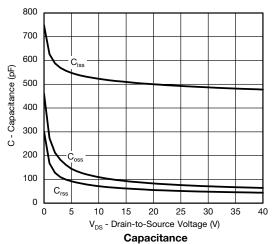








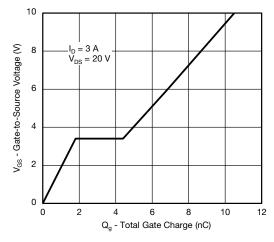
Transconductance



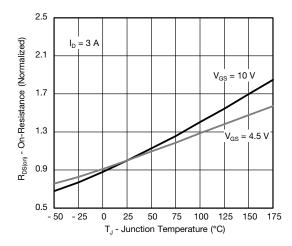
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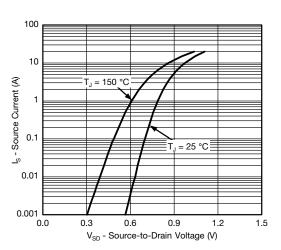
TYPICAL CHARACTERISTICS ($T_A = 25 \, ^{\circ}\text{C}$, unless otherwise noted)



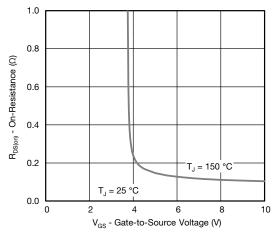
Gate Charge



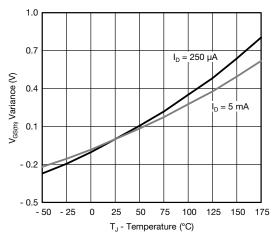
On-Resistance vs. Junction Temperature



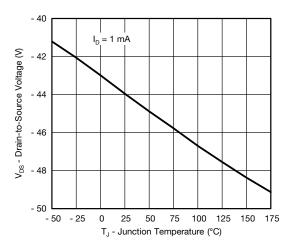
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



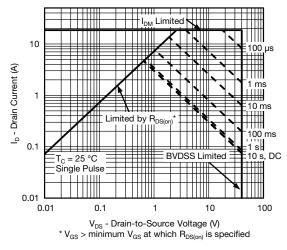
Threshold Voltage



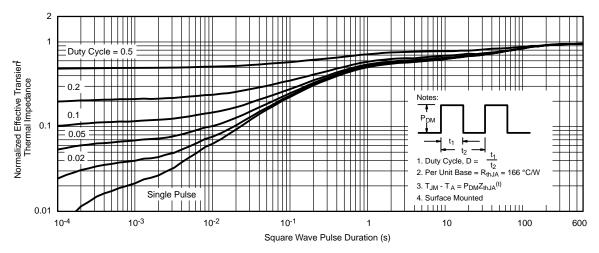
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



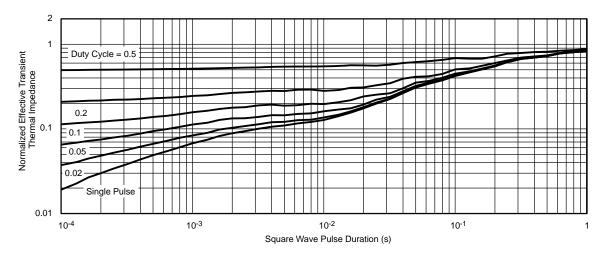
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



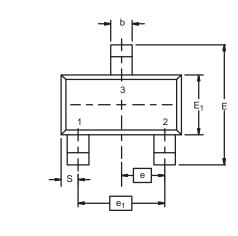
Normalized Thermal Transient Impedance, Junction-to-Ambient

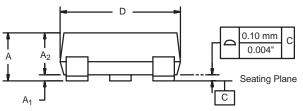
Note

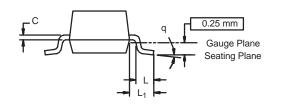
- · The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



SOT-23 (TO-236): 3-LEAD







Dim	MILLIM	IETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95	0.95 BSC		4 Ref	
e ₁	1.90	1.90 BSC		8 Ref	
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025	5 Ref	
S	0.50 Ref		0.020) Ref	
q	3°	8°	3°	8°	

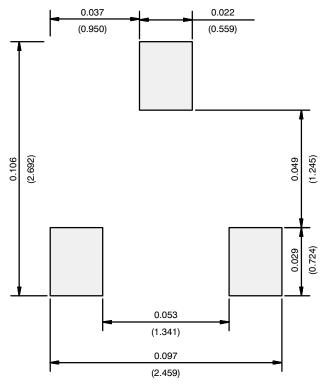
ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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