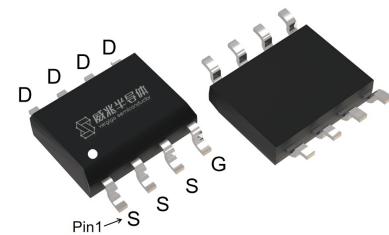


## Features

- Enhancement mode
- VitoMOS® II Technology
- Fast Switching and High efficiency
- 100% Avalanche test

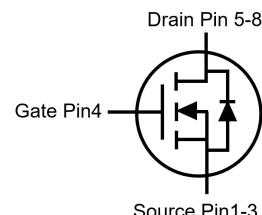
$V_{DS}$	40	V
$R_{DS(on),Typ}@ V_{GS}=10\text{ V}$	3.7	$\text{m}\Omega$
$R_{DS(on),Typ}@ V_{GS}=4.5\text{ V}$	5.4	$\text{m}\Omega$
$I_D$	22	A

SOP8



Halogen-Free

Part ID	Package Type	Marking	Packing
VSO007N04MS-G	SOP8	007N04M	3000PCS/Reel



## Maximum ratings, at $T_A = 25^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit
$V(BR)DSS$	Drain-Source breakdown voltage	40	V
$V_{GS}$	Gate-Source voltage	$\pm 20$	V
$I_S$	Diode continuous forward current	$T_A = 25^\circ\text{C}$	A
$I_D$	Continuous drain current @ $V_{GS}=10\text{V}$	$T_A = 25^\circ\text{C}$	A
		$T_A = 70^\circ\text{C}$	A
$I_{DM}$	Pulse drain current tested ①	$T_A = 25^\circ\text{C}$	A
$EAS$	Avalanche energy, single pulsed ②	45	mJ
$P_D$	Maximum power dissipation	$T_A = 25^\circ\text{C}$	W
$T_{STG,TJ}$	Storage and Junction Temperature Range	-55 to 150	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	Typical	Max	Unit
$R_{\theta JL}$	Thermal Resistance, Junction-to-Lead	24	29	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	40	48	$^\circ\text{C/W}$

### Electrical Characteristics

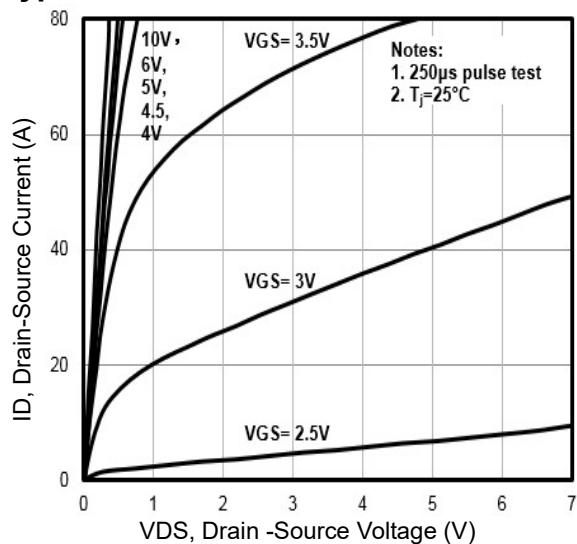
Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	--	--	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	$\mu\text{A}$
	Zero Gate Voltage Drain Current( $T_j = 125^\circ\text{C}$ )	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	$\pm 100$	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.1	1.6	2.2	V
$R_{\text{DS}(\text{on})}$	Drain-Source On-State Resistance ③	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	--	3.7	4.8	mΩ
		$T_j = 100^\circ\text{C}$	--	4.5	--	mΩ
$R_{\text{DS}(\text{on})}$	Drain-Source On-State Resistance ③	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=8\text{A}$	--	5.4	7	mΩ
<b>Dynamic Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	965	1285	1710	pF
$C_{\text{oss}}$	Output Capacitance		325	430	570	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		40	55	75	pF
$R_g$	Gate Resistance	$f=1\text{MHz}$	0.2	1.6	5	Ω
$Q_{\text{g}}(10\text{V})$	Total Gate Charge	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=10\text{A}, V_{\text{GS}}=10\text{V}$	--	27	36	nC
$Q_{\text{g}}(4.5\text{V})$	Total Gate Charge		--	14	19	nC
$Q_{\text{gs}}$	Gate-Source Charge		--	3.9	5.2	nC
$Q_{\text{gd}}$	Gate-Drain Charge		--	6.3	9.5	nC
<b>Switching Characteristics</b>						
$T_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=20\text{V}, I_{\text{D}}=10\text{A}, R_g=3\Omega, V_{\text{GS}}=10\text{V}$	--	7	--	ns
$T_r$	Turn-on Rise Time		--	25	--	ns
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		--	24	--	ns
$T_f$	Turn-Off Fall Time		--	12	--	ns
<b>Source- Drain Diode Characteristics@ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{\text{SD}}$	Forward on voltage	$I_{\text{SD}}=10\text{A}, V_{\text{GS}}=0\text{V}$	--	0.8	1.2	V
$T_{\text{rr}}$	Reverse Recovery Time	$I_{\text{SD}}=10\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=100\text{A}/\mu\text{s}$	--	24	48	ns
$Q_{\text{rr}}$	Reverse Recovery Charge		--	9.4	19	nC

NOTE: ① Repetitive rating; pulse width limited by max junction temperature.

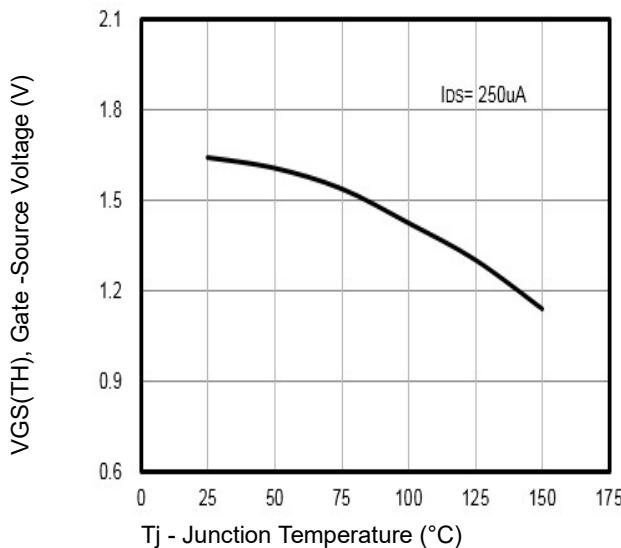
② Limited by  $T_{j\text{max}}$ , starting  $T_j = 25^\circ\text{C}$ ,  $L = 0.1\text{mH}$ ,  $R_g = 25\Omega$ ,  $I_{AS} = 30\text{A}$ ,  $V_{GS} = 10\text{V}$ . Part not recommended for use above this value

③ Pulse width  $\leq 380\mu\text{s}$ ; duty cycle  $\leq 2\%$ .

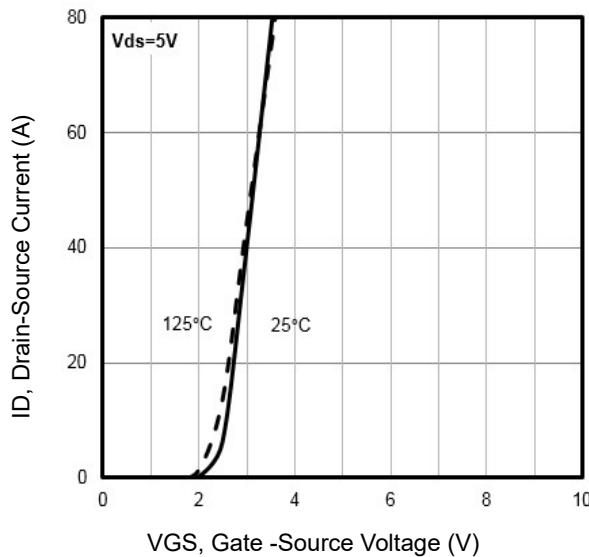
## Typical Characteristics



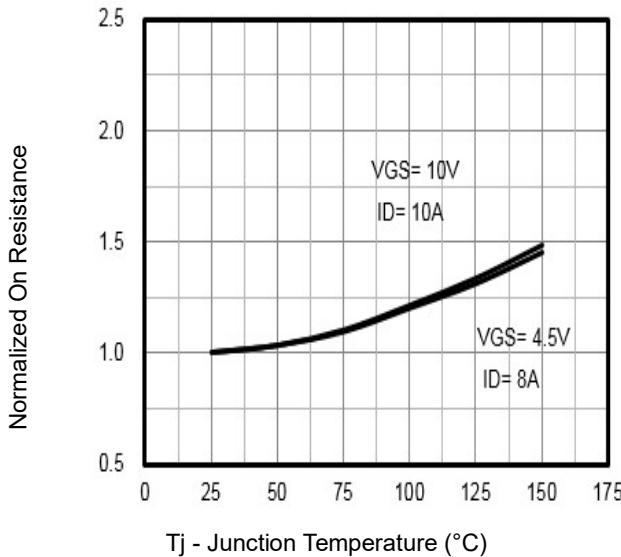
**Fig1.** Typical Output Characteristics



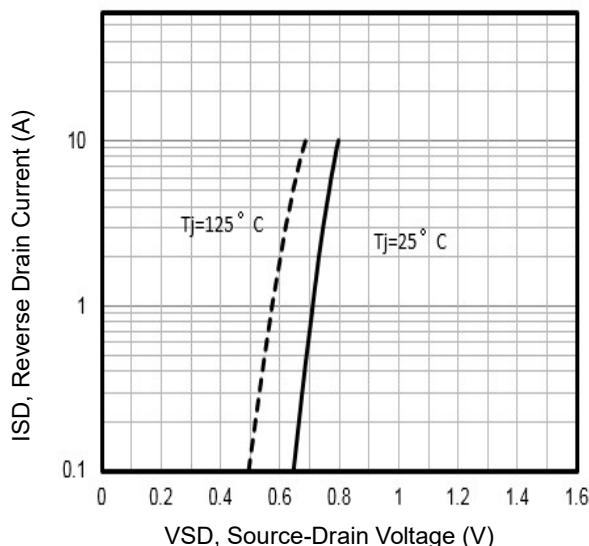
**Fig2.**  $V_{GS(TH)}$  Gate-Source Voltage Vs.  $T_j$



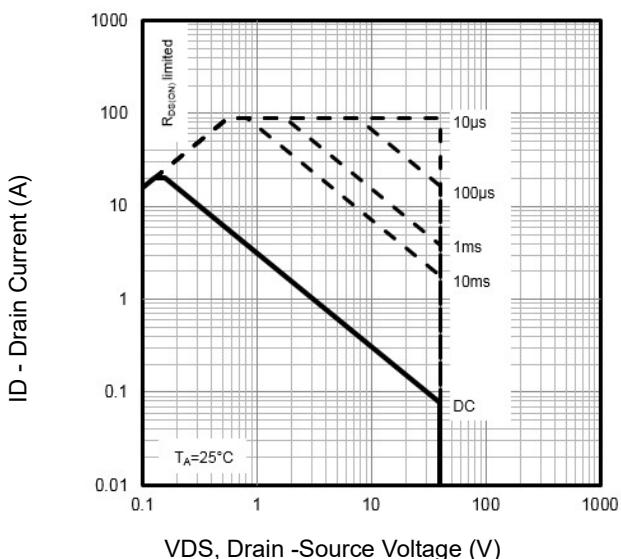
**Fig3.** Typical Transfer Characteristics



**Fig4.** Normalized On-Resistance Vs.  $T_j$



**Fig5.** Typical Source-Drain Diode Forward Voltage



**Fig6.** Maximum Safe Operating Area

## Typical Characteristics

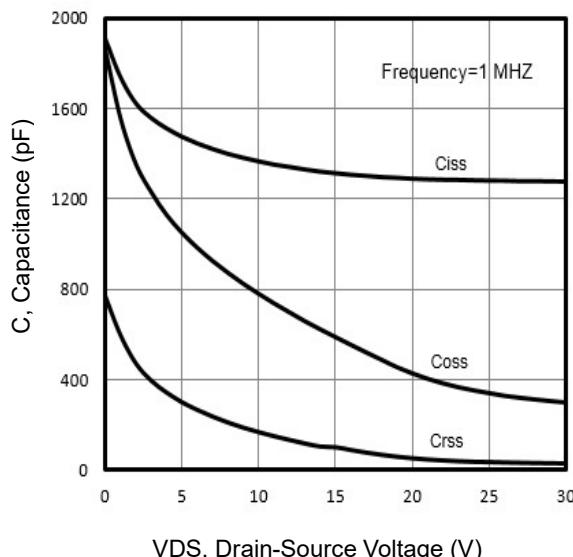


Fig7. Typical Capacitance Vs. Drain-Source Voltage

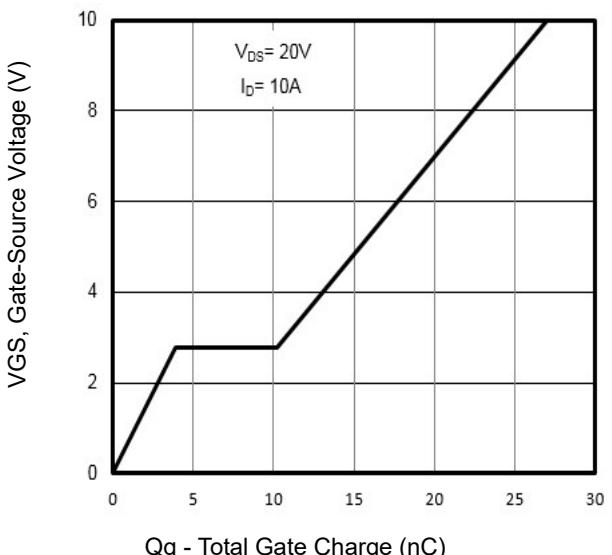


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

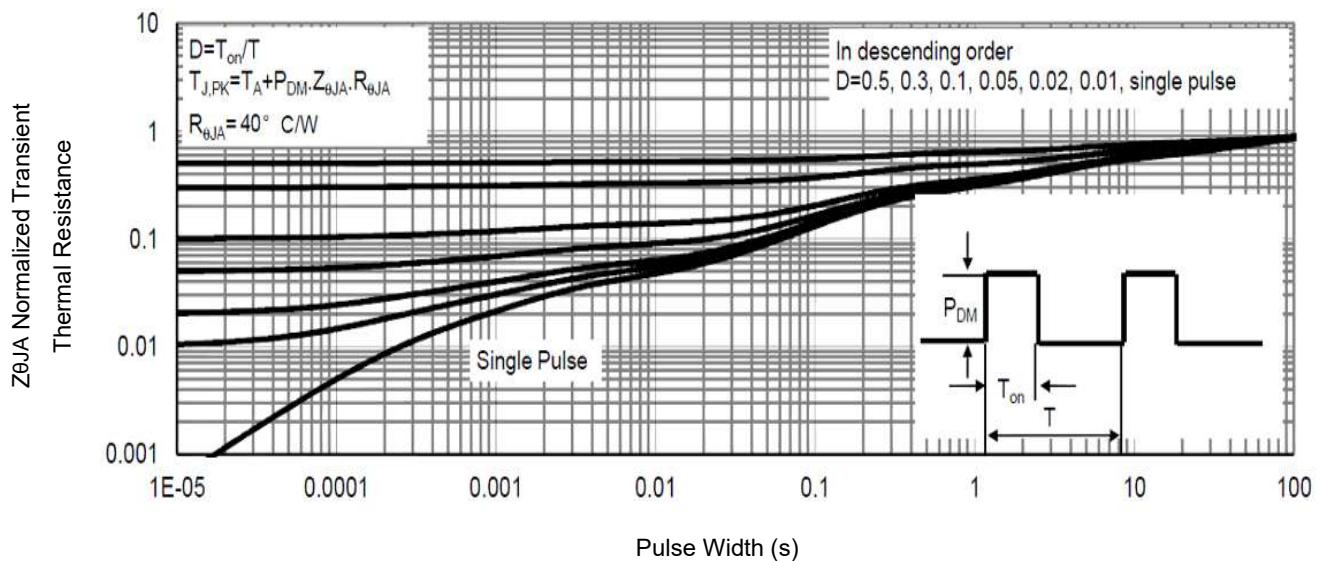


Fig9. Normalized Maximum Transient Thermal Impedance

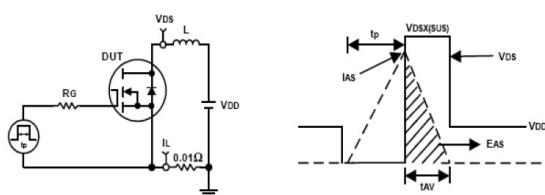


Fig10. Unclamped Inductive Test Circuit and waveforms

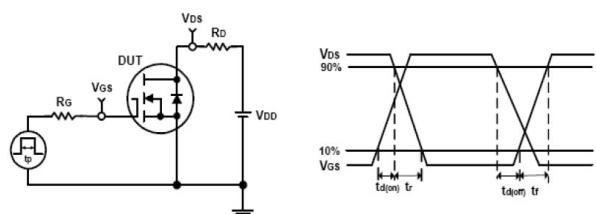
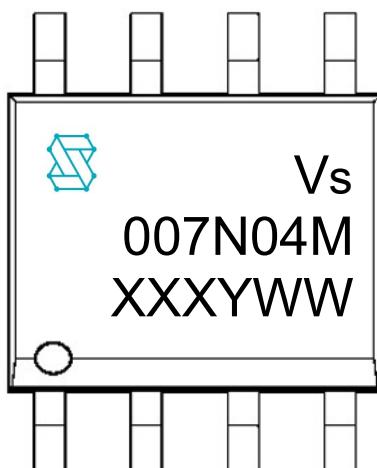


Fig11. Switching Time Test Circuit and waveforms

## Marking Information



1<sup>st</sup> line: Vergiga Code (Vs), Vergiga Logo

2<sup>nd</sup> line: Part Number (007N04M)

3<sup>rd</sup> line: Date code (XXXYWW)

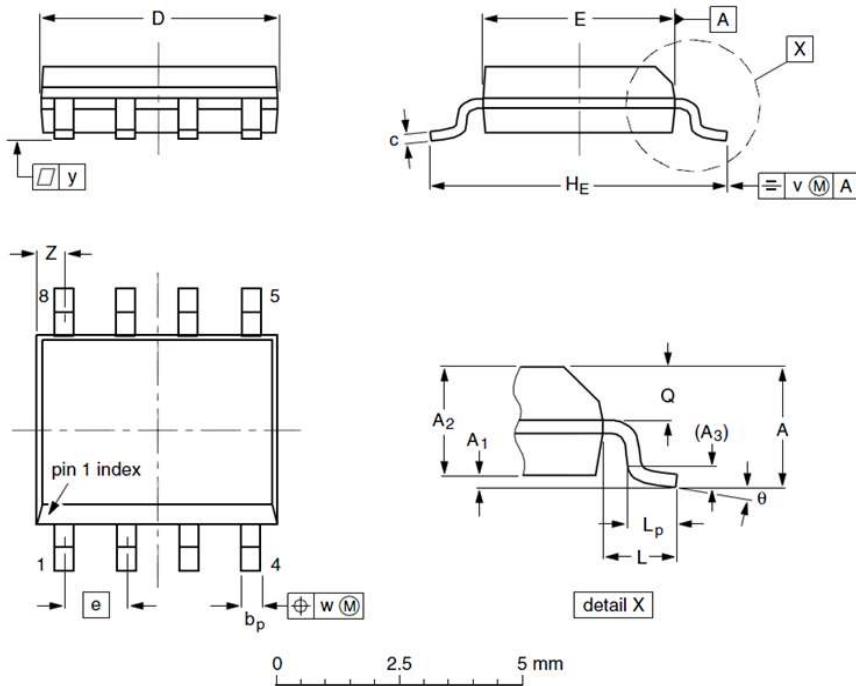
XXX: Wafer Lot Number Code , code changed with Lot Number

Y: Year Code , refer to table below

WW: Week Code (01 to 53)

Code	C	D	E	F	G	H	J	K	L	M	N	P	Q	R	S	T
Year	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030

## SOP8 Package Outline Data



Label	Dimensions (unit: mm)		
	Min	Typ	Max
A	--	--	1.75
A <sub>1</sub>	0.10	0.18	0.25
A <sub>2</sub>	1.25	1.35	1.50
A <sub>3</sub>	--	0.25	--
b <sub>p</sub>	0.36	0.42	0.51
c	0.19	0.22	0.25
D	4.80	4.92	5.00
E	3.80	3.90	4.00
e	--	1.27	--
H <sub>E</sub>	5.80	6.00	6.20
L	--	1.05	--
L <sub>p</sub>	0.40	0.68	1.00
Q	0.60	0.65	0.725
v	--	0.25	--
w	--	0.25	--
y	--	0.10	--
z	0.30	0.50	0.70
θ	0°		8°

### Notes:

- Follow JEDEC MS-012.
- Dimension "D" does NOT include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15mm per side.
- Dimension "E" does NOT include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.25mm per side.
- Dimension "bp" does NOT include dambar protrusion. Allowable dambar protrusion shall be 0.1mm total in excess of "bp" dimension at maximum material condition. The dambar cannot be located on the lower radius of the foot.

## Customer Service

### Sales and Service:

[sales@vgsemi.com](mailto:sales@vgsemi.com)

**Vergiga Semiconductor CO., LTD**

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[TK31J60W5,S1VQ\(O\)](#) [2SK2614\(TE16L1,Q\)](#) [DMN1017UCP3-7](#) [EFC2J004NUZTDG](#) [FCAB21350L1](#) [P85W28HP2F-7071](#) [DMN1053UCP4-7](#)  
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[BSS340NWH6327XTSA1](#) [MCM3400A-TP](#) [DMTH10H4M6SPS-13](#) [IRF40SC240ARMA1](#) [IPS60R1K0PFD7SAKMA1](#)  
[IPS60R360PFD7SAKMA1](#) [IPS60R600PFD7SAKMA1](#)