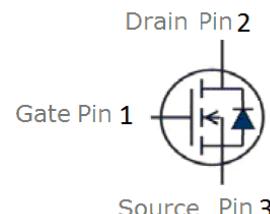
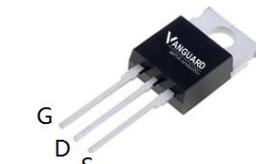


## Features

- N-Channel, 10V Logic Level Control
- Enhancement mode
- Very low on-resistance  $R_{DS(on)}$  @  $V_{GS}=10V$
- 100% Avalanche test
- Pb-free lead plating; RoHS compliant

$V_{DS}$	80	V
$R_{DS(on),TYP} @ V_{GS}=10V$	4.5	$m\Omega$
$I_D$	155	A

**TO-220AB**


Part ID	Package Type	Marking	Tape and reel information
VS150N08BT	TO-220AB	150N08BT	50PCS/Tube

## Maximum ratings, at $T_j=25^{\circ}\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	80	V
$I_s$	Diode continuous forward current	$T_c=25^{\circ}\text{C}$	A
$I_D$	Continuous drain current @ $V_{GS}=10V$	$T_c=25^{\circ}\text{C}$	A
		$T_c=100^{\circ}\text{C}$	A
$I_{DM}$	Pulse drain current tested ①	$T_c=25^{\circ}\text{C}$	A
EAS	Avalanche energy, single pulsed ②	823	mJ
$P_D$	Maximum power dissipation	$T_c=25^{\circ}\text{C}$	W
$V_{GS}$	Gate-Source voltage	$\pm 25$	V
$T_{STG} T_J$	Storage and operating temperature range	-55 to 175	$^{\circ}\text{C}$

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.58	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62.5	$^{\circ}\text{C}/\text{W}$

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	80	--	--	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=80\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	$\mu\text{A}$
	Zero Gate Voltage Drain Current( $T_j=125^\circ\text{C}$ )	$V_{\text{DS}}=80\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 25\text{V}, V_{\text{DS}}=0\text{V}$	--	--	$\pm 100$	nA
$V_{\text{GS(TH)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.0	3.0	4.0	V
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance <sup>③</sup>	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=80\text{A}$	--	4.5	6	$\text{m}\Omega$
<b>Dynamic Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	4115	--	pF
$C_{\text{oss}}$	Output Capacitance		--	570	--	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		--	395	--	pF
$R_g$	Gate Resistance	$f=1\text{MHz}$	--	2.6	--	$\Omega$
$Q_g$	Total Gate Charge	$V_{\text{DS}}=40\text{V}, I_{\text{D}}=80\text{A}, V_{\text{GS}}=10\text{V}$	--	69	--	nC
$Q_{\text{gs}}$	Gate-Source Charge		--	23	--	nC
$Q_{\text{gd}}$	Gate-Drain Charge		--	22	--	nC
<b>Switching Characteristics</b>						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=40\text{V}, I_{\text{D}}=80\text{A}, R_{\text{G}}=3\Omega, V_{\text{GS}}=10\text{V}$	--	28	--	nS
$t_r$	Turn-on Rise Time		--	20	--	nS
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	50	--	nS
$t_f$	Turn-Off Fall Time		--	23	--	nS
<b>Source- Drain Diode Characteristics@ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{\text{SD}}$	Forward on voltage	$I_{\text{SD}}=80\text{A}, V_{\text{GS}}=0\text{V}$	--	0.9	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{SD}}=80\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=500\text{A}/\mu\text{s}$	--	28	--	nS
$Q_{\text{rr}}$	Reverse Recovery Charge			110		nC

**NOTE:**

① Repetitive rating; pulse width limited by max. junction temperature.

② Limited by  $T_{j\text{max}}$ , starting  $T_j = 25^\circ\text{C}$ ,  $L = 0.5\text{mH}$ ,  $R_G = 25\Omega$ ,  $I_{AS} = 37\text{A}$ ,  $V_{GS} = 10\text{V}$ . Part not recommended for use above this value

③ Pulse width  $\leq 300\mu\text{s}$ ; duty cycle  $\leq 2\%$ .



Vanguard  
Semiconductor

VS150N08BT  
80V/155A N-Channel Advanced Power MOSFET

## Typical Characteristics

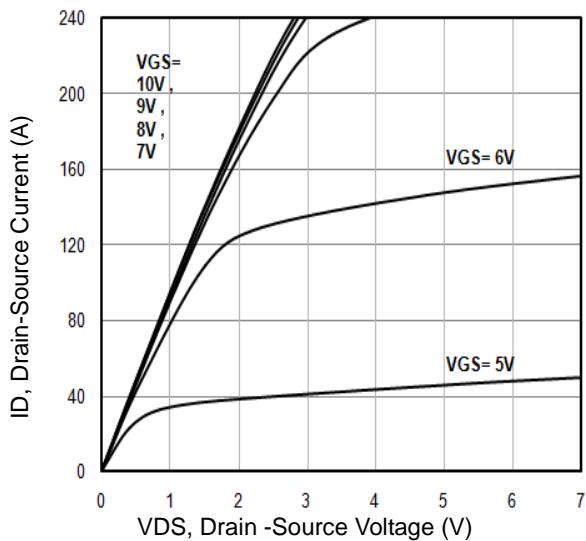


Fig1. Typical Output Characteristics

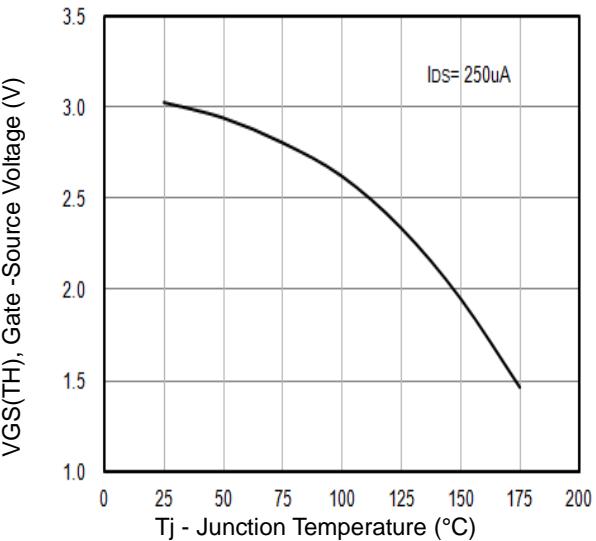


Fig2.  $V_{GS(TH)}$  Gate -Source Voltage Vs.  $T_j$

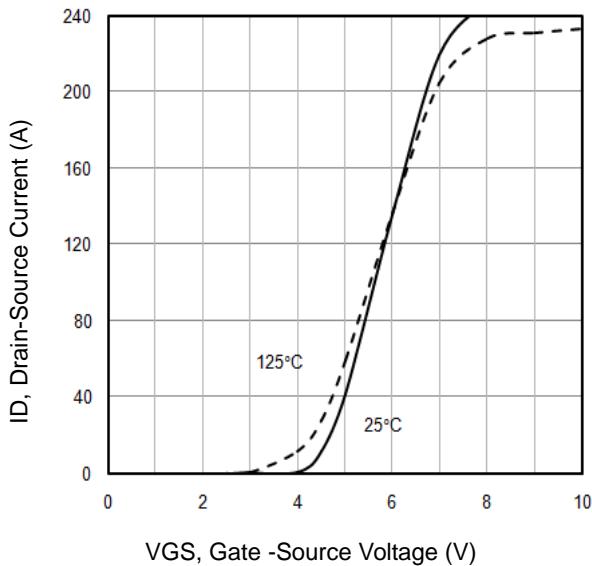


Fig3. Typical Transfer Characteristics

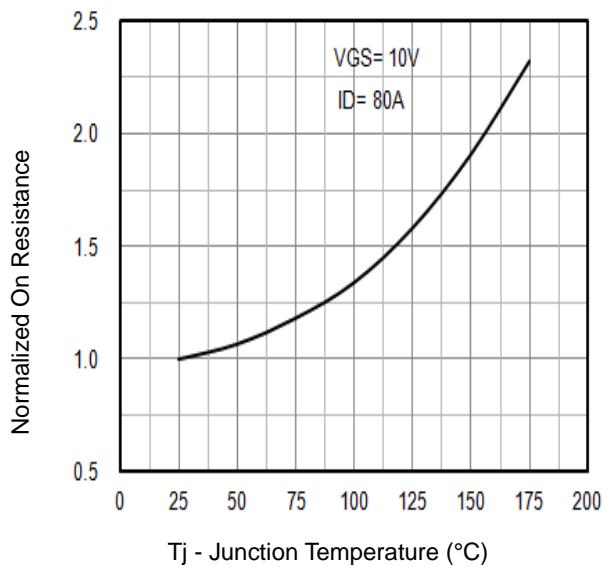


Fig4. Normalized On-Resistance Vs. Temperature

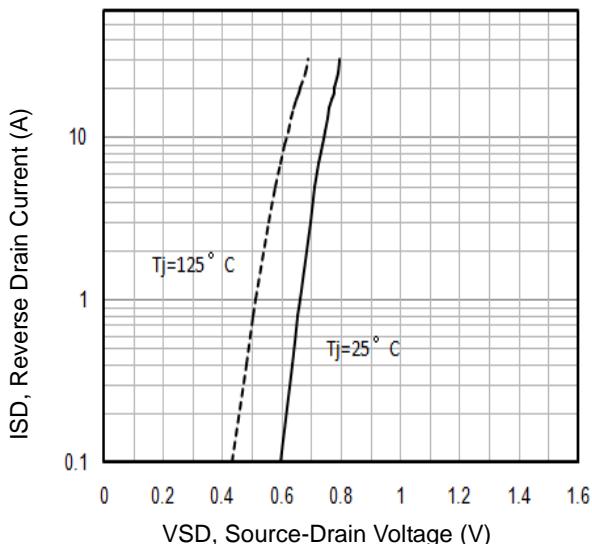


Fig5. Typical Source-Drain Diode Forward Voltage

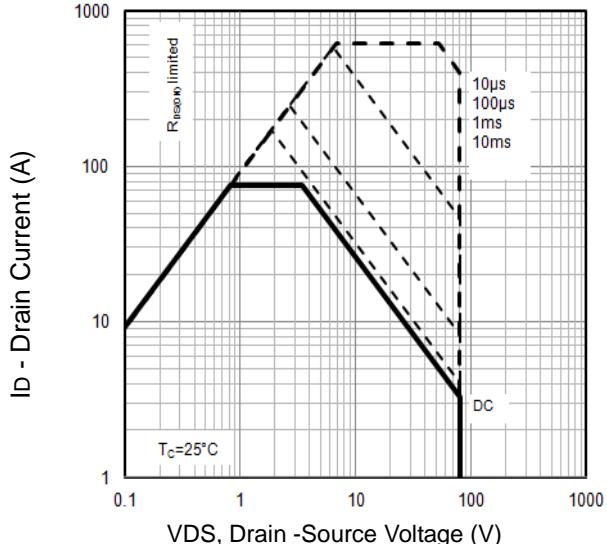
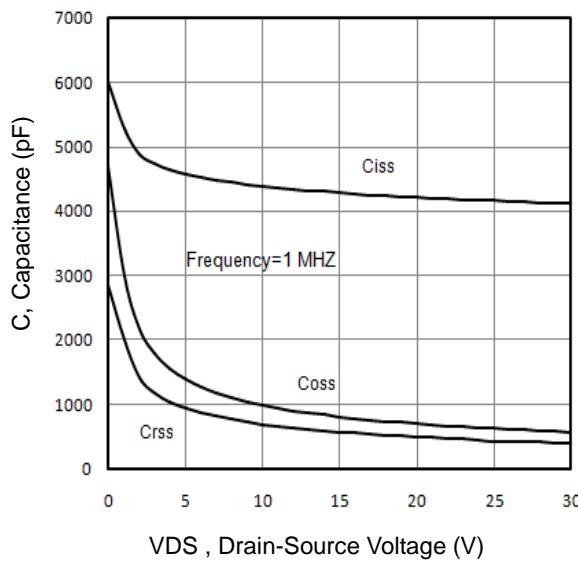
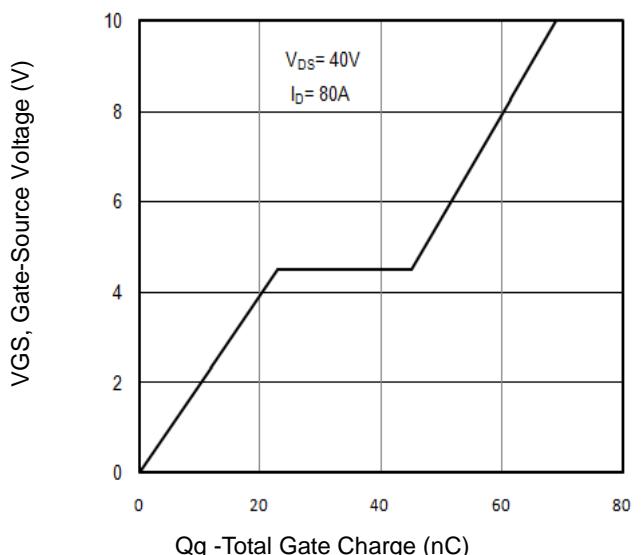


Fig6. Maximum Safe Operating Area

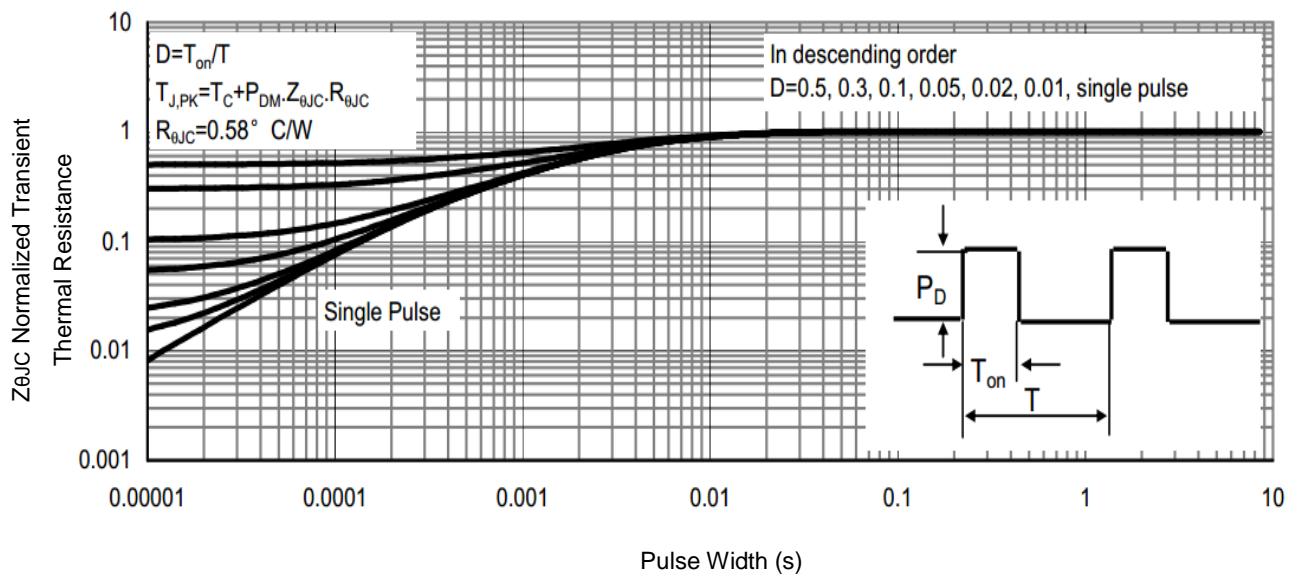
## Typical Characteristics



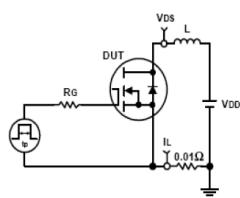
**Fig7.** Typical Capacitance Vs.Drain-Source Voltage



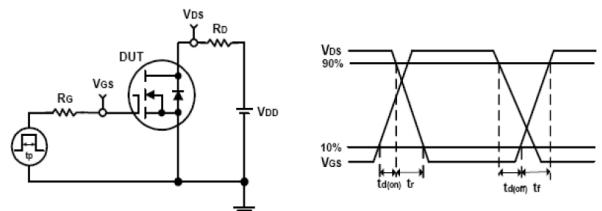
**Fig8.** Typical Gate Charge Vs.Gate-Source Voltage



**Fig9.** Normalized Maximum Transient Thermal Impedance

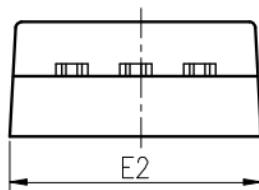
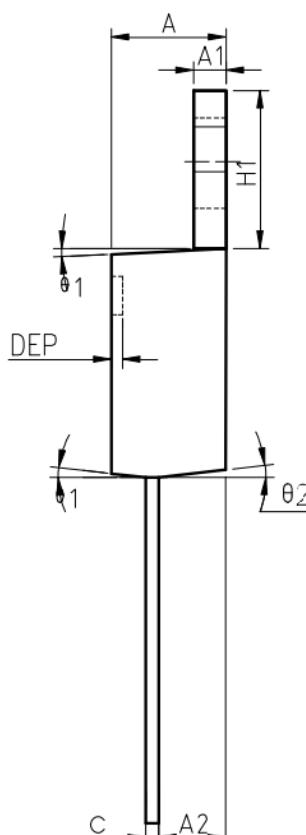
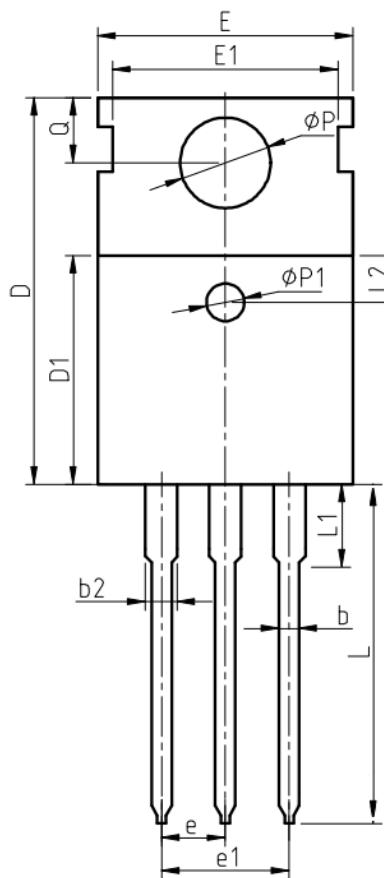


**Fig10.** Unclamped Inductive Test Circuit and waveforms



**Fig11.** Switching Time Test Circuit and waveforms

## TO-220AB Package Outline Data



Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
<b>A</b>	4.30	4.52	4.70
<b>A1</b>	1.15	1.30	1.40
<b>A2</b>	2.20	2.40	2.60
<b>b</b>	0.70	0.80	1.00
<b>b2</b>	1.17	1.32	1.50
<b>c</b>	0.45	0.50	0.61
<b>D</b>	15.30	15.65	15.90
<b>D1</b>	9.00	9.20	9.40
<b>DEP</b>	0.05	0.10	0.25
<b>E</b>	9.66	9.90	10.28
<b>E1</b>	-	8.70	-
<b>E2</b>	9.80	10.00	10.20
<b>φP1</b>	1.40	1.50	1.60
<b>e</b>	2.54 BSC		
<b>e1</b>	5.08 BSC		
<b>H1</b>	6.40	6.50	6.80
<b>L</b>	12.70	-	14.27
<b>L1</b>	-	-	3.95
<b>L2</b>	2.40	2.50	2.60
<b>φP</b>	3.53	3.60	3.70
<b>Q</b>	2.70	2.80	2.90
<b>θ1</b>	5 °	7 °	9 °
<b>θ2</b>	1 °	3 °	5 °

### Notes:

1. Refer to JEDEC TO-220 variation AB
2. Dimension "D" and "E" do NOT include mold flash. Mold flash shall not exceed 0.127mm per side.

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