

# 50N06

**Power MOSFET**

## 50 Amps, 60 Volts N-CHANNEL POWER MOSFET

### ■ DESCRIPTION

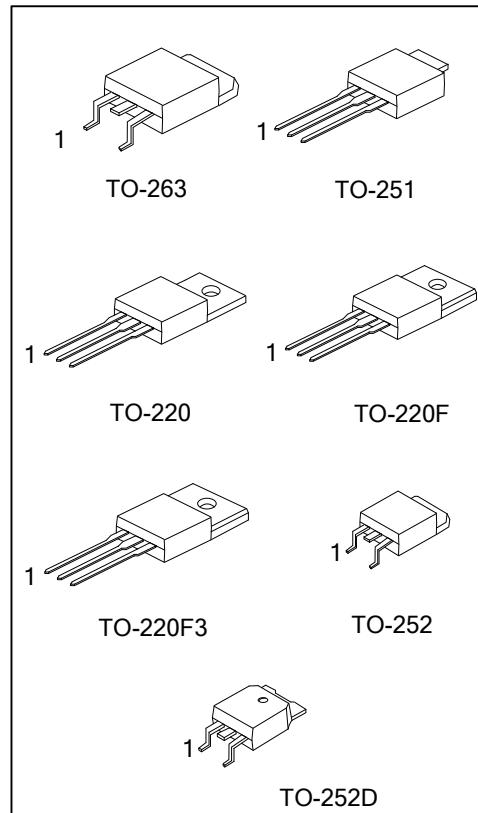
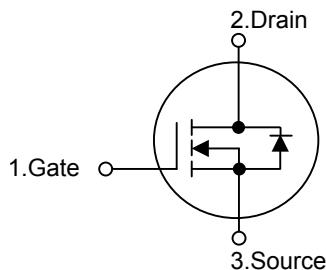
The UTC **50N06** is three-terminal silicon device with current conduction capability of about 50A, fast switching speed. Low on-state resistance, breakdown voltage rating of 60V, and max threshold voltages of 4 volt.

It is mainly suitable electronic ballast, and low power switching mode power appliances.

### ■ FEATURES

- \*  $R_{DS(ON)} < 23m\Omega @ V_{GS} = 10 \text{ V}$
- \* Fast switching capability
- \* 100% avalanche energy specified
- \* Improved dv/dt capability

### ■ SYMBOL



### ■ ORDERING INFORMATION

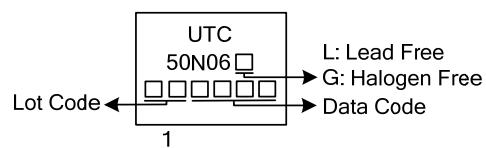
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
50N06L-TA3-T	50N06G-TA3-T	TO-220	G	D	S	Tube
50N06L-TF3-T	50N06G-TF3-T	TO-220F	G	D	S	Tube
50N06L-TF3T-T	50N06G-TF3T-T	TO-220F3	G	D	S	Tube
50N06L-TM3-T	50N06G-TM3-T	TO-251	G	D	S	Tube
50N06L-TN3-R	50N06G-TN3-R	TO-252	G	D	S	Tape Reel
50N06L-TND-R	50N06G-TND-R	TO-252D	G	D	S	Tape Reel
50N06L-TQ2-T	50N06G-TQ2-T	TO-263	G	D	S	Tube
50N06L-TQ2-R	50N06G-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

50N06L-TA3-T	(1)Packing Type (2)Package Type (3)Lead Plating	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF3: TO-220F, TF3T: TO-220F3, TM3: TO-251, TN3: TO-252, TND: TO-252D TQ2: TO-263 (3) L: Lead Free, G: Halogen Free
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**■ MARKING INFORMATION**

PACKAGE	MARKING
TO-220	
TO-220F	
TO-220F3	
TO-251	
TO-252	
TO-252D	
TO-263	



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	60	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Continuous Drain Current	T <sub>C</sub> = 25°C	I <sub>D</sub>	50	A
	T <sub>C</sub> = 100°C		35	A
Pulsed Drain Current (Note 2)		I <sub>DM</sub>	200	A
Avalanche Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	480	mJ
	Repetitive (Note 2)	E <sub>AR</sub>	13	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	7	V/ns
Power Dissipation (T <sub>C</sub> =25°C)	TO-220/TO-263	P <sub>D</sub>	120	W
	TO-220F/TO-220F3		70	W
	TO-251/TO-252		46	W
	TO-252D			
Junction Temperature		T <sub>J</sub>	+150	°C
Operation and Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by T<sub>J</sub>

3. L=0.38mH, I<sub>AS</sub>=50A, V<sub>DD</sub>= 25V, R<sub>G</sub>=20Ω, Starting T<sub>J</sub>=25°C

4. I<sub>SD</sub> ≤ 50A, di/dt ≤ 300A/μs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub>=25°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F	θ <sub>JA</sub>	62	°C/W
	TO-220F3/TO-263		100	°C/W
Junction to Case	TO-251/TO-252	θ <sub>JC</sub>	1.24	°C/W
	TO-252D		1.78	°C/W
	TO-220		2.7	°C/W
	TO-220F/TO-220F3		1.24	°C/W

■ ELECTRICAL CHARACTERISTICS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}} = 0 \text{ V}, I_{\text{D}} = 250 \mu\text{A}$	60			V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 60 \text{ V}, V_{\text{GS}} = 0 \text{ V}$		10		$\mu\text{A}$
Gate-Source Leakage Current	Forward	$V_{\text{GS}} = 20\text{V}, V_{\text{DS}} = 0 \text{ V}$		100		nA
	Reverse	$V_{\text{GS}} = -20\text{V}, V_{\text{DS}} = 0 \text{ V}$		-100		nA
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$I_{\text{D}} = 250 \mu\text{A}$ , Referenced to $25^\circ\text{C}$		0.07		$\text{V}/^\circ\text{C}$
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250 \mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 25 \text{ A}$		18	23	$\text{m}\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{\text{ISS}}$	$V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 25 \text{ V}$ $f = 1\text{MHz}$		900	1220	pF
Output Capacitance	$C_{\text{OSS}}$			430	550	pF
Reverse Transfer Capacitance	$C_{\text{RSS}}$			80	100	pF
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	$t_{\text{D(ON)}}$	$V_{\text{DD}} = 30\text{V}, I_{\text{D}} = 0.5 \text{ A}, R_{\text{G}} = 25\Omega$ (Note 1, 2)		60	80	ns
Turn-On Rise Time	$t_{\text{R}}$			180	220	ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			300	350	ns
Turn-Off Fall Time	$t_{\text{F}}$			200	250	ns
Total Gate Charge	$Q_{\text{G}}$	$V_{\text{DS}} = 50\text{V}, V_{\text{GS}} = 10 \text{ V}$ $I_{\text{D}} = 1.3\text{A}$ (Note 1, 2)		60	80	nC
Gate-Source Charge	$Q_{\text{GS}}$			9		nC
Gate-Drain Charge	$Q_{\text{GD}}$			20		nC
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
Drain-Source Diode Forward Voltage	$V_{\text{SD}}$	$I_{\text{S}} = 50\text{A}, V_{\text{GS}} = 0 \text{ V}$			1.5	V
Maximum Continuous Drain-Source Diode Forward Current	$I_{\text{S}}$				50	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{\text{SM}}$				200	A
Reverse Recovery Time	$t_{\text{RR}}$	$I_{\text{S}} = 50\text{A}, V_{\text{GS}} = 0 \text{ V}$		54		ns
Reverse Recovery Charge	$Q_{\text{RR}}$	$dI_{\text{F}}/dt = 100 \text{ A}/\mu\text{s}$		81		$\mu\text{C}$

Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ 

2. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

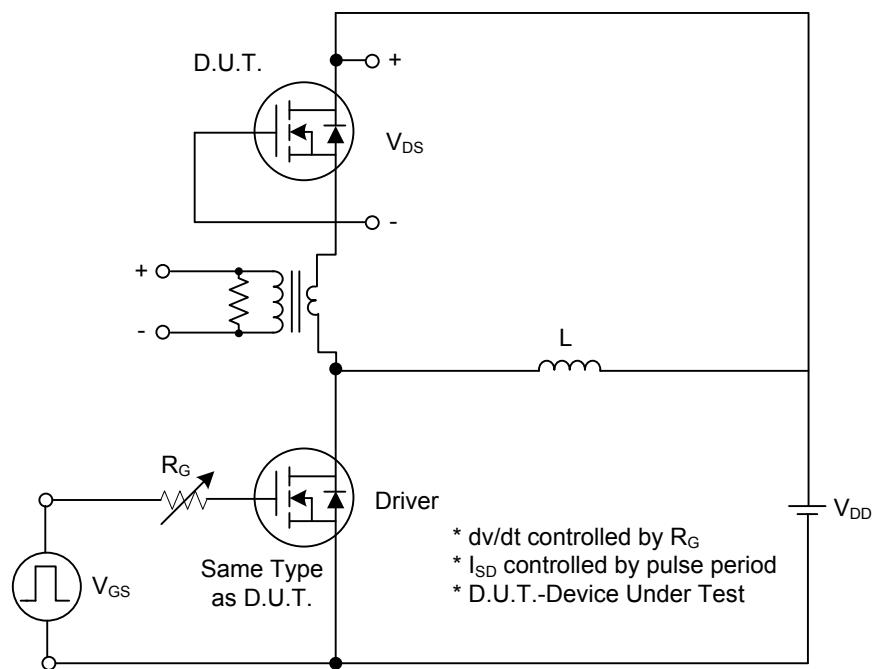


Fig. 1A Peak Diode Recovery dv/dt Test Circuit

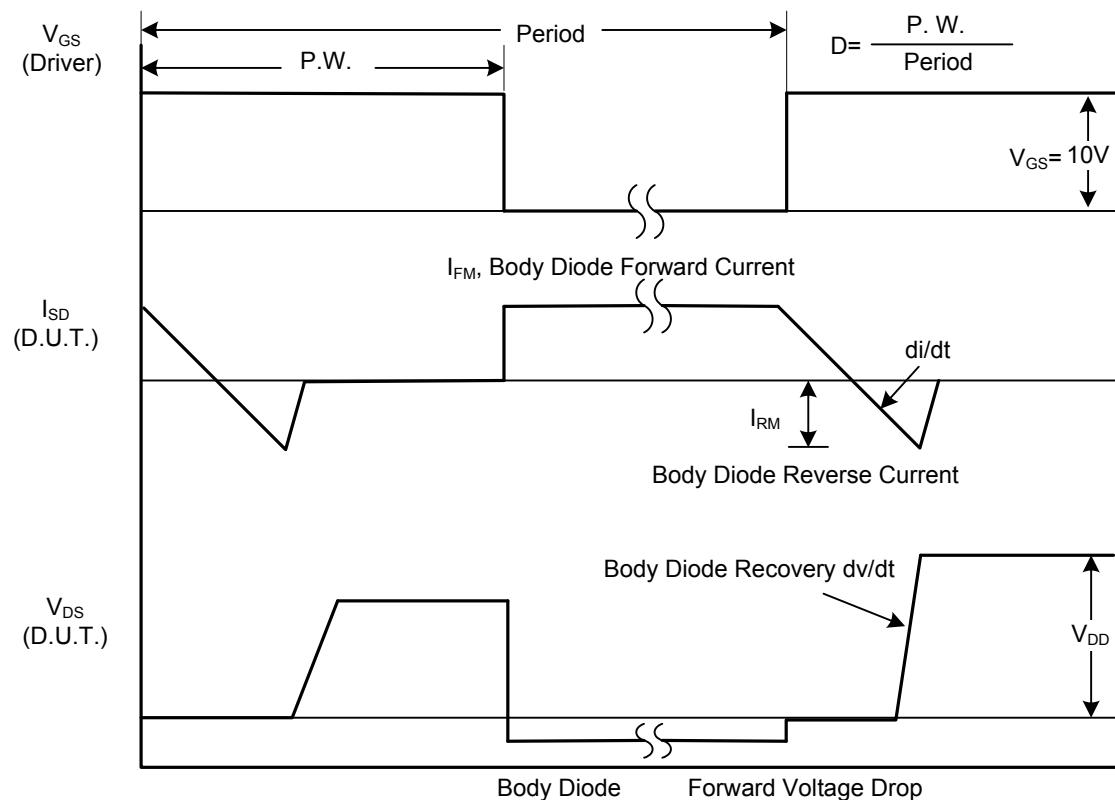


Fig. 1B Peak Diode Recovery dv/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS (Cont.)

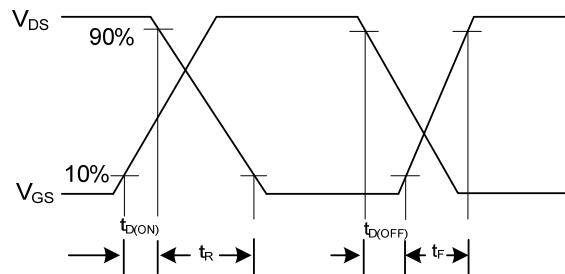
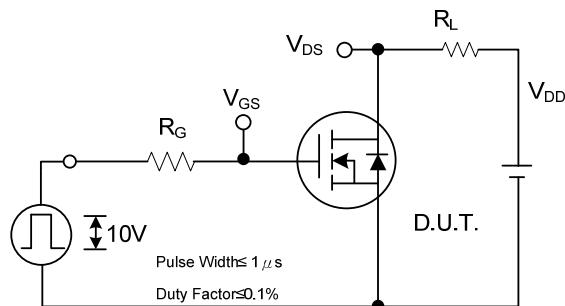


Fig. 2A Switching Test Circuit

Fig. 2B Switching Waveforms

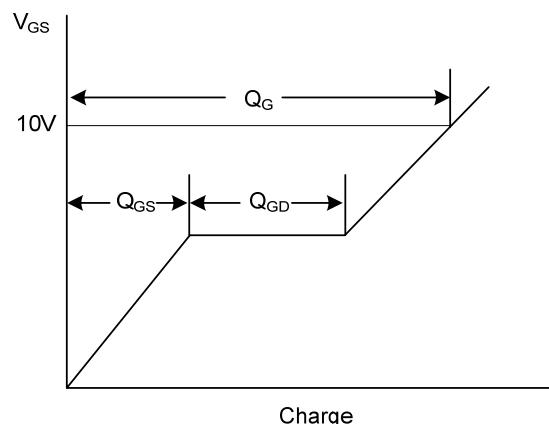
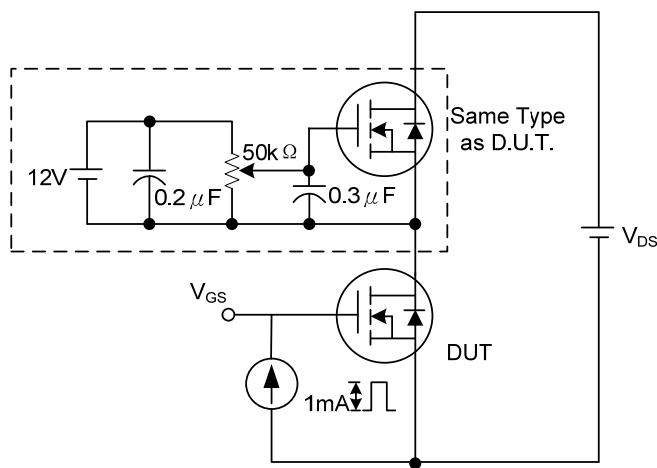


Fig. 3A Gate Charge Test Circuit

Fig. 3B Gate Charge Waveform

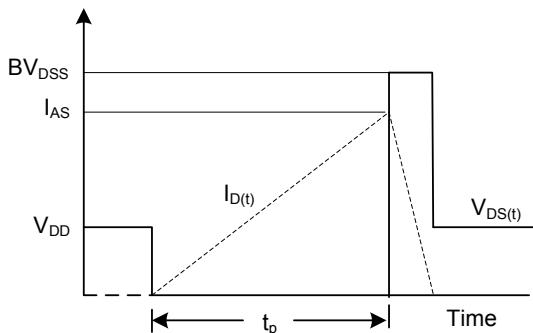
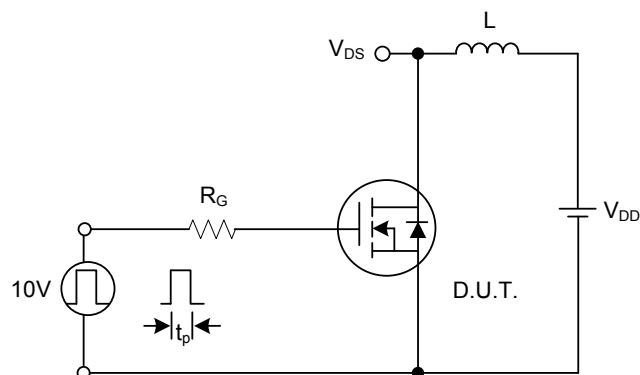
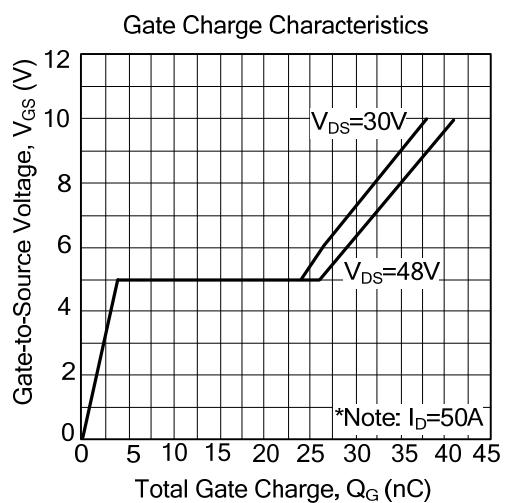
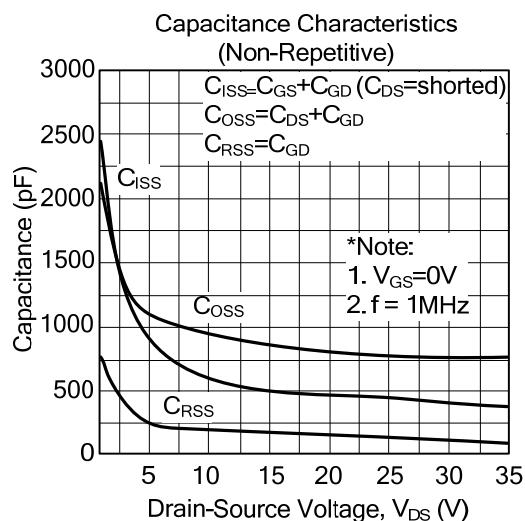
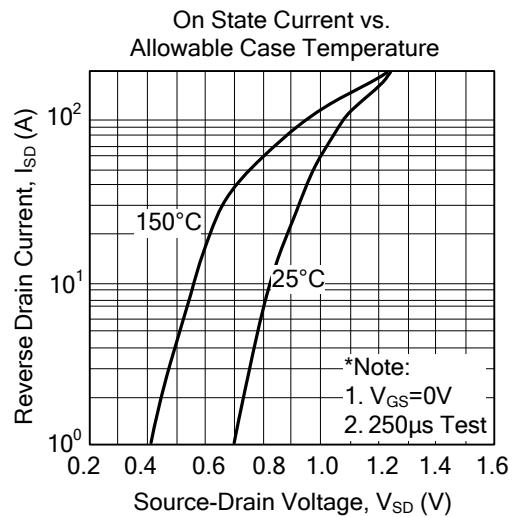
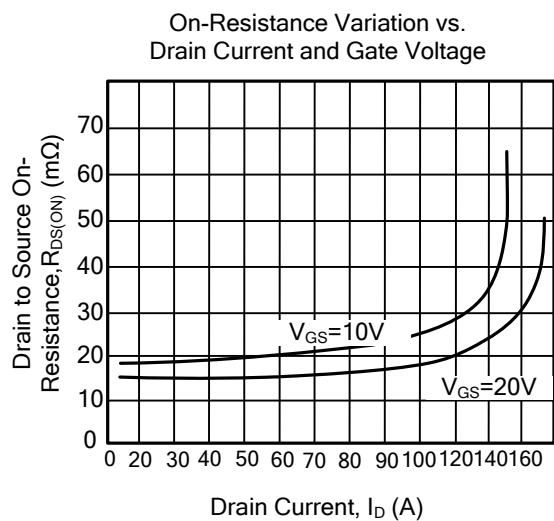
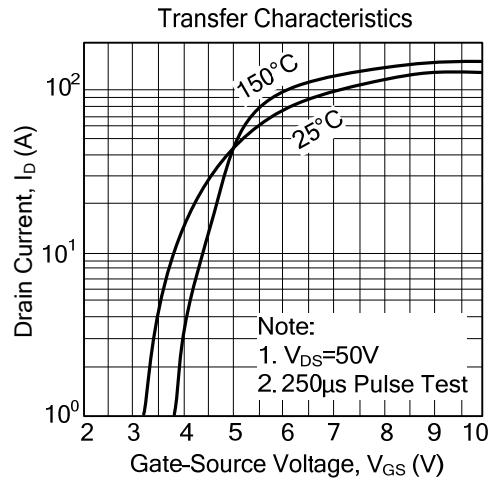
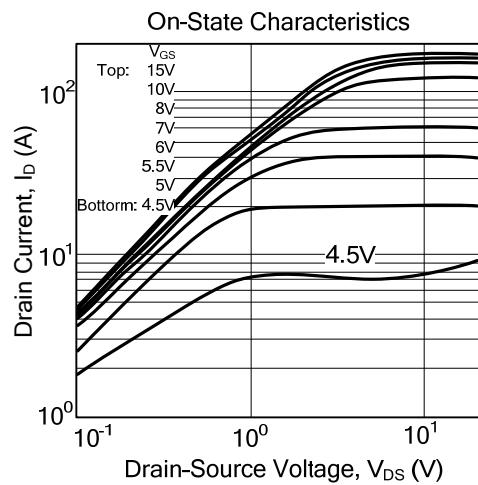


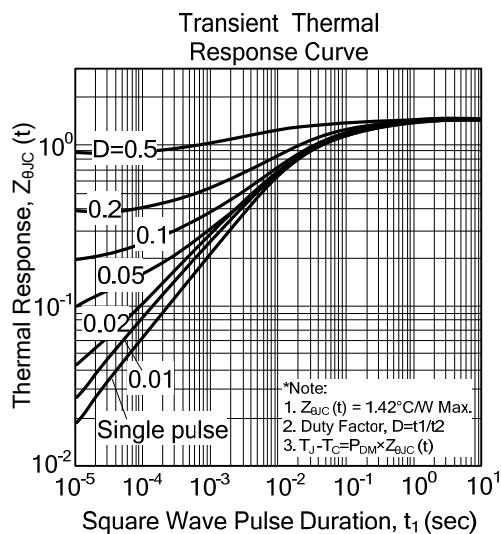
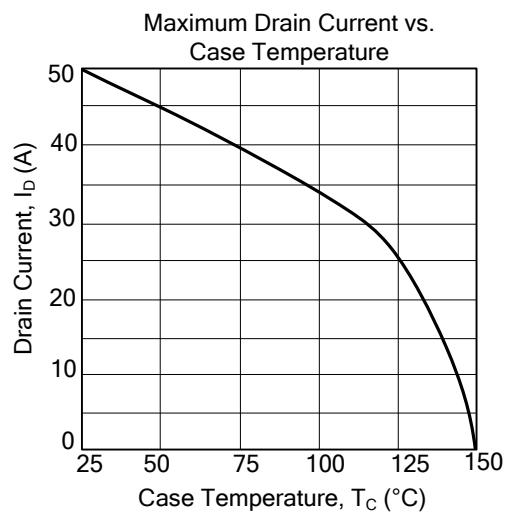
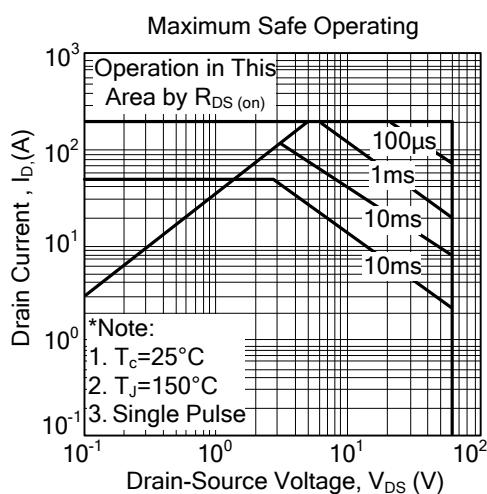
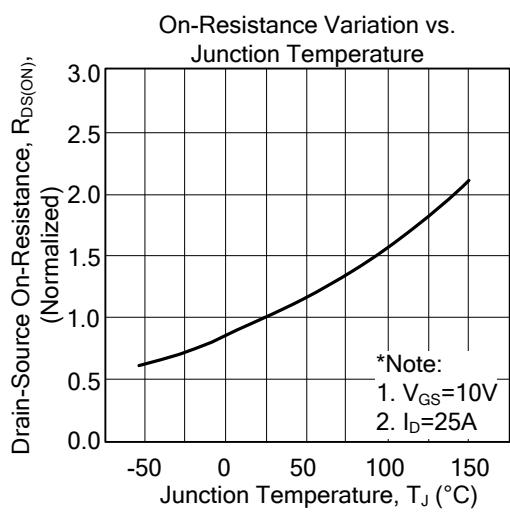
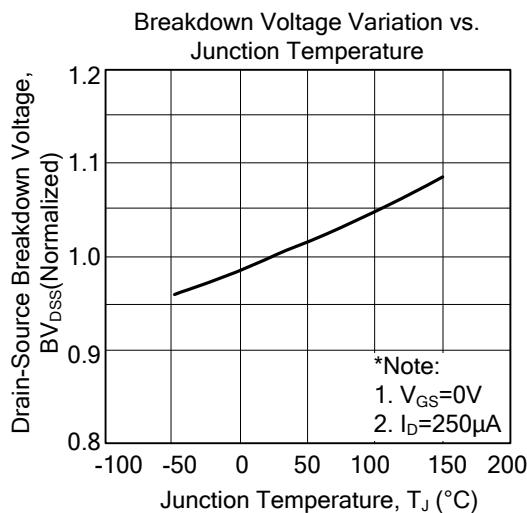
Fig. 4A Unclamped Inductive Switching Test Circuit

Fig. 4B Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS(Cont.)



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