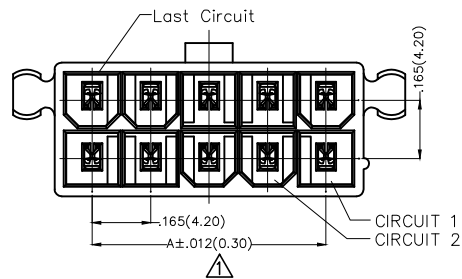


REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD
1	△	Size change	15/MAY/20	KATE	CHERRY

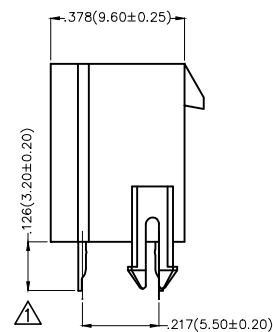
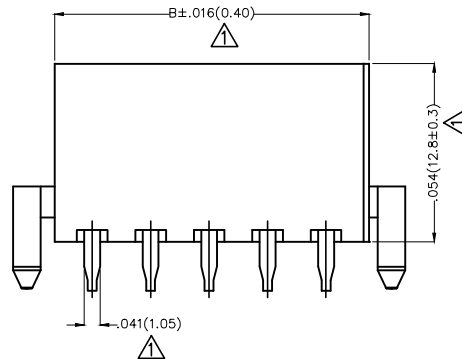


Electrical

Current Rating: 9A AC(rms)/DC
Voltage Rating: 600V AC(rms)/DC
Contact Resistance: 10 mΩ Max
Insulation Resistance: 1000 MΩ MIN
Withstanding Voltage: 1500V AC r.m.s
Temperature Range—Operating: -40°C~+105°C

Material and Plating

Housing: PA66(UL 94V-0)
Contact Pin: Phosphor Bronze
Plating: Tin Plated

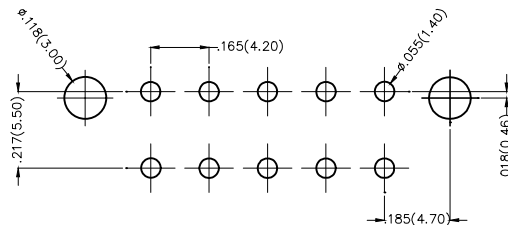


Circuits (n)	Part No.	Dimensions(in/mm)	
		A	B
2	FWF42008-D02S22TK	—	.213(5.40)
4	FWF42008-D04S22TK	.165(4.20)	.379(9.60)
6	FWF42008-D06S22TK	.331(8.40)	.543(13.80)
8	FWF42008-D08S22TK	.496(12.60)	.709(18.00)
10	FWF42008-D10S22TK	.661(16.80)	.874(22.20)
12	FWF42008-D12S22TK	.827(21.00)	1.039(26.40)
14	FWF42008-D14S22TK	.992(25.20)	1.205(30.60)
16	FWF42008-D16S22TK	1.157(29.40)	1.370(34.80)
18	FWF42008-D18S22TK	1.323(33.60)	1.535(39.00)
20	FWF42008-D20S22TK	1.488(37.80)	1.700(43.20)
22	FWF42008-D22S22TK	1.654(42.00)	1.866(47.40)
24	FWF42008-D24S22TK	1.819(46.20)	2.031(51.60)

Ordering Information

FWF 420 08 — D XX S 2 2 T K
1 2 3 4 5 6 7 8 9 10

1	Category FWF—Wafer	2	Series Number 420—Pitch 4.2mm	3	Distinction No. 08	4	Row Option Double Row	5	Circuits XX	6	Entry Angle S—180° Vertical
7	Plating 2—Tin Plated	8	Material—Resin 2—PA66	9	Color—Resin T—Transparent	10	Packaging K—Tray				



Recommended P.C.Board Layout

 THIRD ANGLE PROJECTION		GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY FRANK	DATE 28/JUN/13	PART NO. FWF42008-DXXS22TK	ITEM NO. FWF42008	 Leader Of Industry	
		X.±.012(0.30)	X.±5°	CHECKED BY	DATE	TITLE			
DESIGN UNITS Inch (metric)		X.X±.008(0.20)	.X'±2°	JACOB	28/JUN/13	Wire to Board (Wafer) Pitch 4.2mm 180° Vertical (DIP)		REV 1	SHEET NO. 1/1
		X.XX±.006(0.15)	.XX'±1°	DRAWN BY	DATE	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			
SCALE 5:1	SIZE A4	X.XXX±.004(0.10)	.XXX'±0.5°	CHERRY	28/JUN/13				

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