

REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD
1		Dimension: $\phi.065(\phi1.65)$ Change to $\phi.071(\phi1.80)$	20150919	CHERRY	FAN
2		Dimension: .587(14.90) Change to .567(14.40)	20171127	VERNON	
3		Add dimension	26/APR/19	KATE	FRANK

Electrical

Current Rating: 7A AC(rms)/DC

Voltage Rating: 250V AC(rms)/DC

Contact Resistance: 10 m Ω Max

Insulation Resistance: 1000 M Ω MIN

Withstanding Voltage: 1500V AC r.m.s

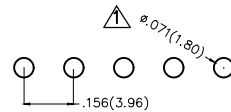
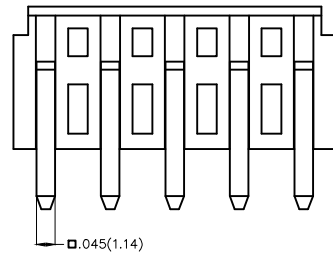
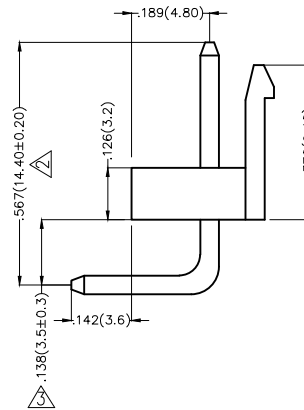
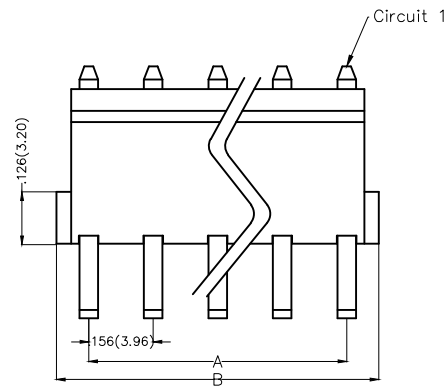
Temperature Range—Operating: $-25^{\circ}\text{C}\sim+85^{\circ}\text{C}$

Material and Plating

Housing: PA6T(UL 94V-0)/PA66(UL 94V-0)

Contact Pin: Brass

Plating: Tin Plated



Recommended P.C.Board Layout

Ordering Information

FWF 396 02 — S XX B 2 X XX B
1 2 3 4 5 6 7 8 9 10

1	Category FWF—Wafer	2	Series Number 396—Pitch 3.96mm	3	Distinction No. 02	4	Row Option S—Single Row	5	Circuits xx	6	Entry Angle B—90° Angle
7	Plating 2—Tin Plated	8	Material—Resin 2—PA66 4—PA6T	9	Color—Resin W1—White W5—White	10	Packaging B—PE Bag				

 THIRD ANGLE PROJECTION		GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY FRANK	DATE 19/SEP/15	PART NO. FWF39602—SXXB2XXXB	ITEM NO. FWF39602	 Leader Of Industry	
		X.±.012(0.30)	X.±5°	CHECKED BY	DATE	TITLE Wire to Board (Wafer) Pitch 3.96mm 90° Angle (DIP)			
DESIGN UNITS Inch (metric)		X.X±.008(0.20)	.X°±2°	CHERRY	19/SEP/15				
SCALE 5:1	SIZE A4	X.XX±.006(0.15)	.XX°±1°	DRAWN BY	DATE	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			
		X.XXX±.004(0.10)	.XXX°±0.5°	JACOB	19/SEP/15				

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