

TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

TCR3UG series

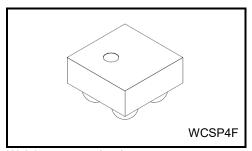
Ultra low quiescent current, Fast Load Transient 300 mA CMOS Low Dropout Regulator in ultra small package

The TCR3UG series are CMOS general-purpose single-output voltage regulators with an on/off control input, featuring ultra low quiescent bias current and low dropout voltage.

These voltage regulators are available in fixed output voltages between 0.8 V and 5.0 V and capable of driving up to 300 mA. They feature Over-current protection, Thermal Shutdown function and Auto-discharge option.

The TCR3UG series is offered in the ultra small plastic mold package WCSP4F (0.645 mm x 0.645 mm (typ.); t 0.33 mm (max)) and has a low dropout voltage of 155 mV (3.3 V output, IOUT = 300 mA). As small ceramic input and output capacitors 1 μF can be used with the TCR3UG series, these devices are ideal for portable applications that require high-density board assembly

such as cellular phones, IoT equipment and wearable devices.



Weight: 0.26 mg (typ.)

Applications

Power IC developed for portable applications, IoT equipment and wearable devices

Features

- Ultra small package WCSP4F (0.645 mm x 0.645 mm (typ.); t 0.33 mm (max)).
- Low quiescent bias current (IB(ON) = 0.34 µA (typ.) at IOUT = 0 mA, output voltage up to 1.5 V)
- High ripple rejection ratio 70 dB (Vout = 0.8 V)
- Fast load transient response ± 60 mV (Vout = 0.8 V), Iout = 1 mA ⇔ 50 mA
- Low dropout voltage
 - VDO =VIN-VOUT = 140 mV (typ.) (VOUT = 3.3 V), IOUT = 300 mA
- Wide range output voltage line up (Vout = 0.8 to 5.0 V)
- High Vout accuracy ± 1.0 % (1.8 V ≤ Vout)
- Auto-discharge (TCR3UGxxA series)/ Non-discharge (TCR3UGxxB series) line up
- Overcurrent protection
- · Thermal shutdown function
- Inrush current protection circuit
- Pull down connection between CONTROL and GND
- Ceramic capacitors can be used (CIN = 1 μF, COUT = 1 μF)

Start of commercial production 2017-10



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Input voltage	VIN	-0.3 to 6.0	V
Control voltage	Vст	-0.3 to V _{IN} + 0.3 ≤ 6.0	V
Output voltage	Vout	-0.3 to V _{IN} + 0.3 ≤ 6.0	V
Power dissipation	PD	800 (Note1)	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	−55 to 150	°C

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Rating at mounting on a board

Glass epoxy (FR4) board dimension: 40 mm x 40 mm (both sides of board), t = 1.6 mm Metal pattern ratio: a surface approximately 50 %, the reverse side approximately 50 %

Through hole: diameter 0.5 mm x 24

Operating Ranges

Characteristics	Symbol		Rating		Unit
Input voltage	VIN	1.5 to 5.5 (Note 2)			V
Control voltage	Vст	0 to VIN			V
Output voltage	Vout	0.8 to 5.0			V
Output current	lout	DC 300 (Note 3)		mA	
Operating Temperature	T _{opr}	-40 to 85			°C
Output Capacitance	Cout	≥ 1.0 µF			_
Input Capacitance	CIN	≥ 1.0 µF			_

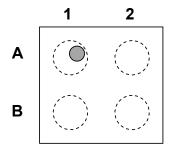
Note 2: IOUT = 1 mA.

Please refer to Dropout Voltage (Page 13) and use it within Absolute Maximum Ratings Junction temperature and Operation Temperature Ranges.

Note 3: Do not operate at or near the maximum ratings of operating ranges for extended periods of time. Exposure to such conditions may adversely impact product reliability and results in failures not covered by warranty.



Pin Assignment (top view)



	1	2
Α	VIN	VOUT
В	CONTROL	GND



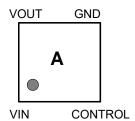
List of Products Number, Output voltage and Marking

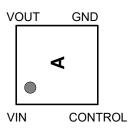
Product No.	Output voltage(V)	Auto dis- charge	Marking	Product No.	Output voltage(V)	Auto dis- charge	Marking**
TCR3UG08A	0.8		Α	TCR3UG08B	0.8		Α
TCR3UG085A	0.85		В	TCR3UG085B	0.85		В
TCR3UG09A	0.9		С	TCR3UG09B	0.9		С
TCR3UG095A	0.95		D	TCR3UG095B	0.95		D
TCR3UG10A	1.0		Е	TCR3UG10B	1.0		Е
TCR3UG105A	1.05		F	TCR3UG105B	1.05		F
TCR3UG11A	1.1		Н	TCR3UG11B	1.1		Н
TCR3UG115A	1.15		J	TCR3UG115B	1.15		J
TCR3UG12A	1.2		K	TCR3UG12B	1.2		K
TCR3UG13A	1.3		L	TCR3UG13B	1.3		L
TCR3UG135A	1.35		М	TCR3UG135B	1.35		М
TCR3UG15A	1.5		N	TCR3UG15B	1.5		N
TCR3UG175A	1.75		Р	TCR3UG175B	1.75		Р
TCR3UG18A	1.8		R	TCR3UG18B	1.8		R
TCR3UG1825A	1.825		G	TCR3UG185B	1.85		S
TCR3UG185A	1.85	Yes	S	TCR3UG19B	1.9	No	Т
TCR3UG19A	1.9	165	Т	TCR3UG25B	2.5	INO	U
TCR3UG25A	2.5		U	TCR3UG26B	2.6		V
TCR3UG26A	2.6		V	TCR3UG27B	2.7		W
TCR3UG27A	2.7		W	TCR3UG28B	2.8		Х
TCR3UG28A	2.8		Х	TCR3UG285B	2.85		Υ
TCR3UG285A	2.85		Υ	TCR3UG30B	3.0		0
TCR3UG30A	3.0		0	TCR3UG31B	3.1		1
TCR3UG31A	3.1		1	TCR3UG32B	3.2		2
TCR3UG32A	3.2		2	TCR3UG33B	3.3		3
TCR3UG33A	3.3		3	TCR3UG35B	3.5		4
TCR3UG35A	3.5		4	TCR3UG36B	3.6		5
TCR3UG36A	3.6		5	TCR3UG41B	4.1		9
TCR3UG41A	4.1		9	TCR3UG42B	4.2		6
TCR3UG42A	4.2		6	TCR3UG45B	4.5		7
TCR3UG45A	4.5		7	TCR3UG50B	5.0		8
TCR3UG50A	5.0		8				

^{**}Marking is rotated 90 degrees to the left.

Top Marking (top view)

Example: TCR3UG08A (0.8 V output)

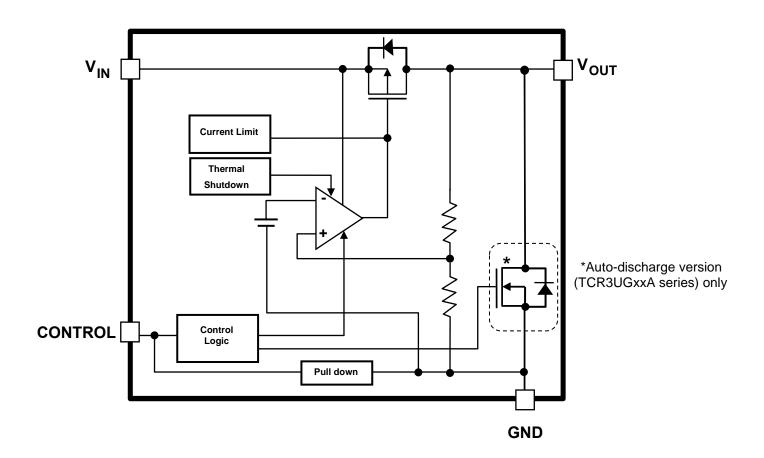




Example: TCR3UG08B (0.8 V output)



Block Diagram





Electrical Characteristics

(Unless otherwise specified,

VIN = VOUT+1 V (VOUT>1.5V), VIN=2.5V (VOUT \leq 1.5V), IOUT = 50 mA, CIN = COUT = 1 μ F)

Characteristics Sym		Test Condition		T _j = 25°C			T _j = -40 to 85°C (Note 9)		Unit
				Min	Тур.	Max	Min	Max	
Output voltage accuracy	Vout	IOUT = 50 mA	Vout < 1.8 V	-18	_	+18	_	_	mV
Output voltage accuracy	VOU1	(Note 4)	1.8 V ≤ V _{OUT}	-1.0	_	+1.0	_	_	%
Input voltage	VIN	I _{OUT} = 1 mA		1.5	_	5.5	1.5	5.5	V
Line regulation	Reg·line	I _{OUT} = 1 mA	(Note 5)	_	1	15	_	_	mV
Load regulation	Reg·load	1 mA ≤ I _{OUT} ≤ 300	mA (Note 6)	_	10	30	_	_	mV
	I _B (ON1)	I _{OUT} = 0 mA, V _{OU}	_T ≤ 1.5V (Note 7)	_	0.34	_	_	0.58	μΑ
Quiescent current	I _{B(ON2)}	I _{OUT} = 0 mA, 1.75 V ≤ V _{OUT} ≤ 5	V (Note 7)	_	0.38	_	_	0.68	μA
Ot and have assessed	I _B (OFF1)	V _{CT} = 0 V, V _{IN} = 2.5 V		-	0.03	_	_	0.16	μΑ
Stand-by current	I _B (OFF2)	V _{CT} = 0 V, V _{IN} = 5.5 V		-	0.03	_	_	0.20	μΑ
Control pull down current	ICT	_		-	0.1	_	_	_	μΑ
Decreed wells as			V _{OUT} = 1.8 V	_	335	_	_	457	mV
Dropout voltage	VDO	IOUT = 300 mA	V _{OUT} = 3.3 V	_	140	_	_	273	mV
Output noise voltage	V _{NO}	I _{OUT} = 10 mA, 10 Hz ≤ f ≤ 100 kHz, Ta = 25°C (Note 6)		_	50	_	_	_	μV _{rms}
Ripple rejection ratio	R.R.	I _{OUT} = 10 mA, f = 1 kHz, V _{Ripple} = 200 mV _{p-p} , Ta = 25°C (Note 6)		_	70	_	_	_	dB
1 1 (4) (I _{OUT} = 1 mA → 50 mA (Note 8)		_	-60	_	_	_	mV
Load transient response ∠V _{OU}		I _{OUT} = 50 mA → 1 mA (Note 8)		-	+60	_	_	_	mV
Temperature coefficient	T _{CVO}	-40°C ≤ T _{opr} ≤ 85°C		_	75	_	_	_	ppm/°C
Control voltage (ON)	VCT (ON)	_		1.0	_	5.5	1.0	5.5	V
Control voltage (OFF)	VCT (OFF)	_		0	_	0.4	0	0.4	V
Discharge on resistance	R _{SD}	_		_	10	_	_	_	Ω

Note 4: stable state with fixed IOUT condition Note 5: $VOUT \le 1.5 \text{ V}, 2.5 \text{ V} \le VIN \le 5.5 \text{ V}$

1.75 V \leq VOUT \leq 4.2 V, VOUT + 1 V \leq VIN \leq 5.5 V VOUT = 4.5 V, VOUT = 5.0 V, not applicable

Note 6: VOUT = 0.8V

Note 7: except Control pull down current (ICT)

Note 8: VOUT = 0.8 V, VIN = 3.3 V

Note 9: This parameter is warranted by design



Dropout voltage

(IOUT = 300 mA, $C_{IN} = C_{OUT} = 1 \mu F$)

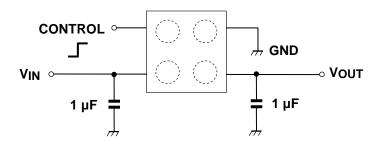
Output voltages	Symbol	Min	Typ. Tj = 25°C	Max (Note 10)	Unit	
0.8 V ≤ V _{OUT} < 0.9 V		_	1025	1257		
0.9 V ≤ V _{OUT} < 1.0 V		_	930	1157		
1.0 V ≤ V _{OUT} < 1.1 V		_	835	1057		
1.1 V ≤ V _{OUT} < 1.2 V		_	740	957		
1.2 V ≤ V _{OUT} < 1.3 V		_	660	857		
1.3 V ≤ V _{OUT} < 1.5 V		_	580	757		
1.5 V ≤ V _{OUT} < 1.6 V	.,	_	450	617	.,	
1.6 V ≤ V _{OUT} < 1.8 V	VDO	_	400	537	mV	
1.8 V ≤ V _{OUT} < 2.0 V		_	335	457		
2.0 V ≤ V _{OUT} < 2.5 V		_	260	405		
2.5 V ≤ V _{OUT} < 3.0 V		_	185	327		
3.0 V ≤ V _{OUT} < 3.6 V		_	140	273		
3.6 V ≤ V _{OUT} < 4.5 V		_	130	228		
4.5 V ≤ V _{OUT} ≤ 5.0 V		_	120	195		

Note 10: T_j = -40 to 85 °C. This parameter is warranted by design



Application Note

1. Example of Application Circuit



CONTROL voltage	VOUT voltage
HIGH	ON
LOW	OFF
OPEN	OFF

The figure above shows the Example of configuration for using a Low-Dropout regulator. Insert a capacitor at V_{OUT} and V_{IN} pins for stable input/output operation. (Ceramic capacitors can be used).

2. Power Dissipation

Board-mounted power dissipation ratings are available in the Absolute Maximum Ratings table. Power dissipation is measured on the board condition shown below.

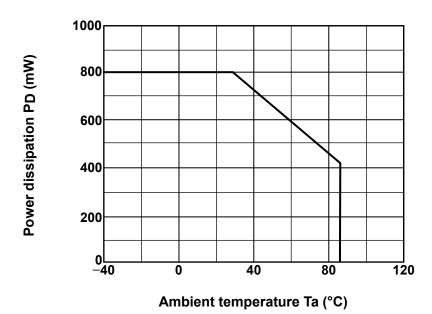
[The Board Condition]

Board material: Glass epoxy (FR4)

Board dimension: 40 mm x 40 mm (both sides of board), t = 1.6 mm

Metal pattern ratio: a surface approximately 50 %, the reverse side approximately 50 %

Through hole: diameter 0.5 mm x 24





Attention in Use

Output Capacitors

Ceramic capacitors can be used for these devices. However, because of the type of the capacitors, there might be unexpected thermal features. Please consider application condition for selecting capacitors. And Toshiba recommends the ESR of ceramic capacitor is under 10 Ω . For stable operation, we recommend over 1 μ F.

Mounting

The long distance between IC and input output capacitor might affect phase compensation by impedance in wire and inductor. For stable power supply, input output capacitor need to mount near IC as much as possible. Also VIN and GND pattern need to be large and make the wire impedance small as possible.

Permissible Loss

Please have enough design patterns for expected maximum permissible loss. And under consideration of ambient temperature, input voltage, output current etc., we recommend proper dissipation ratings for maximum permissible loss; in general maximum dissipation rating is 70 to 80 %.

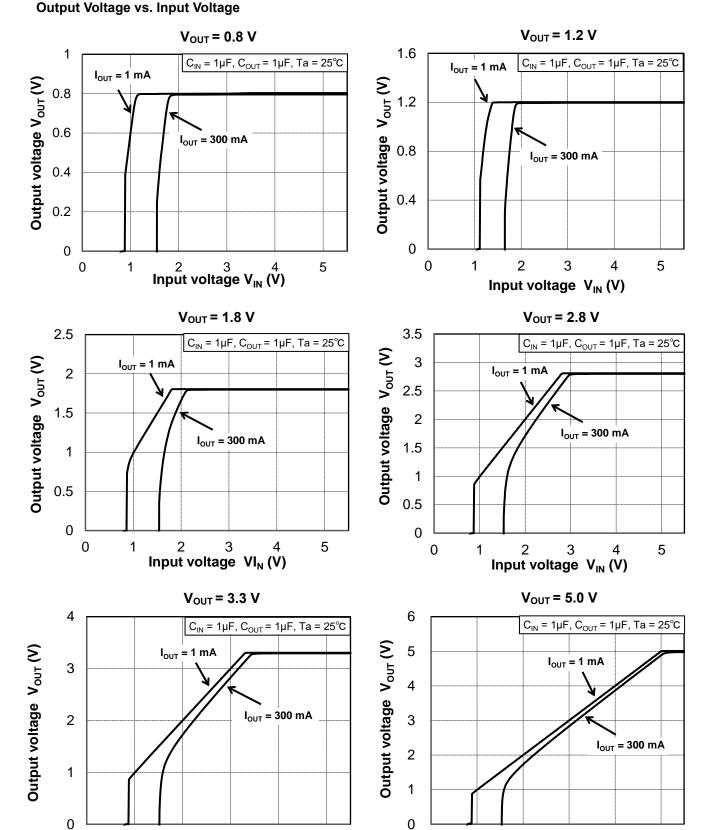
Over current Protection and Thermal shutdown function

Over current protection and Thermal shutdown function are designed in these products, but these are not designed to constantly ensure the suppression of the device within operation limits. Depending on the condition during actual usage, it could affect the electrical characteristic specification and reliability. Also note that if output pins and GND pins are not completely shorted out, these products might break down.

When using these products, please read through and understand the concept of dissipation for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommends inserting failsafe system into the design.



Representative Typical Characteristics

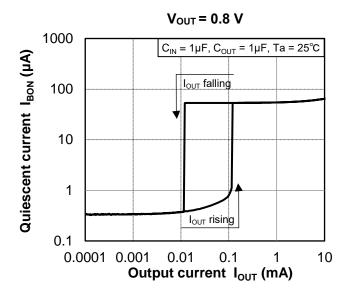


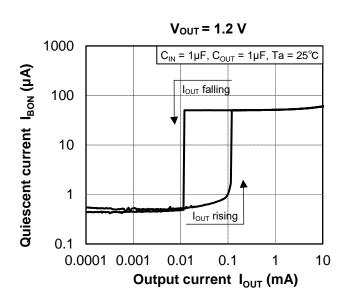
Input voltage V_{IN} (V)

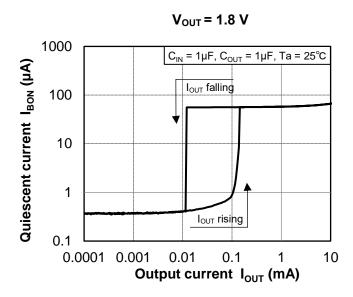
Input voltage V_{IN} (V)

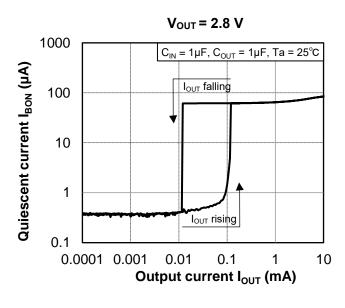


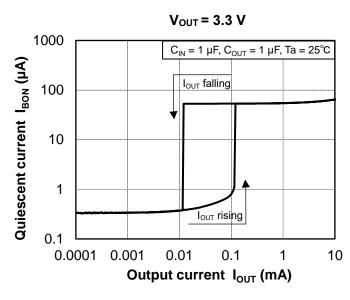
Output Voltage vs. Output Current

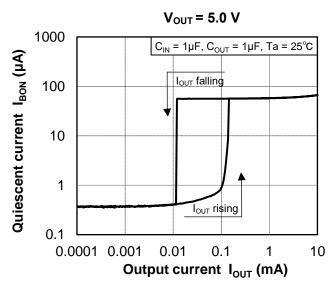






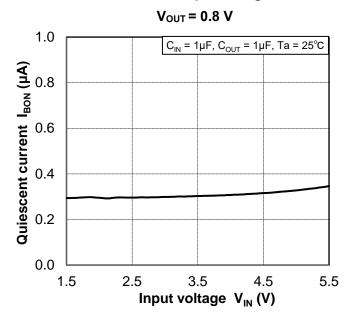


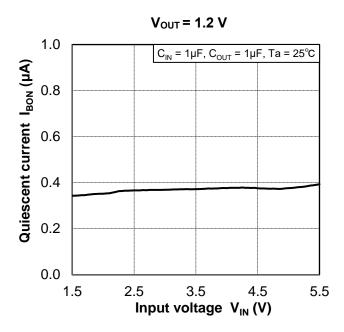


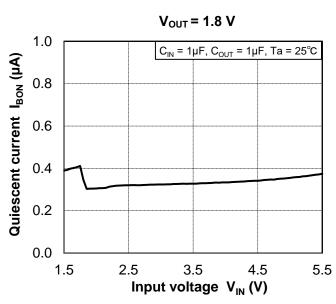


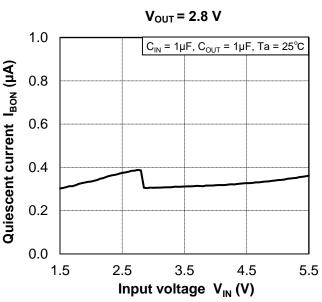


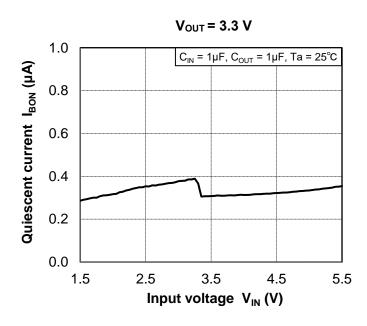
Quiescent Current vs. Input Voltage

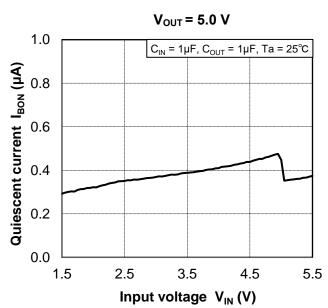






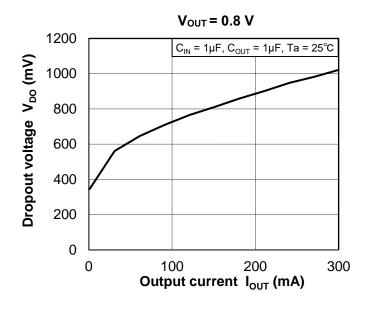


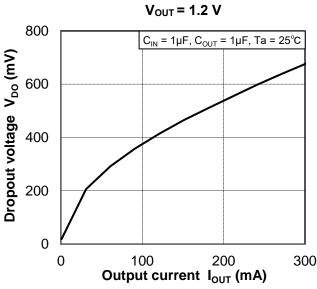


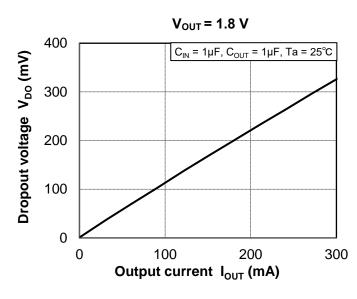


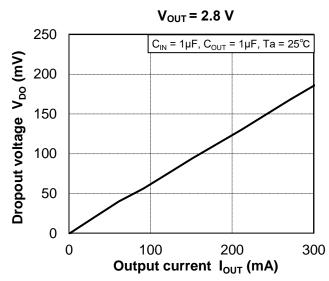


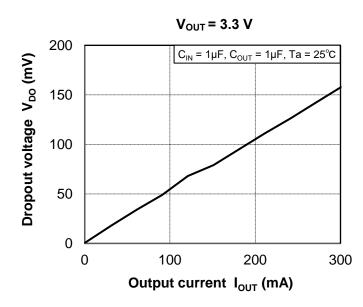
Dropout Voltage vs. Output current

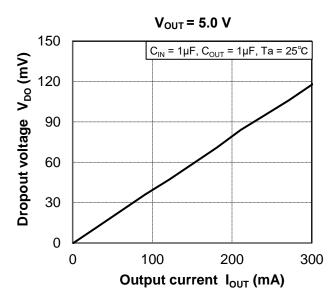






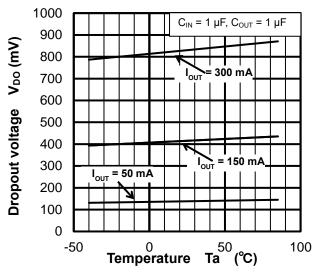


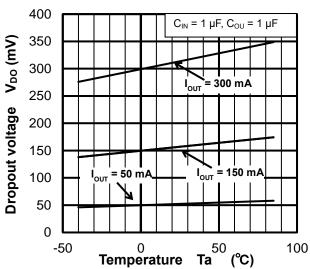


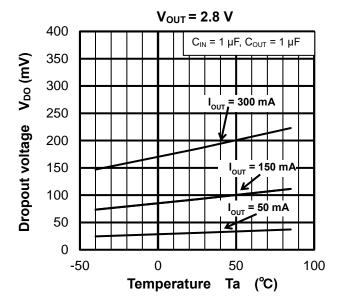




Dropout Voltage vs. Temperature

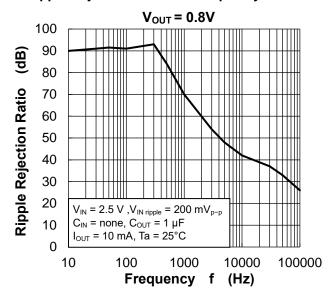


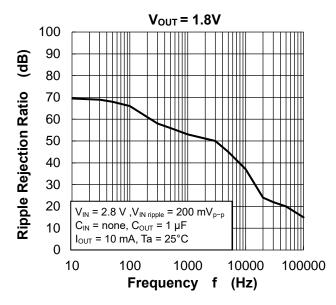


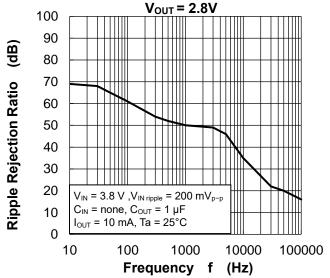




Ripple Rejection Ratio vs. Frequency





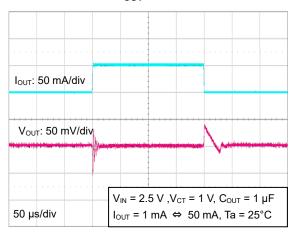


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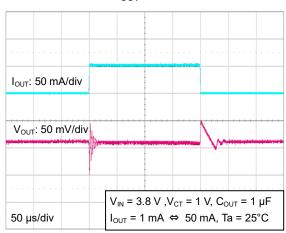


Load Transient Response

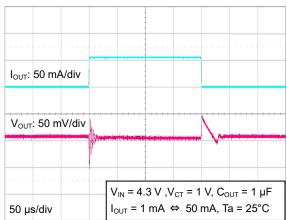
$V_{OUT} = 0.8V$



$V_{OUT} = 2.8V$



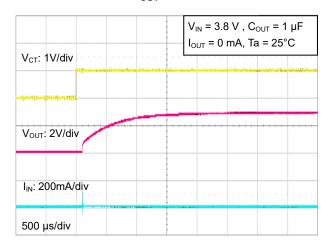
$V_{OUT} = 3.3V$



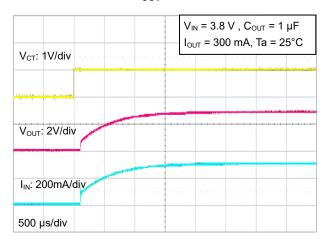


ton Response

$V_{OUT} = 2.8V$

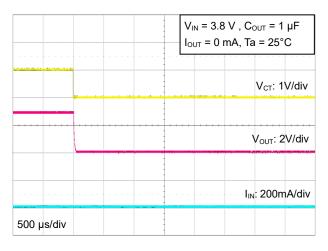


$V_{OUT} = 2.8V$

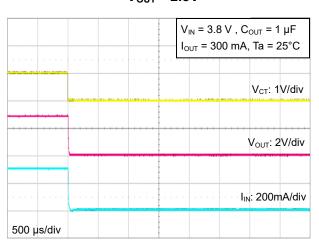


toff Response (Auto-discharge)

 $V_{OUT} = 2.8V$



$V_{OUT} = 2.8V$

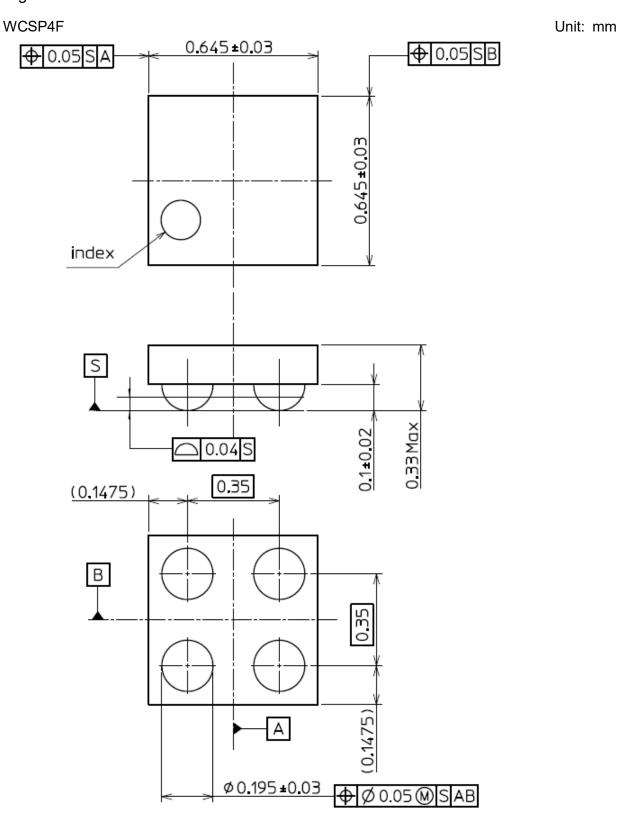


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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Package Information

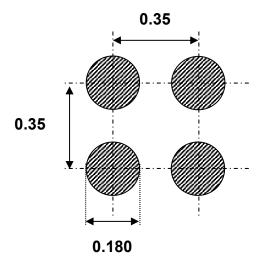


Weight: 0.26 mg (typ.)



Land pattern dimensions for reference only

Unit: mm





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