REVISIONS DESCRIPTION APVD DWN INITIAL DRAWN 220CT2019 RV MB

## **Product Facts**

Oualified to:

MIL-PRF-83726/28 MIL-PRF-83726/29 MIL-PRF-83726/30 MIL-PRF-83726/31

- Fixed delay on operate. fixed delay on release, adjustable delay on operate & adjustable delay on release
- Meets or exceeds electrostatic discharge MIL-STD-1686 Class Non-Sensitive
- Welded hermetically sealed enclosure occupies about 1 in<sup>3</sup> (16.4 cm<sup>3</sup>)
- 10A, 2 form C (DPDT) output

TD2 series time delay relays are available for delay on gating a semiconductor operate or delay on release operation. Either can be whether the controlling supplied as fixed or resistor adjustable types. Both military and commercial function. versions are offered.

These products consist of solid state timing circuits controlling our FCA-210 series relays, providing 2 Form C (DPDT) output contacts rated 10 amps. The internal timing circuit uses an R/C controlled oscillator with a programmable digital pulse counter, switch to operate the relay. Timing is independent of voltage is a ramp or step

For the adjustable models the user specifies a one decade range in seconds, within which the required delay will be set. This range is programmed internally at the time of manufacture. The required delay is obtained by calculating the oscillator timing resistor as

follows and connecting it externally to terminals 1D · 3D as below.

 $R_{EXT} = [(T_1 / T_0) - 1] 100K$ 

 $T_0 = Minimum time of$ selected decade in seconds.

T1 = Required time delay. EXAMPLE

Selected Range = 3-30 sec Required Time = 15 sec

 $R_{EXT} = [(15/3) -1] 100K = 400K$ 



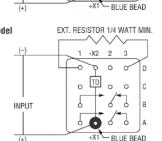
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# **Timing Action and Terminal Wiring**

The time delay starts on the application of input voltage to X1-X2. The timing circuit energizes the end of the time delay period.

INPUT VOLTAGE +X1 / -X2 INPUT OFF ENERGIZED OUTPUT DEENERGIZED | DELAY

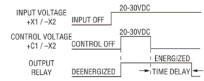
Fixed Model -X2 2 0 0 o TD 0 0 INPUT



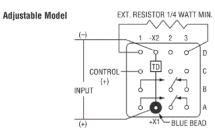
### Delay On Release:

Fixed Model

The input voltage is continuous to X1-X2. When the control voltage is applied to C1-X2 the timing circuit and the relay are both energized. The time delay starts when the control voltage is shut off.



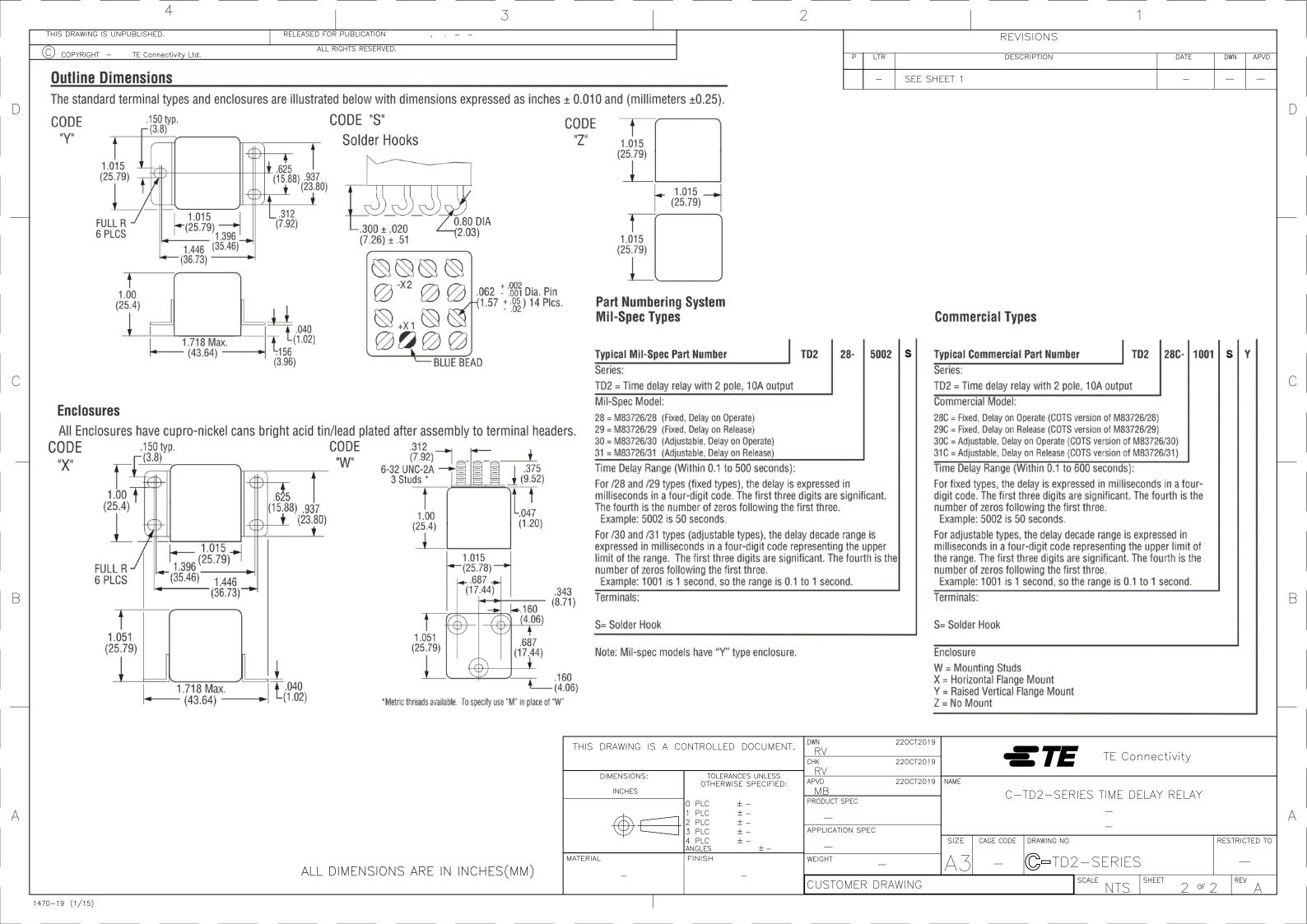
-X2 2 CONTROL INPUT 6 6



# Terminal designations shown in the diagrams above are for reference only. They do not appear on the relay header.

 THIS DRAWING IS A CO	DWN 220CT201	∍	_			
11113 1510/14/1110 15 77 00	RV		TE Connectivity			
		CHK 220CT201	<b>∃</b>			
DIMENSIONS:	TOLERANCES UNLESS	- <u>  RV                                   </u>				
DIMENSIONS.	OTHERWISE SPECIFIED:	APVD 220CT201	NAME			
INCHES		MB		C-	TD2-SERIES TIME DELAY RELA	Y
	O PLC ± -	PRODUCT SPEC		C	TOZ SENIES HIVE DELAN NELA	'
1	1 PLC ± -				<del>-</del>	
	2 PLC ± -	_				
	3 PLC ± -	APPLICATION SPEC			<del>_</del>	
т -	4 PLC ± -		SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
	ANGLES ±-					
MATERIAL	FINISH	WEIGHT	$I \wedge Z$	_	C-TD2-SERIES	
					S 102 3LIVIL3	
_	_	CUSTOMER DRAWING		•	SCALE SHEET	REV .
		CUSTOMER DRAWING			I NIS I 1	of 2   A

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