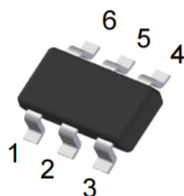


Features

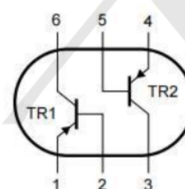
- High current (500mA)
- Replaces two SOT23 packaged transistors on same PCB area.
- Transistor elements are independent, eliminating interference.
- Mounting cost, and area, are reduced by one half.

Package and Pin Configuration

SOT23-6



Circuit diagram



Marking:T17

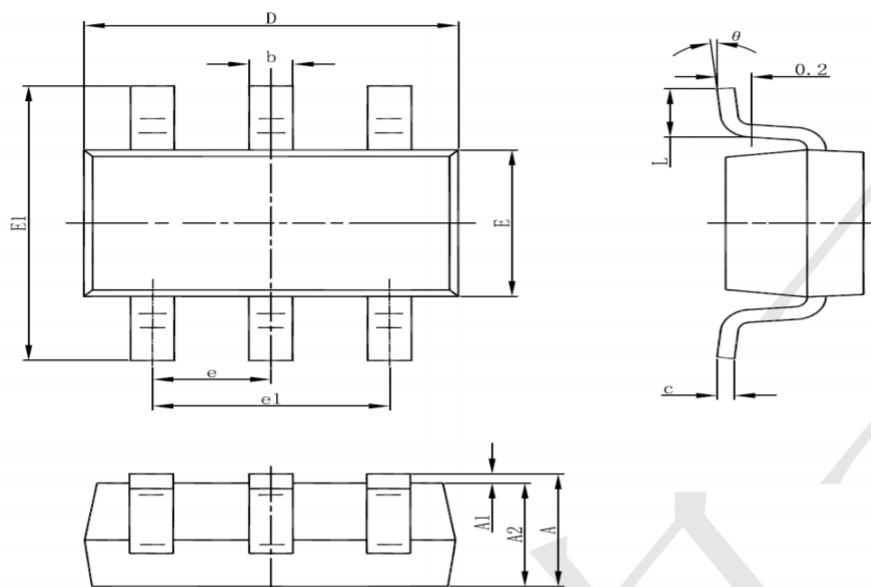
Absolute Maximum Ratings (T_A=25°C)

Parameter	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-60	V
Collector-Emitter Voltage	V _{CEO}	-50	V
Emitter-Base Voltage	V _{EBO}	-5	V
Collector Current -Continuous	I _C	-0.6	A
Power Dissipation	P _C	300 (Total)	mW
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-50 to +150	°C

Electrical Characteristics (T_A=25°C, unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I _C =-100μA, I _E =0	-60			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =-1mA, I _B =0	-50			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =-100μA, I _C =0	-5			V
Collector cut-off current	I _{CBO}	V _{CB} =-40V, I _B =0			-100	nA
Emitter cut-off current	I _{EBO}	V _{EB} =-4V, I _C =0			-100	nA
DC current gain	h _{FE}	V _{CE} =-3V, I _C =-100mA	120		390	
Collector-emitter saturation voltage	V _{CESAT}	I _C =-500mA, I _B =-50mA			-0.6	V
Base-emitter saturation voltage	V _{BESAT}	I _C =-500mA, I _B =-20mA			-1.2	V
Transition frequency	f _T	V _{CE} =-5V; I _C =-10mA; f=100MHz		250		MHz

SOT23-6 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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