

## Features

- 2 $\mu$ A Ground Current at no Load
- $\pm 2\%$  Output Accuracy
- 600mA Output Current
- 10nA Disable Current ( by option )
- Wide Operating Input Voltage Range: 1.2V to 5.5V
- Dropout Voltage: 0.32V at 600mA / V<sub>OUT</sub> 3.3V
- Adjustable Output Voltage
- Stable with Ceramic or Tantalum Capacitor
- Current Limit Protection
- Over-Temperature Protection
- SOT-23-5 Packages Available

## Applications

- Portable, Battery Powered Equipment
- Low Power Microcontrollers
- Laptop, Palmtops and PDAs
- Wireless Communication Equipment
- Audio/Video Equipment
- Car Navigation Systems

## General Description

The TP172CADJS5 are a group of low-dropout ( LDO ) voltage regulators offering the benefits of wide input voltage range from 1.2V to 5.5V, low dropout voltage, low power consumption, and miniaturized packaging. Quiescent current of only 2 $\mu$ A makes these devices ideal for powering the battery-powered, always-on systems that require very little idle-state power dissipation to a longer service life. There is an option of

shutdown mode by selecting the parts with the EN pin and pulling it low. The shutdown current in this mode goes down to only 10nA ( typical ).

The TP172CADJS5 of linear regulators are stable with the ceramic output capacitor over its wide input range from 1.2V to 5.5V and the entire range of output load current ( 0mA to 600mA ).

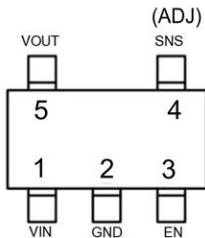
## Ordering Information

**TP172CADJS5**

S5:SOT23-5 Package

Output voltage: ADJ  
(SNS)VFB=0.8V

## PIN CONFIGURATION



Pin No	Pin Name	Pin Function
2	GND	Ground
5	VOUT	Output of the Regulator
1	VIN	Input of Supply Voltage.
3	EN	Enable Control Input.
4	SNS	Sense of Output Voltage.

## TYPICAL APPLICATION

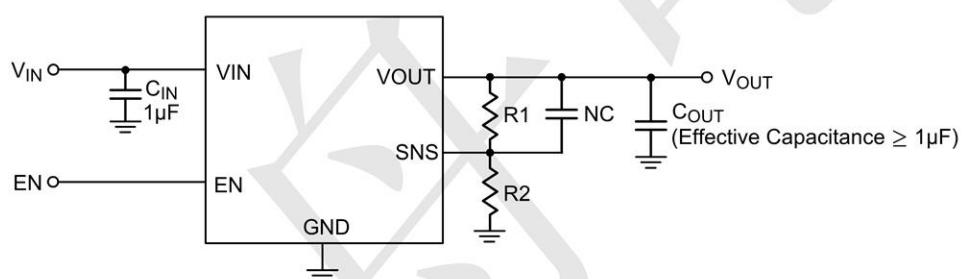
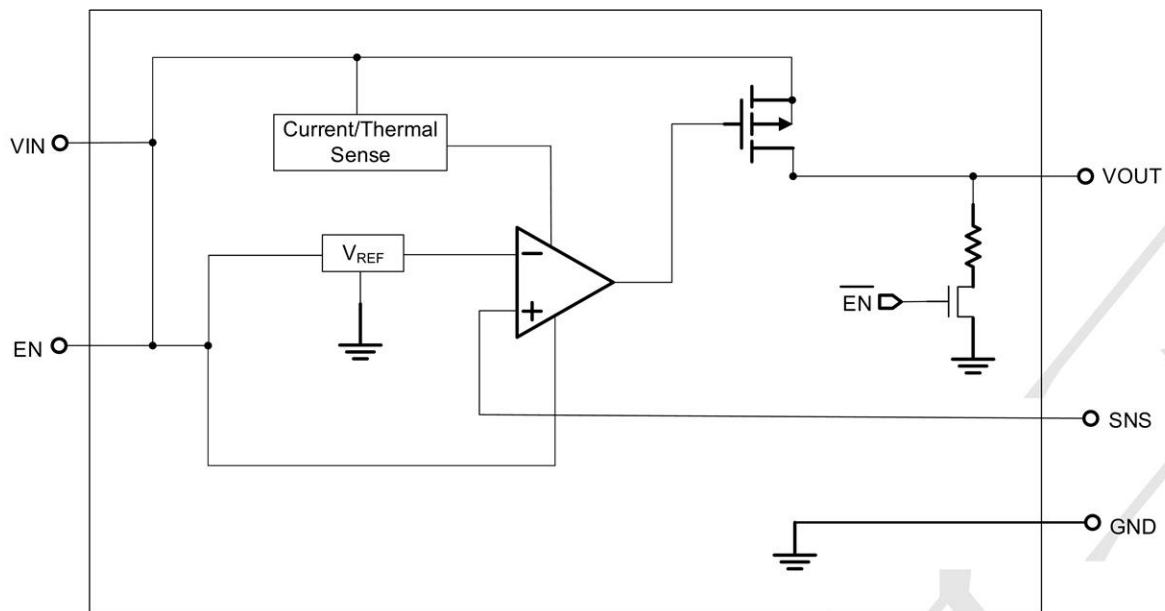


Figure 3. Adjustable Output Voltage Application Circuit

$$R_1 = R_2 \times \left( \frac{V_{OUT}}{0.8V} - 1 \right) \quad \text{where } R_2 < 24K\Omega$$



## BLOCK DIAGRAM



### Absolute Maximum Rating ( $T_A=25^\circ\text{C}$ unless otherwise noted)

VIN to GND -----	-0.3V to 6.5V
VOUT, EN, SNS to GND -----	-0.3V to 6V
VOUT to VIN -----	-6V to 0.3V
Package Thermal Resistance (Note 2)	
SOT-23-5, $\theta_{JA}$ -----	200 $^\circ\text{C} / \text{W}$
Lead Temperature (Soldering, 10 sec.) -----	260 $^\circ\text{C}$
Junction Temperature -----	150 $^\circ\text{C}$
Storage Temperature Range -----	-60 $^\circ\text{C}$ to 150 $^\circ\text{C}$
ESD Susceptibility	
HBM -----	2KV
MM -----	200V
CDM -----	2KV

### Recommended Operating Conditions

Input Voltage VIN -----	1.2V to 5.5V
Junction Temperature Range -----	-40 $^\circ\text{C}$ to 125 $^\circ\text{C}$
Ambient Temperature Range -----	-40 $^\circ\text{C}$ to 85 $^\circ\text{C}$



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TP172CADJS5

2uA 600mA Ultra-LowDropout Regulator

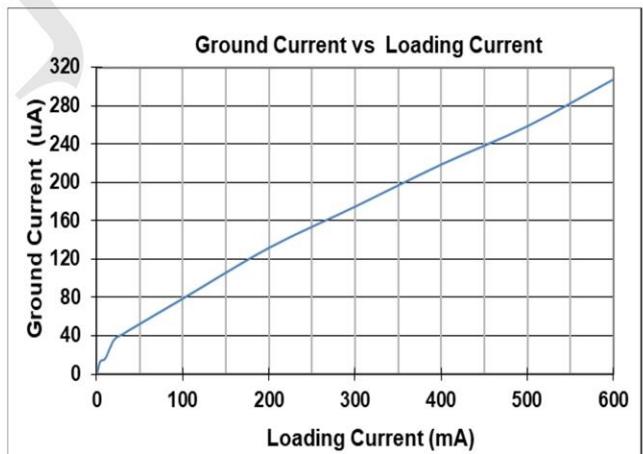
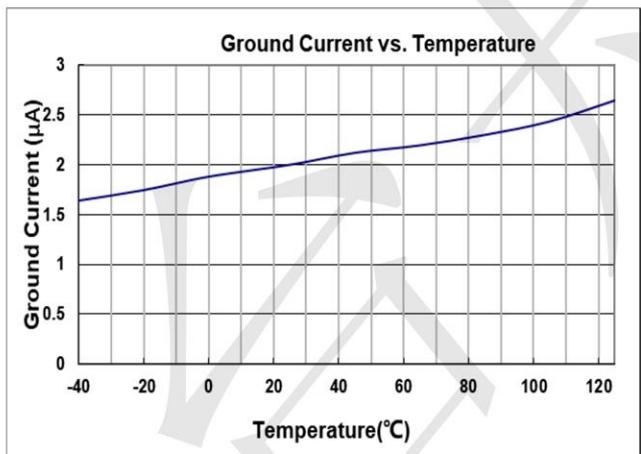
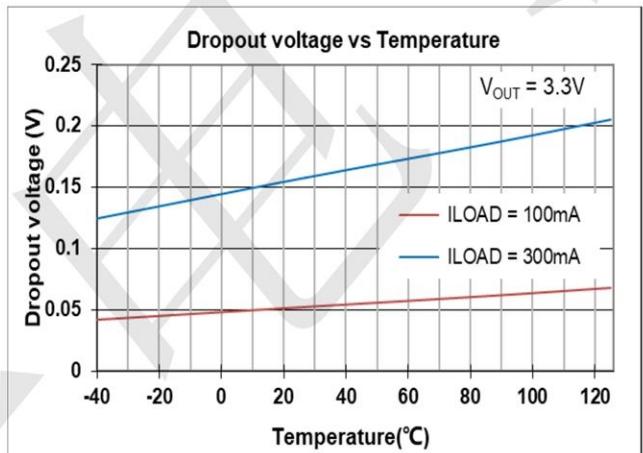
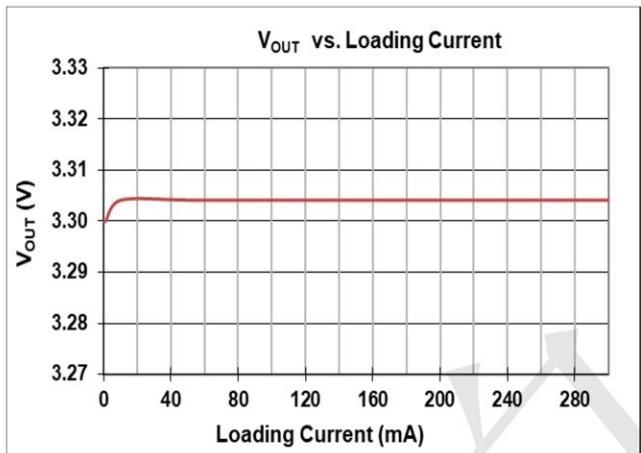
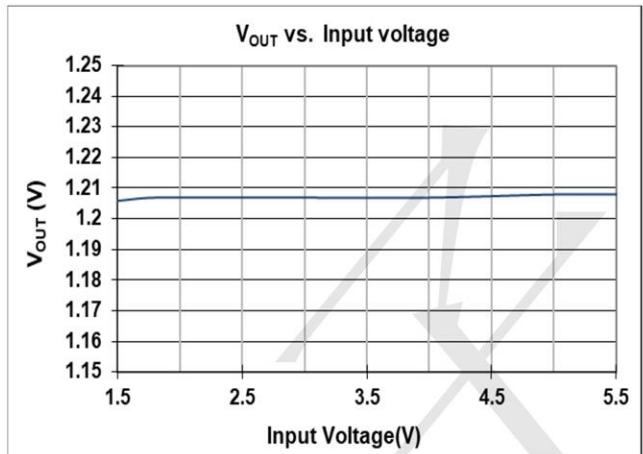
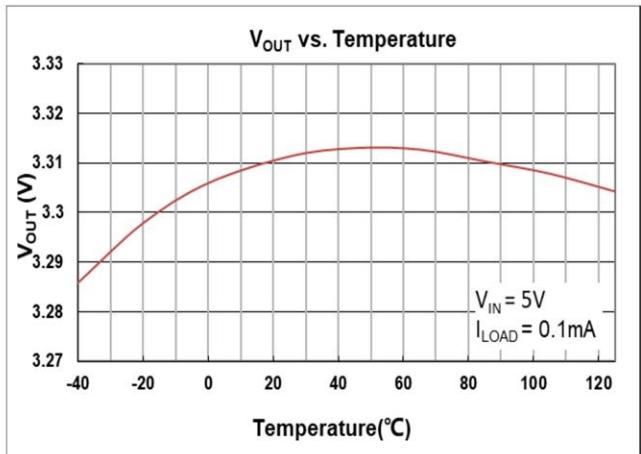
## Electrical Characteristics

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(V<sub>IN</sub> =5V, V<sub>EN</sub> = 5V T<sub>A</sub>=25°C unless otherwise specified)

Parameter	Symbol	Test Conditions		Min	Typ	Max	Unit
Supply Voltage	V <sub>IN</sub>			1.2	--	5.5	V
DC Output Voltage Accuracy	V <sub>SNS</sub>	I <sub>LOAD</sub> =0.1mA			0.8		V
SNS Input Current	I <sub>SNS</sub>	SNS = V <sub>OUT</sub>			0.7		μA
Dropout Voltage (I <sub>LOAD</sub> =600mA) (Note 3)	V <sub>DROP_3V</sub>	V <sub>OUT</sub> ≥ 3V			0.32		V
	V <sub>DROP_2.8V</sub>	V <sub>OUT</sub> = 2.8V			0.36		
	V <sub>DROP_2.5V</sub>	V <sub>OUT</sub> = 2.5V			0.36		
	V <sub>DROP_1.8V</sub>	V <sub>OUT</sub> = 1.8V			0.57		
	V <sub>DROP_1.5V</sub>	V <sub>OUT</sub> = 1.5V			0.71		
	V <sub>DROP_1.2V</sub>	V <sub>OUT</sub> = 1.2V			0.8		
Ground Current	I <sub>Q</sub>	I <sub>LOAD</sub> = 0mA			2		μA
Shutdown Ground Current	I <sub>SD</sub>	V <sub>EN</sub> = 0V, V <sub>OUT</sub> = 0V			0.01	0.5	μA
V <sub>OUT</sub> Shutdown Leakage Current	I <sub>LEAK</sub>				0.01	0.5	
Enable Threshold Voltage	V <sub>IH</sub>	EN Rising				2	V
	V <sub>IL</sub>	EN Falling			0.6		
EN Input Current	I <sub>EN</sub>	V <sub>EN</sub> = 5V			10	100	nA
Line Regulation	ΔLINE	I <sub>LOAD</sub> =30mA, 1.5V ≤ V <sub>IN</sub> ≤ 5.5V or (V <sub>OUT</sub> + 0.2V) ≤ V <sub>IN</sub> ≤ 5.5V			0.2		%
Load Regulation	ΔLOAD	10mA ≤ I <sub>LOAD</sub> ≤ 0.3A			0.2		%
Output Current Limit	I <sub>LIM</sub>	V <sub>OUT</sub> =0		600	1100		mA
Power Supply Rejection Ratio (I <sub>LOAD</sub> =5mA)	PSRR	V <sub>OUT</sub> =1.2V, V <sub>IN</sub> = 2V	f = 100Hz	--	80	--	dB
			f = 1kHz	--	75	--	
Output Voltage Noise (BW = 10Hz to 100kHz, C <sub>OUT</sub> =1μF,)	Noise	V <sub>IN</sub> = 3.5V I <sub>LOAD</sub> =0.1A	V <sub>OUT</sub> =0.9V	--	40	--	μV <sub>RMS</sub>
			V <sub>OUT</sub> =2.8V	--	50	--	
Thermal Shutdown Temperature	T <sub>SD</sub>	I <sub>LOAD</sub> =10mA		--	155	--	°C
Thermal Shutdown Hysteresis	ΔT <sub>SD</sub>			--	15	--	°C
Discharge Resistance		EN = 0V , V <sub>OUT</sub> = 0.1V		--	100	--	Ω

## Typical Characteristics



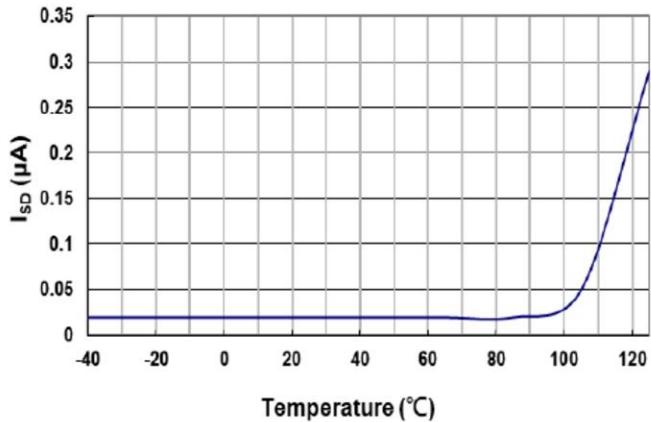


Fig. 11 Shutdown Ground Current vs. Temperature

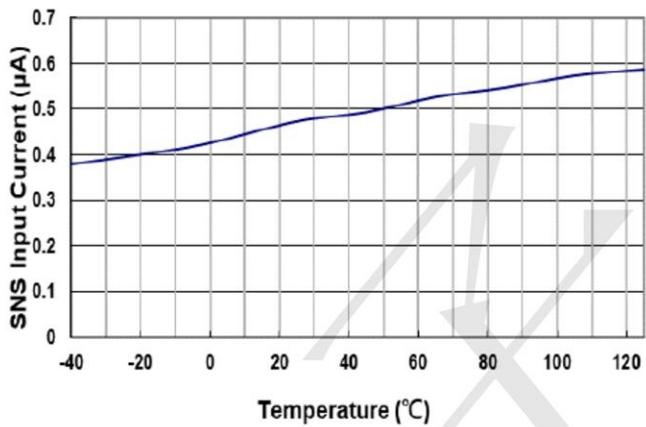


Fig. 12 SNS Input Current vs. Temperature

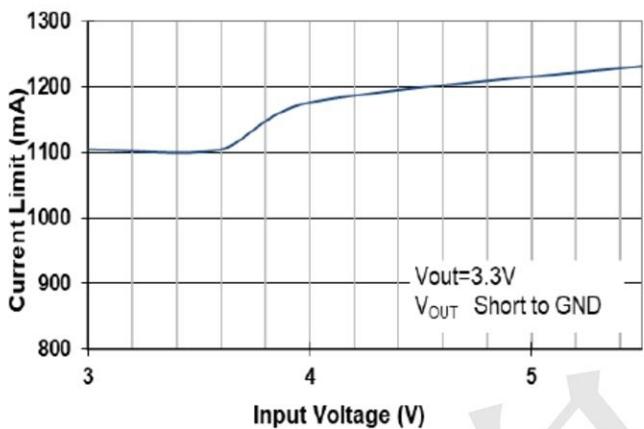


Fig. 13 Current Limit vs. Input Voltage

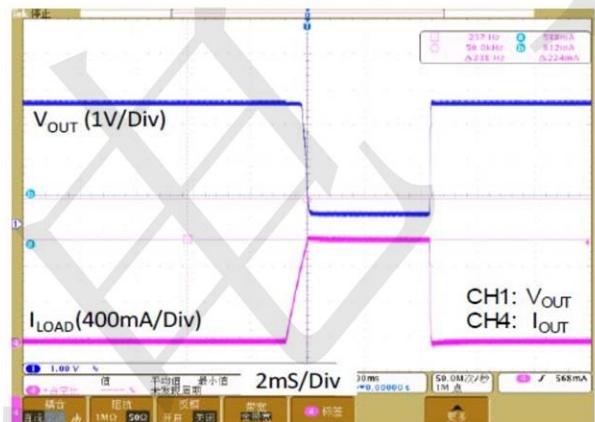


Fig. 14 Current Limit Response

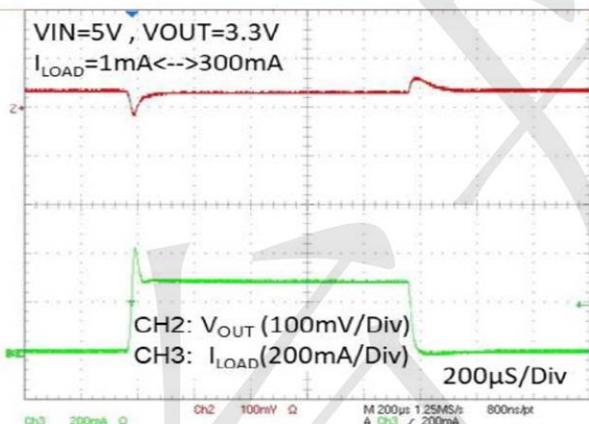


Fig. 15 Load Transient Response

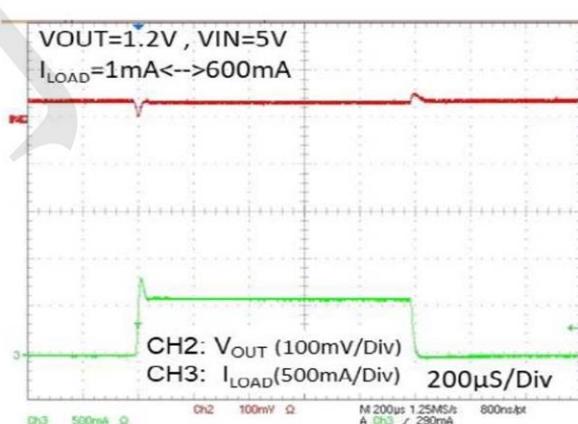
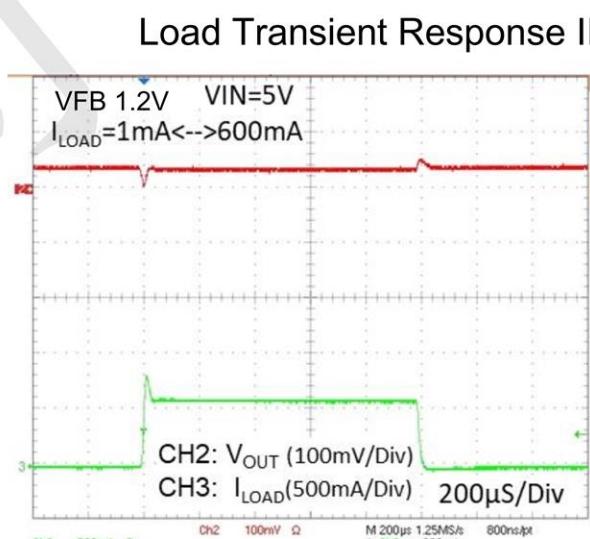
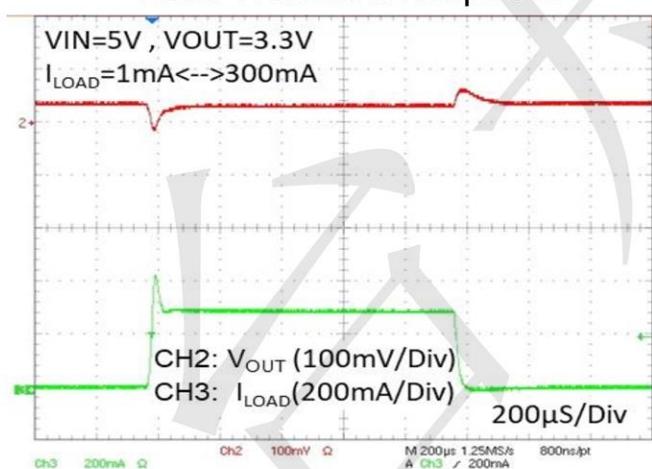
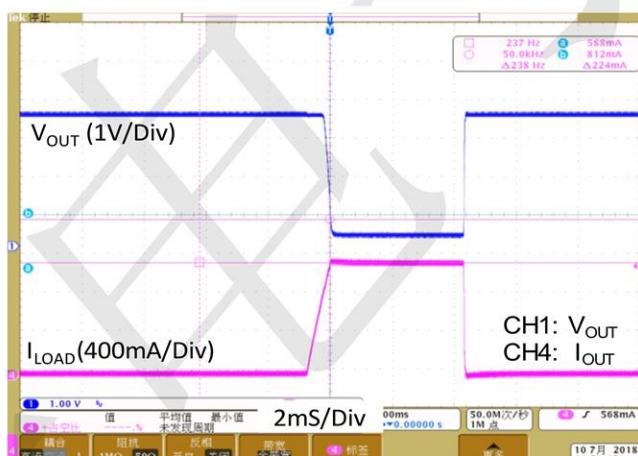
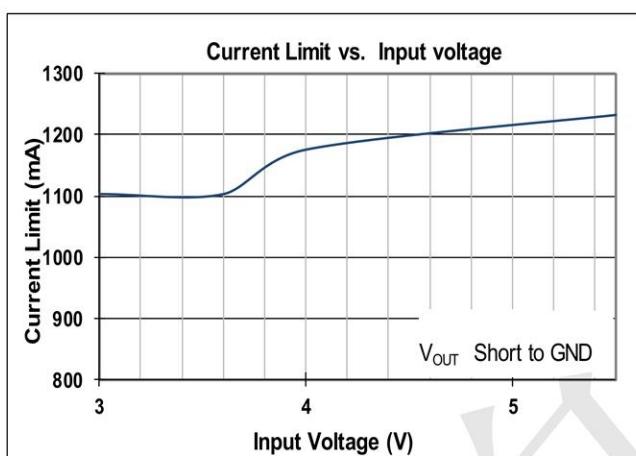
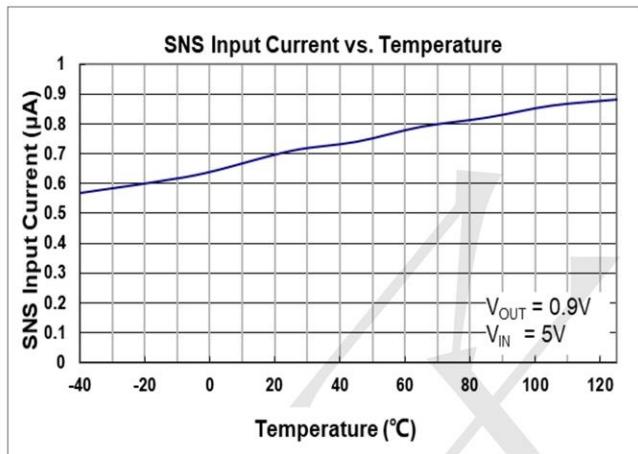
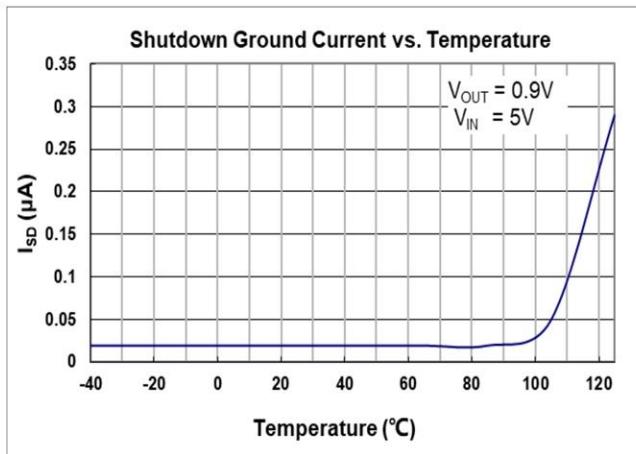
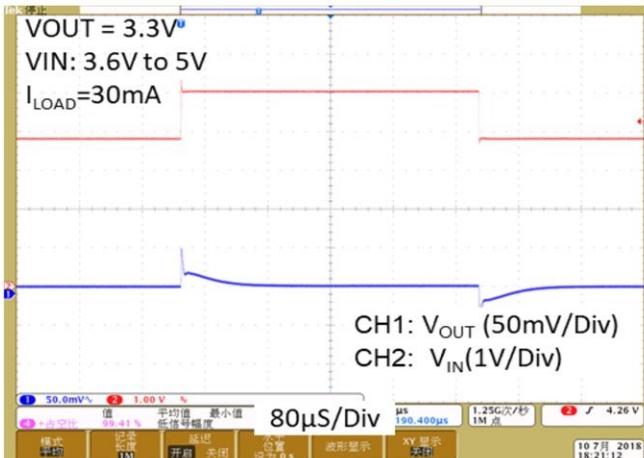


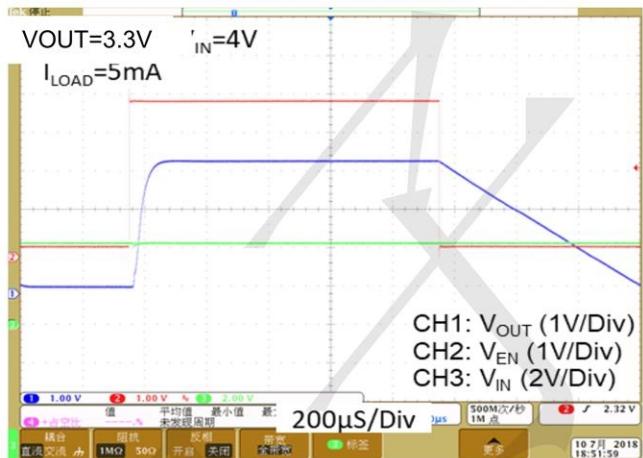
Fig. 16 Load Transient Response



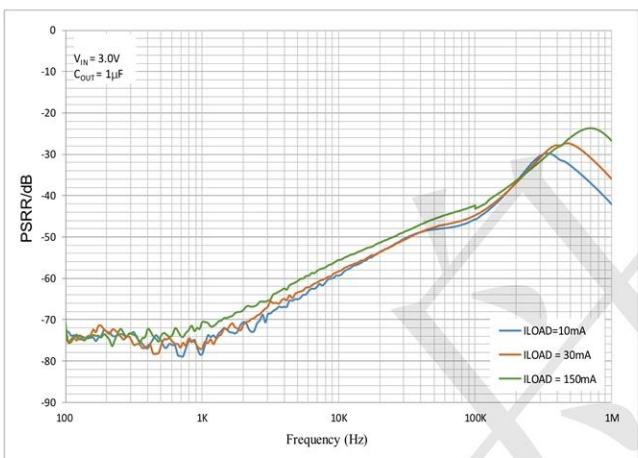
### Line Transient Response



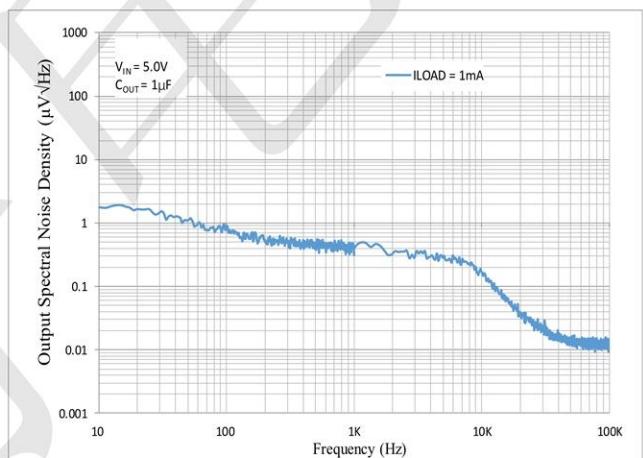
### V<sub>OUT</sub> Turn On/Off by EN



### PSRR vs. Frequency



### Noise Density Spectrum





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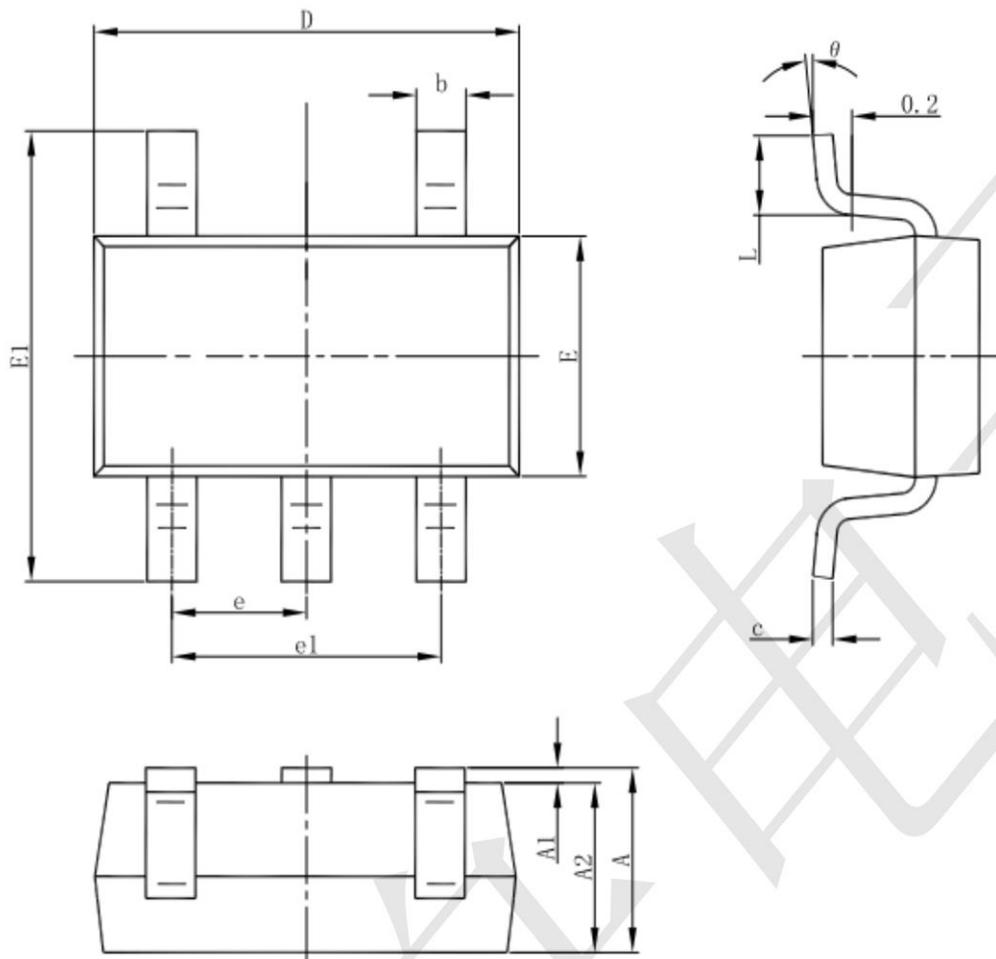
**TP172CADJS5**

2uA 600mA Ultra-LowDropout Regulators

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### Package information

SOT23-5



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
$\theta$	0°	8°	0°	8°

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