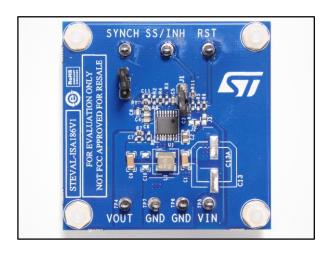


STEVAL-ISA186V1

38 V, 0.5 A synchronous step-down switching regulator evaluation board based on A6985F5V

Data brief



Features

- AECQ100 qualification
- 0.5 A DC output current
- 4 V to 38 V operating input voltage
- Low consumption mode or low noise mode
- Programmable Iskip current
- 45 μA I_Q at light load (LCM V_{IN} = 12 V)
- 8 µA IQ-SHTDWN
- Adjustable f sw (250 kHz 2 MHz)
- Fixed output voltage Vout = 5 V
- Embedded output voltage supervisor
- Synchronization
- Adjustable soft-start time
- Internal current limiting
- Overvoltage protection
- Output voltage sequencing
- Peak current mode architecture
 R_{DS(on)HS} = 360 mΩ; R_{DS(on)LS} = 150 mΩ
- Thermal shutdown
- RoHS compliant

Description

The STEVAL-ISA186V1 is a product evaluation board based on the A6985F5V ST synchronous step-down switching regulator, which can deliver up to 0.5 A and, with its 100% duty cycle ability to withstand cold crank events and wide input operating voltage range, renders the A6985F5V the ideal choice for battery-powered automotive systems. Synchronous rectification helps achieve higher efficiency at full load as well as application compactness, while high-frequency switching (programmable up to 2 MHz) helps to reduce the cost and size of power passive components while remaining outside the AM band. The device can operate in low consumption mode (LCM) with a quiescent current of 45 µA, hence ensuring the high efficiency under light load condition required in typical car body applications that are active when the car is parked. A low noise mode (LNM) can be selected to meet the requirements of infotainment applications with forced PWM mode under all load conditions. The default board configuration is LCM active, 2 MHz switching frequency, high ISKIP current and the switchover feature enabled, but all of these settings can be easily changed so the user can evaluate different application scenarios.

Schematic diagram STEVAL-ISA186V1

1 Schematic diagram

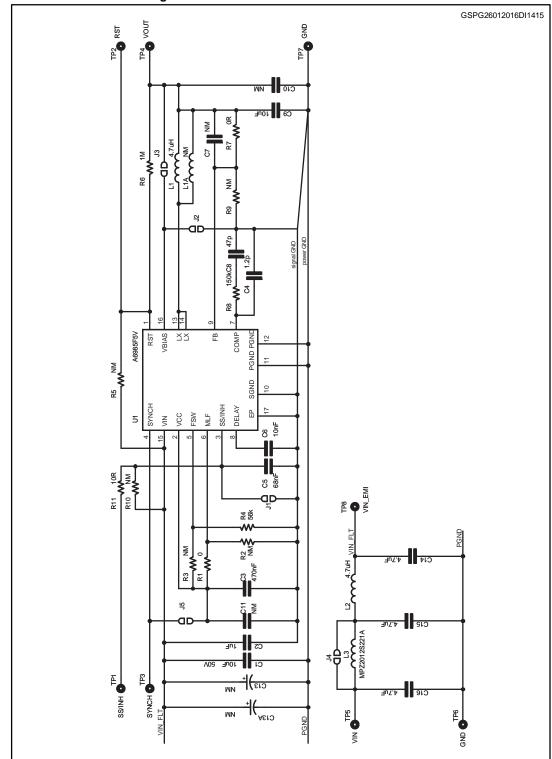


Figure 1: STEVAL-ISA186V1 schematic circuit

STEVAL-ISA186V1 Revision history

2 Revision history

Table 1: Document revision history

Date	Version	Changes
26-Jan-2016	1	Initial release.

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