

DATA SHEET

SKY12340-364LF: 300 kHz – 2.0 GHz Five-Bit Digital Attenuator with Serial-to-Parallel Driver

Applications

- Cellular infrastructure
- Wireless receivers

Features

- Single, +5 V DC supply voltage
- Integrated silicon serial-to-parallel driver
- Attenuation: 0.5 dB steps to 15.5 dB
- Small, QFN (32-pin, 5 x 5 mm) Pb-free package (MSL1, 260 °C per JEDEC J-STD-020)

NEW



Skyworks Green™ products are RoHS (Restriction of Hazardous Substances)-compliant, conform to the EIA/EICTA/JEITA Joint Industry Guide (JIG) Level A guidelines, are halogen free according to IEC-61249-2-21, and contain <1,000 ppm antimony trioxide in polymeric materials.

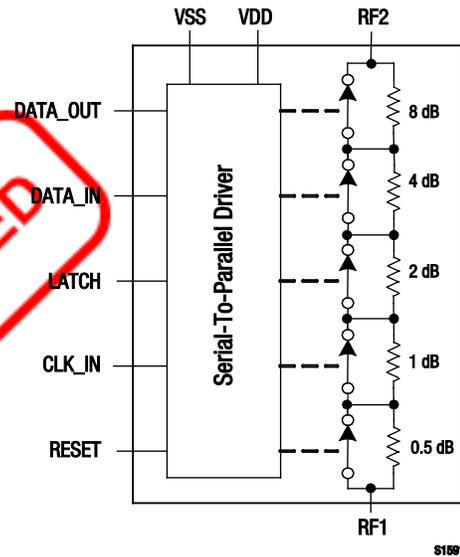


Figure 1. SKY12340-364LF Block Diagram

Description

The SKY12340-364LF is a GaAs FET five-bit digital attenuator I/C with a serial-to-parallel driver. The device is provided in a 5 x 5 mm, 32-pin Quad Flat No-Lead (QFN) package.

The SKY12340-364LF is particularly suited at IF frequencies for which high attenuation accuracy, low insertion loss, and low intermodulation products are required.

A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

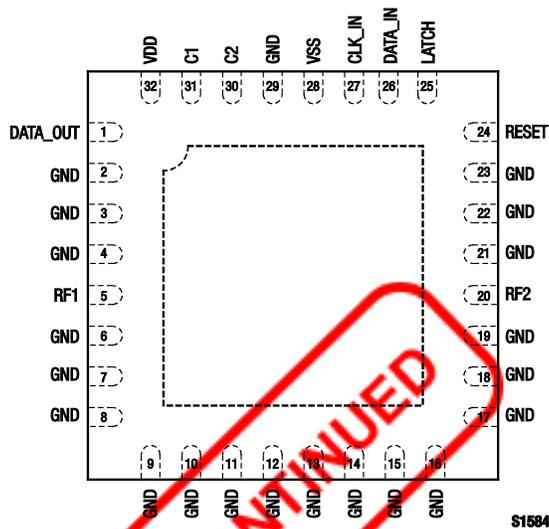


Figure 2. SKY12340-364LF Pinout – 32-Pin QFN (Top View)

Table 1. SKY12340-364LF Signal Descriptions

| Pin # | Name | Description | Pin # | Name | Description |
|-------|----------|--------------------|-------|---------|--------------------------|
| 1 | DATA_OUT | Serial data output | 17 | GND | Ground |
| 2 | GND | Ground | 18 | GND | Ground |
| 3 | GND | Ground | 19 | GND | Ground |
| 4 | GND | Ground | 20 | RF2 | RF input/output |
| 5 | RF1 | RF input/output | 21 | GND | Ground |
| 6 | GND | Ground | 22 | GND | Ground |
| 7 | GND | Ground | 23 | GND | Ground |
| 8 | GND | Ground | 24 | RESET | Reset |
| 9 | GND | Ground | 25 | LATCH | Latch enable |
| 10 | GND | Ground | 26 | DATA_IN | Serial data input |
| 11 | GND | Ground | 27 | CLK_IN | Serial clock input |
| 12 | GND | Ground | 28 | VSS | Low output voltage level |
| 13 | GND | Ground | 29 | GND | Ground |
| 14 | GND | Ground | 30 | C2 | Charge pump capacitor 2 |
| 15 | GND | Ground | 31 | C1 | Charge pump capacitor 1 |
| 16 | GND | Ground | 32 | VDD | Supply voltage |

Functional Description

The SKY12340-364LF is a five bit digital attenuator comprised of a GaAs attenuator and a silicon CMOS driver. The attenuation setting is controlled by a serial-to-parallel interface. Attenuation is set by a stream of data that is clocked into the shift registers of the silicon chip by the clock signal.

To set the attenuation state, a latch signal is sent to the appropriate pin to send the correct bias voltages to the GaAs attenuator. The silicon chip generates a negative voltage using a negative voltage generator that requires two external components: a charge pump capacitor connected between pins 30 (C2) and 31 (C1) and a voltage hold capacitor connected from pin 28 (VSS) to ground.

More than one attenuator can be cascaded together and the data may be passed through one device to the other using the DATA_OUT signal (pin 1). To reset the attenuator to the insertion loss state, a logic low signal may be sent to the reset pin. DC bias voltage to the silicon CMOS chip is applied to pin 32 (VDD).

Power-Up/Power-Down Timing

Serial data (SDA) is shifted into the register on the rising edge of the clock (SCK), least significant bit (LSB) first. The attenuator

changes states on the rising edge of the latch-enable (LE) signal, according to the most recent five bits of shifted data accepted since the previous falling edge of the LE signal. Refer to the timing diagram in Figure 3 and timing parameter specifications in Table 2.

Power-up sequence is as follows:

0. Connect ground
1. Apply VDD
2. Set all inputs (SCK, SDA, LE)

The power-down sequence is the reverse of above.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY12340-364LF are provided in Table 3. Electrical specifications are provided in Tables 4 and 5.

Typical performance characteristics of the SKY12340-364LF are illustrated in Figures 4 through 10.

The state of the SKY12340-364LF is determined by the logic provided in Table 6.

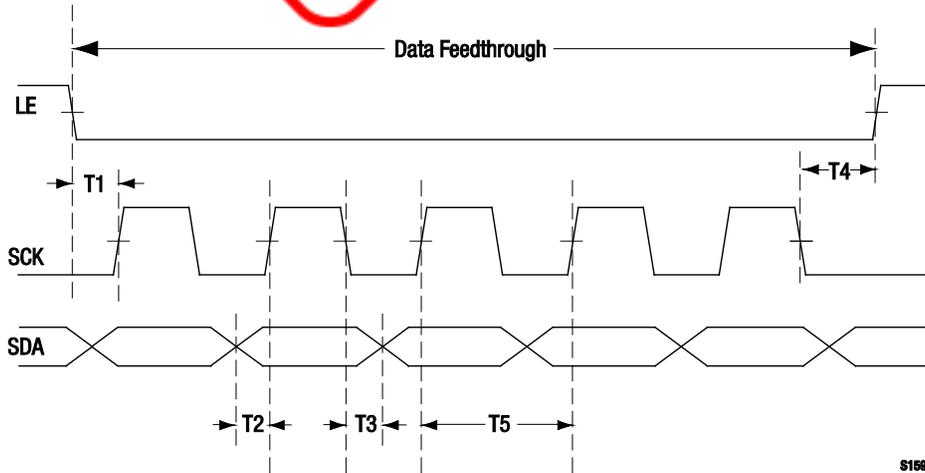


Figure 3. Power-Up/Power-Down Timing

Table 2. Power-Up/Power-Down Timing Parameters

| Parameter | Symbol | Minimum | Typical | Maximum | Units |
|-----------------|------------------|---------|--------------------|---------|-------|
| LE setup time | T1 | 7.5 | 15 | | ns |
| SDA setup time | T2 | 7.5 | 15 | | ns |
| SDA hold time | T3 | 5 | 10 | | ns |
| LE hold time | T4 | 5 | 10 | | ns |
| Clock frequency | f _{CLK} | | 16 | 100 | MHz |
| Clock period | T5 | | 1/f _{CLK} | | |

Table 3. SKY12340-364LF Absolute Maximum Ratings

| Parameter | Symbol | Minimum | Typical | Maximum | Units |
|-----------------------|------------------|------------------------|---------|---------|-------|
| Supply voltage | V _{DD} | -0.5 | | +6.0 | V |
| Input voltage | V _I | -0.5 – V _{DD} | | +0.5 | V |
| RF input power | P _{IN} | | | +25 | dBm |
| Storage temperature | T _{STG} | -65 | | +150 | °C |
| Operating temperature | T _{OP} | -40 | | +85 | °C |

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Table 4. SKY12340-364LF Electrical Specifications (Note 1)
(T_{OP} = +25 °C, Characteristic Impedance [Z₀] = 50 Ω, V_{DD} = 5 V, Unless Otherwise Noted)

| Parameter | Symbol | Test Condition | Min | Typical | Max | Units |
|---------------------------------------|----------------------------------|--|---|------------|-----|------------|
| Insertion loss (Note 2) | IL | 300 kHz to 1.0 GHz | | 1.4 | 1.5 | dB |
| | | 300 kHz to 2.0 GHz | | 1.8 | 2.0 | dB |
| Attenuation range (Note 3) | | | | 15.5 | | dB |
| Attenuation accuracy (Note 3) | | 300 kHz to 2.0 GHz | ±(0.35 + 2% of attenuation setting in dB) | | | |
| Return loss | IS11I | 300 kHz to 2.0 GHz | 12 | 20 | | dB |
| Switching characteristics: | | | | | | |
| Rise, fall | | 10/90% or 90/10% RF | | 50 | | ns |
| On, off | | 50% control to 90/10% RF | | 100 | | ns |
| Video feedthrough | | T _{RISE} = 1 ns, bandwidth = 500 MHz | | 50 | | mV |
| 1 dB input compression point | IP1dB | Insertion loss state | | | | |
| | | 0.5 to 2.0 GHz 0.05 GHz | +27 +26 | +30 +30 | | dBm dBm |
| 3 rd Order Intercept Point | IP3 | 1 MHz spacing, P _{IN} = +5 dBm/tone | | | | |
| | | Insertion loss state: | | | | |
| | | 0.05 GHz | +43 | +45 | | dBm |
| | | 0.5 to 2.0 GHz | +47 | +50 | | dBm |
| Attenuation states: | | | | | | |
| 0.05 GHz | +41 | +45 | | dBm | | |
| 0.5 to 2.0 GHz | +47 | +50 | | dBm | | |
| Input voltage: | V _H V _L | | | | | |
| | | | | | | |
| Low | | | 0 | 0.3 | 0.6 | V |
| Input leakage current | I _L | | | ±0.5 | | μA |
| Quiescent current | I _{DD} | | | 500 | | μA |
| Supply voltage | V _{DD} | | | 5 | | V |

Note 1: Performance is guaranteed only under the conditions listed in this Table.

Note 2: Insertion loss changes by 0.003 dB/°C.

Note 3: Attenuation value referenced above insertion loss. Exposed pad must be connected to RF ground to obtain specified attenuation.

Typical Performance Characteristics

($T_{OP} = +25\text{ }^{\circ}\text{C}$, Characteristic Impedance [Z_0] = $50\ \Omega$, $V_{DD} = 5\text{ V}$, Unless Otherwise Noted)

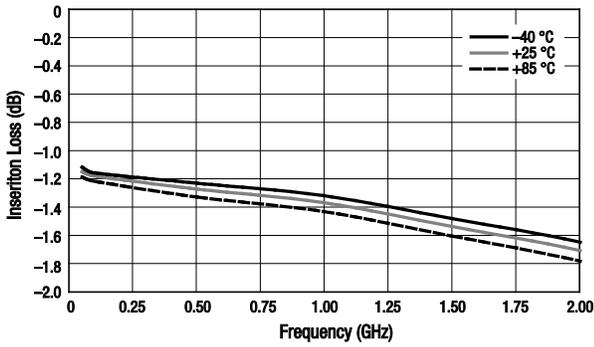


Figure 4. Insertion Loss vs Frequency

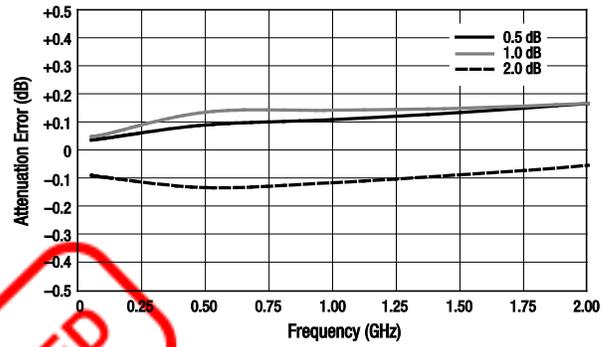


Figure 5. Attenuation Error vs Frequency (0.5, 1.0, and 2.0 dB States)

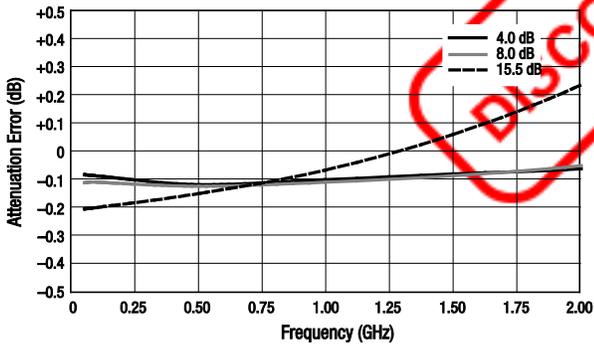


Figure 6. Attenuation Error vs Frequency (4.0, 8.0, and 15.5 dB States)

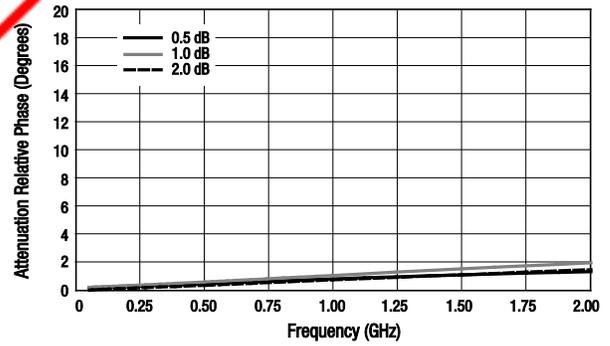


Figure 7. Attenuation Relative Phase vs Frequency, Normalized to Insertion Loss (0.5, 1.0, and 2.0 dB States)

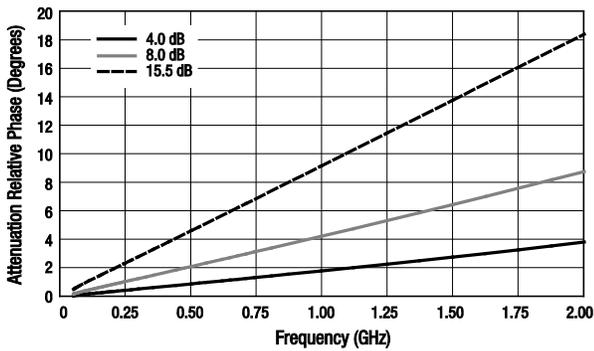


Figure 8. Attenuation Relative Phase vs Frequency, Normalized to Insertion Loss (4.0, 8.0, and 15.5 dB States)

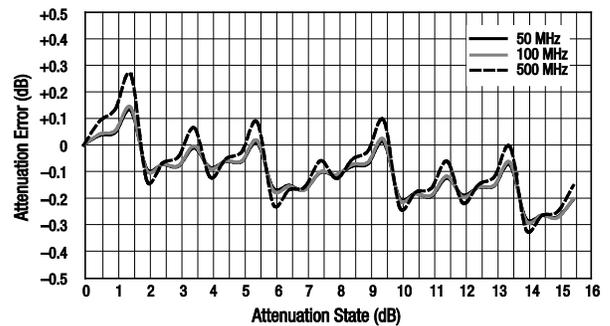


Figure 9. Attenuation Error vs Attenuation State (@ 50, 100, and 500 MHz)

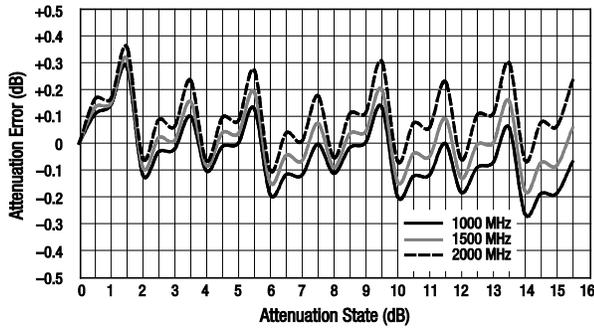


Figure 10. Attenuation Error vs Attenuation State (@ 1000, 1500, and 2000 MHz)

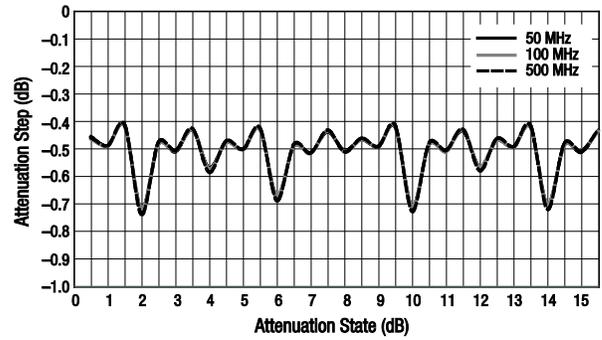


Figure 11. Attenuation Step vs Attenuation State (@ 50, 100, and 500 MHz)

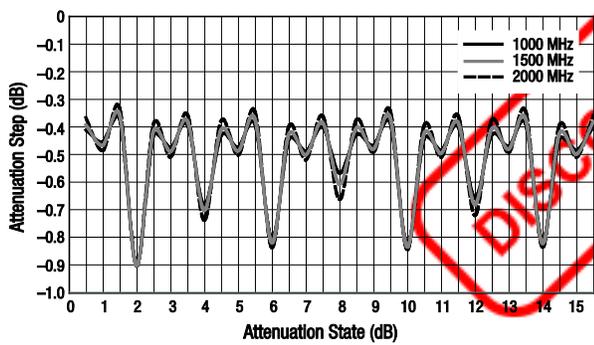


Figure 12. Attenuation Step vs Attenuation State (@ 1000, 1500, and 2000 MHz)

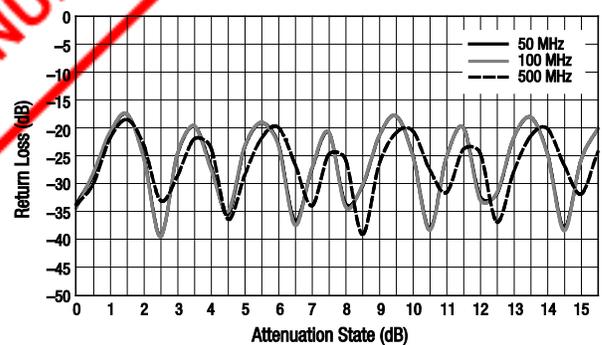


Figure 13. Return Loss vs Attenuation State (@ 50, 100, and 500 MHz)

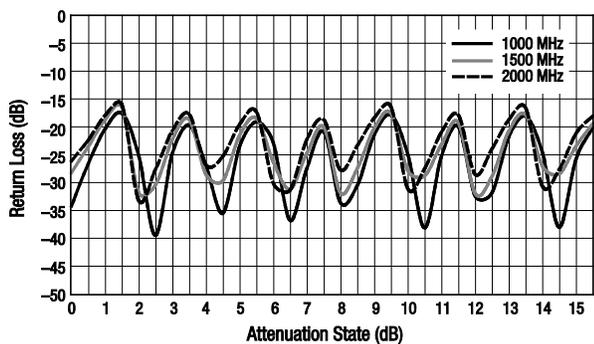


Figure 14. Return Loss vs Attenuation State (@ 1000, 1500, and 2000 MHz)

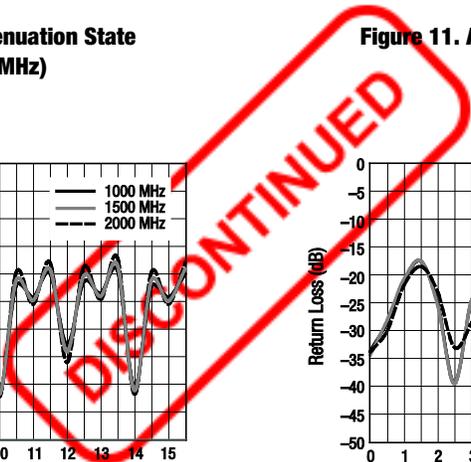


Table 6. SKY12340-364LF Truth Table

| J1 to J2 Attenuation | Reset | Serial Data Input | | | | |
|----------------------|-------|-------------------|--------|--------|--------|--------|
| | | 0.5 dB | 1.0 dB | 2.0 dB | 4.0 dB | 8.0 dB |
| Insertion loss | 1 | 0 | 0 | 0 | 0 | 0 |
| 0.5 dB | 1 | 1 | 0 | 0 | 0 | 0 |
| 1.0 dB | 1 | 0 | 1 | 0 | 0 | 0 |
| 2.0 dB | 1 | 0 | 0 | 1 | 0 | 0 |
| 4.0 dB | 1 | 0 | 0 | 0 | 1 | 0 |
| 8.0 dB | 1 | 0 | 0 | 0 | 0 | 1 |
| 15.5 dB | 1 | 1 | 1 | 1 | 1 | 1 |
| Insertion loss | 0 | X | X | X | X | X |

Note: "1" = V_H = +1.7 V to +5 V. "0" = V_L = 0 to +0.6 V. "X" = don't care. This Table shows the logic required for the major bits and full attenuation. Bit states need to be used in combination to set the sum of the bits selected.

Evaluation Board Description

The SKY12340-364LF Evaluation Board is used to test the performance of the SKY12340-364LF digital attenuator. An assembly drawing for the Evaluation Board is shown in Figure 15 and an Evaluation Board schematic diagram is shown in Figure 16. Table 7 provides the Evaluation Board Bill of Materials (BOM) list.

The Evaluation Board includes the required charge pump capacitor between pins 30 and 31. It also includes the negative voltage holding capacitor required from pin 28 to ground.

A capacitor is added to pin 31 to ground any noise on the DC supply line.

Package Dimensions

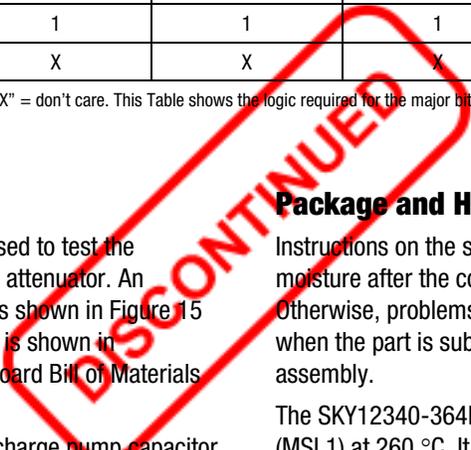
The PCB layout footprint for the SKY12340-364LF is shown in Figure 17. Typical case markings are noted in Figure 18. Package dimensions for the 32-pin QFN are shown in Figure 19, and tape and reel dimensions are provided in Figure 20.

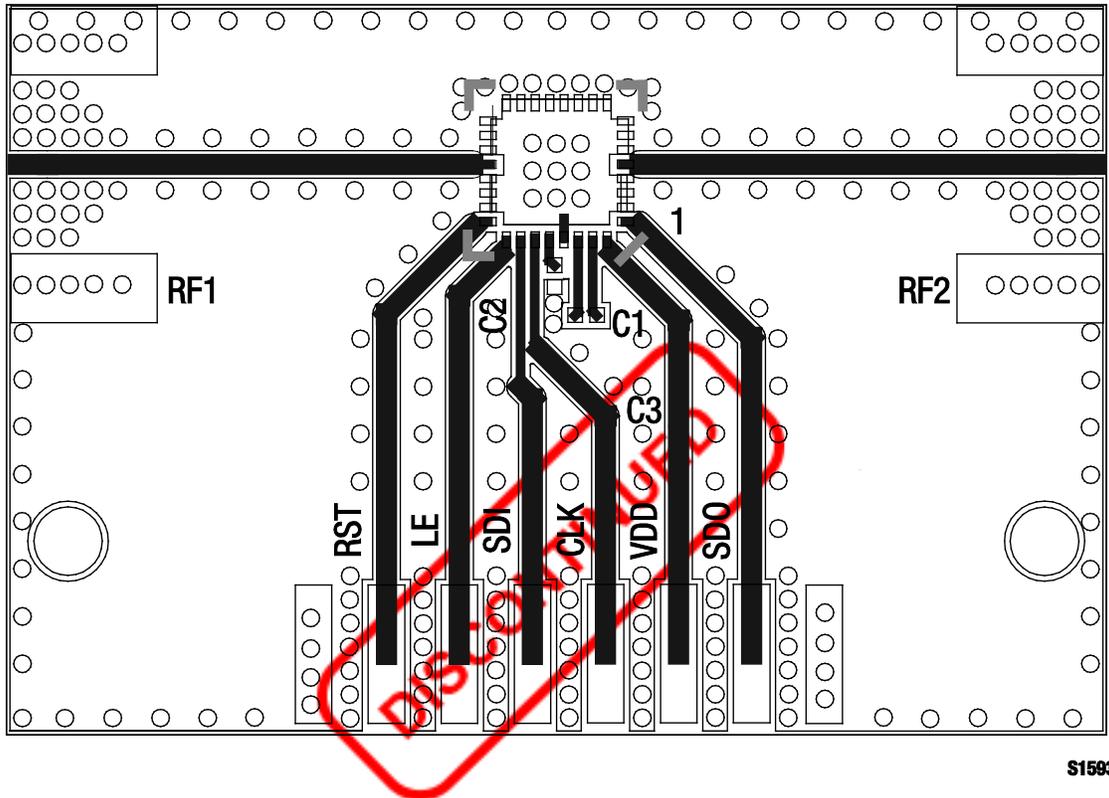
Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY12340-364LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

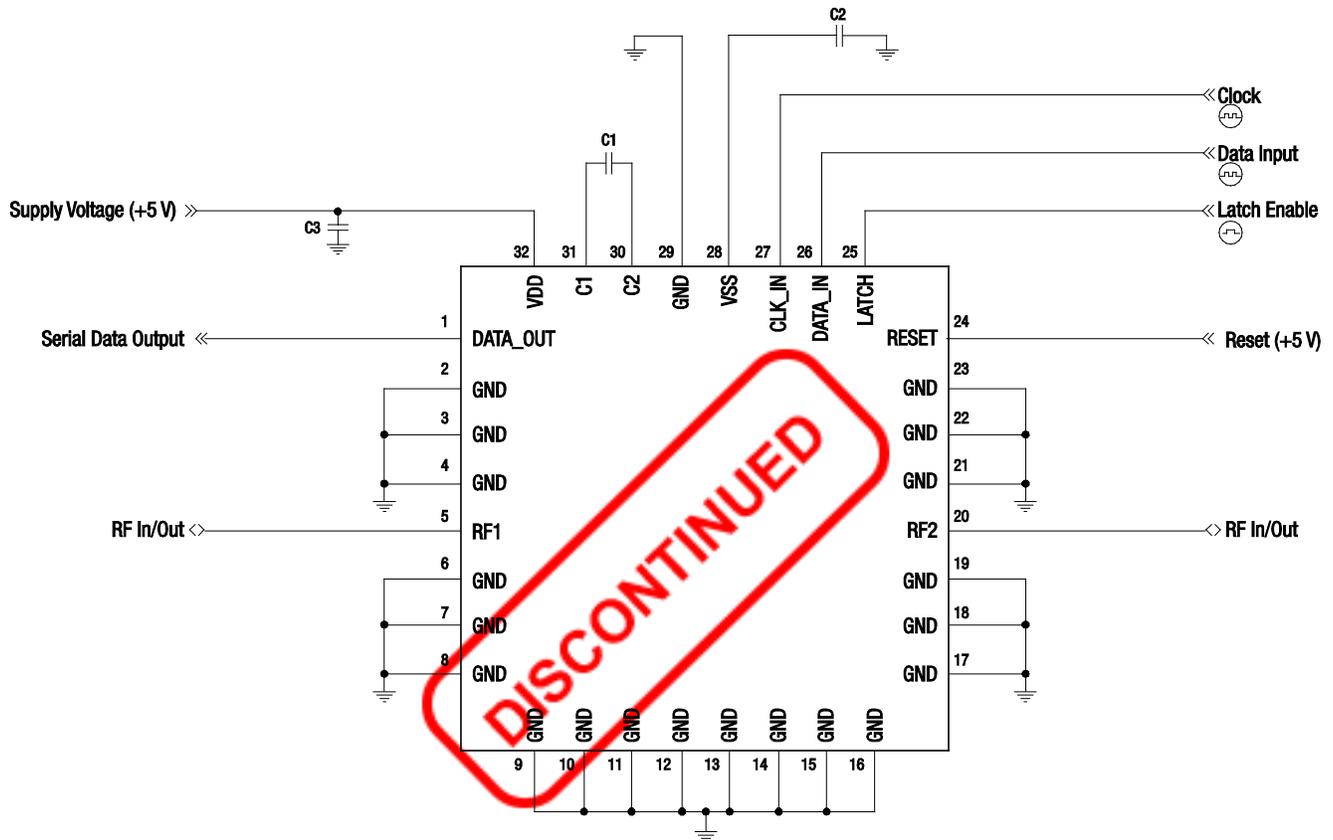
Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.





S1593

Figure 15. SKY12340-364LF Evaluation Board Assembly Diagram



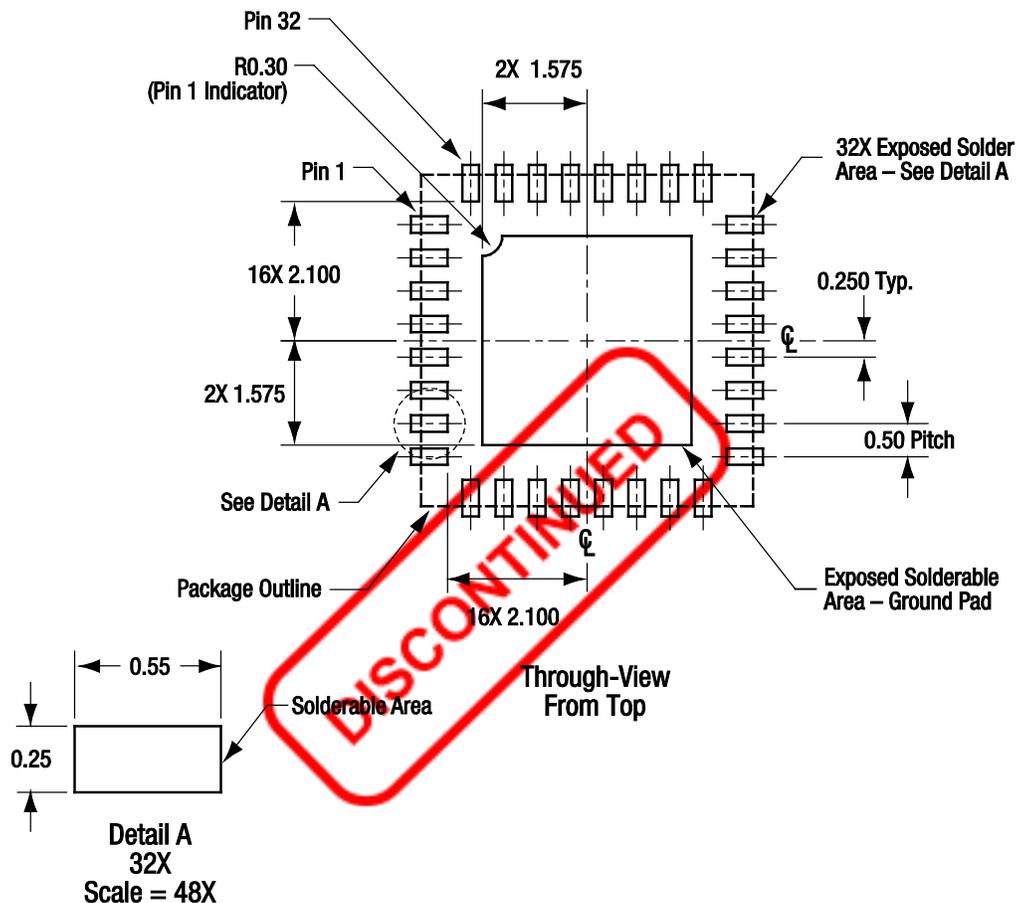
NOTE: Exposed bottom pad must be grounded

S1592

Figure 16. SKY12340-364LF Evaluation Board Schematic Diagram

Table 7. Recommended Evaluation Board Bill of Materials

| Component | Value | Size | Manufacturer |
|-----------|---------------|------|--------------|
| C1 | 0.025 μ F | 0402 | Murata |
| C2 | 0.01 μ F | 0402 | Murata |
| C3 | 0.01 μ F | 0402 | Murata |



All measurements are in millimeters

S1608

Figure 17. SKY12340-364LF PCB Layout Footprint

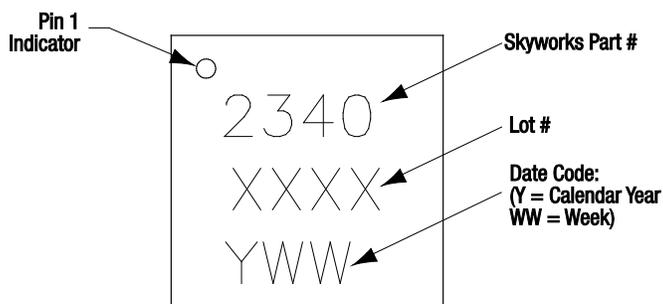
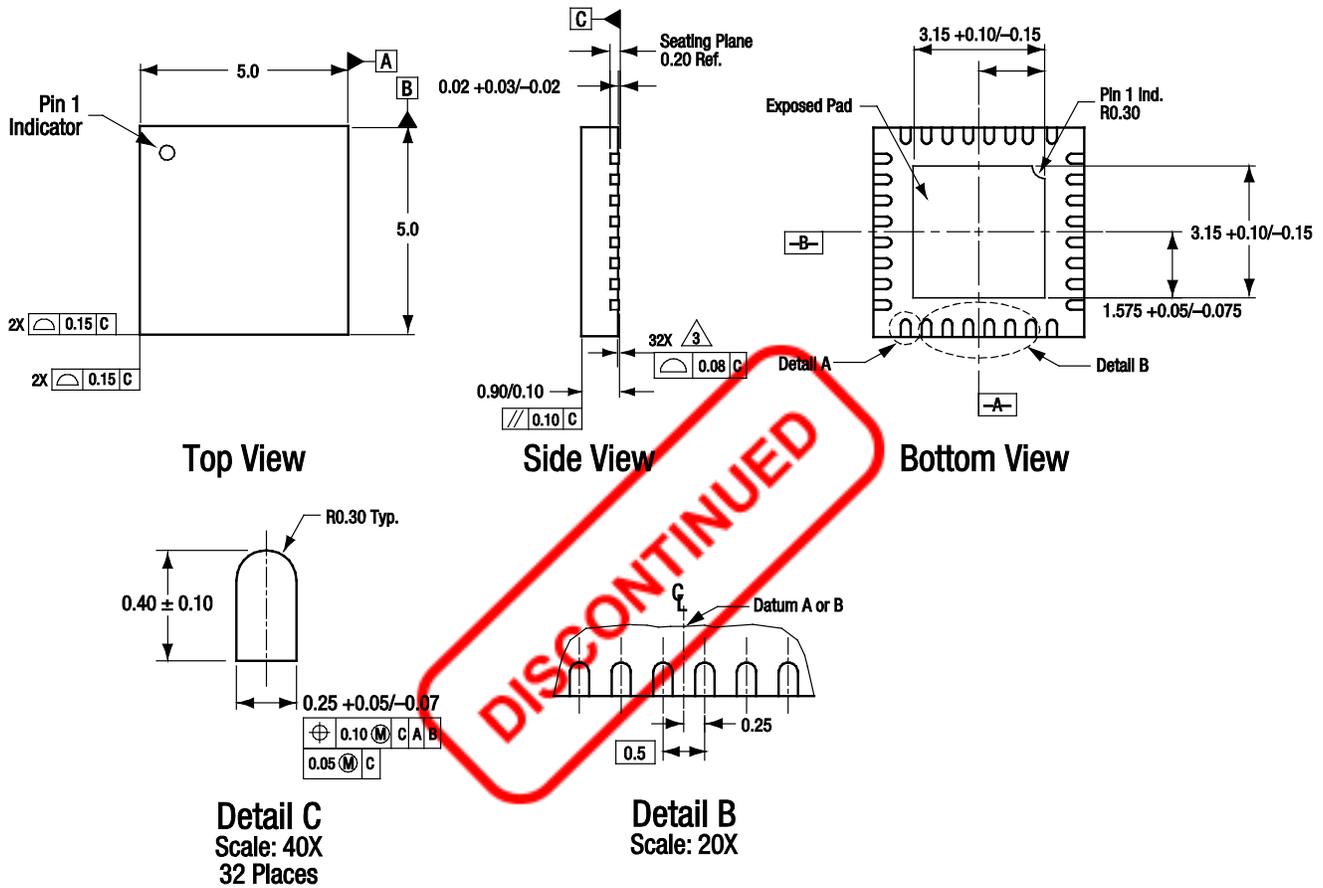


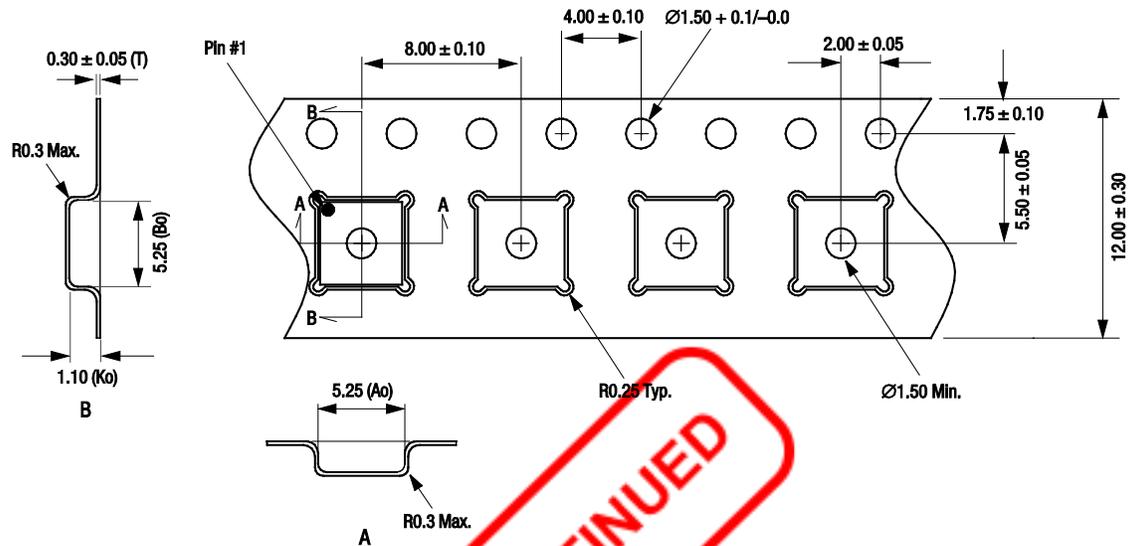
Figure 18. Typical Part Markings



All measurements are in millimeters.
Dimensioning and tolerancing according to ASME Y14.5M-1994.
Coplanarity applies to the exposed heat sink slug as well as the terminals.
Plating requirement per source control drawing (SCD) 2504.

S1583

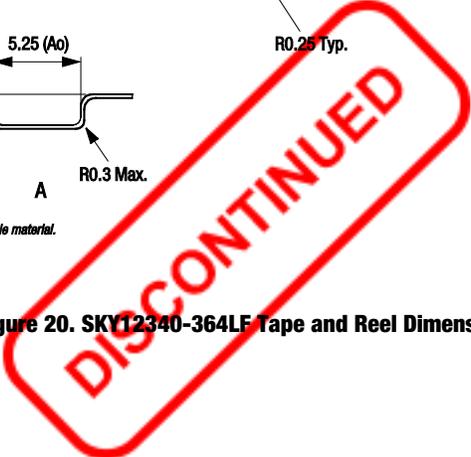
Figure 19. SKY12340-364LF 32-Pin QFN Package Dimensions



- Notes:
1. Carrier tape: black conductive polystyrene, non-bakeable material.
 2. Cover tape material: transparent conductive HSA.
 3. Cover tape size: 9.20 mm width.
 4. All measurements are in millimeters.

S1602

Figure 20. SKY12340-364LF Tape and Reel Dimensions



Ordering Information

| Model Name | Manufacturing Part Number | Evaluation Board Part Number |
|-----------------------------------|---------------------------|------------------------------|
| SKY12340-364LF Digital Attenuator | SKY12340-364LF | SKY12340-364LF-EVB |



Copyright © 2009, 2010, 2011 Skyworks Solutions, Inc. All Rights Reserved.

Information in this document is provided in connection with Skyworks Solutions, Inc. ("Skyworks") products or services. These materials, including the information contained herein, are provided by Skyworks as a service to its customers and may be used for informational purposes only by the customer. Skyworks assumes no responsibility for errors or omissions in these materials or the information contained herein. Skyworks may change its documentation, products, services, specifications or product descriptions at any time, without notice. Skyworks makes no commitment to update the materials or information and shall have no responsibility whatsoever for conflicts, incompatibilities, or other difficulties arising from any future changes.

No license, whether express, implied, by estoppel or otherwise, is granted to any intellectual property rights by this document. Skyworks assumes no liability for any materials, products or information provided hereunder, including the sale, distribution, reproduction or use of Skyworks products, information or materials, except as may be provided in Skyworks Terms and Conditions of Sale.

THE MATERIALS, PRODUCTS AND INFORMATION ARE PROVIDED "AS IS" WITHOUT WARRANTY OF ANY KIND, WHETHER EXPRESS, IMPLIED, STATUTORY, OR OTHERWISE, INCLUDING FITNESS FOR A PARTICULAR PURPOSE OR USE, MERCHANTABILITY, PERFORMANCE, QUALITY OR NON-INFRINGEMENT OF ANY INTELLECTUAL PROPERTY RIGHT; ALL SUCH WARRANTIES ARE HEREBY EXPRESSLY DISCLAIMED. SKYWORKS DOES NOT WARRANT THE ACCURACY OR COMPLETENESS OF THE INFORMATION, TEXT, GRAPHICS OR OTHER ITEMS CONTAINED WITHIN THESE MATERIALS. SKYWORKS SHALL NOT BE LIABLE FOR ANY DAMAGES, INCLUDING BUT NOT LIMITED TO ANY SPECIAL, INDIRECT, INCIDENTAL, STATUTORY, OR CONSEQUENTIAL DAMAGES, INCLUDING WITHOUT LIMITATION, LOST REVENUES OR LOST PROFITS THAT MAY RESULT FROM THE USE OF THE MATERIALS OR INFORMATION, WHETHER OR NOT THE RECIPIENT OF MATERIALS HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

Skyworks products are not intended for use in medical, lifesaving or life-sustaining applications, or other equipment in which the failure of the Skyworks products could lead to personal injury, death, physical or environmental damage. Skyworks customers using or selling Skyworks products for use in such applications do so at their own risk and agree to fully indemnify Skyworks for any damages resulting from such improper use or sale.

Customers are responsible for their products and applications using Skyworks products, which may deviate from published specifications as a result of design defects, errors, or operation of products outside of published parameters or design specifications. Customers should include design and operating safeguards to minimize these and other risks. Skyworks assumes no liability for applications assistance, customer product design, or damage to any equipment resulting from the use of Skyworks products outside of stated published specifications or parameters.

Skyworks, the Skyworks symbol, and "Breakthrough Simplicity" are trademarks or registered trademarks of Skyworks Solutions, Inc., in the United States and other countries. Third-party brands and names are for identification purposes only, and are the property of their respective owners. Additional information, including relevant terms and conditions, posted at www.skyworksinc.com, are incorporated by reference.

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [Attenuators](#) category:

Click to view products by [Skyworks](#) manufacturer:

Other Similar products are found below :

[ATN3580-01](#) [HMC305SLP4ETR](#) [MAAD-009195-000100](#) [TGL4201-00](#) [TGL4201-02](#) [TGL4201-03](#) [TGL4201-06](#) [TGL4201-10](#) [ATN3590-15](#) [20-50TPC](#) [D10AA5Z4](#) [18AH-01](#) [18AH-08](#) [ATN3580-20](#) [ATN3590-09](#) [20-50RP](#) [MASW-008322-000000](#) [MAATCC0009TR](#) [PCAF-10](#) [EXB-24AT9AR5X](#) [ATN3580-10](#) [HMC539ALP3ETR](#) [ATN3580-02](#) [WA04P006XCTL](#) [SKY12408-321LF](#) [ATN3580-03](#) [WA04P005XBTL](#) [HMC-VVD104-SX](#) [WA04P007XCTL](#) [SKY12236-11](#) [MAATSS0018TR-3000](#) [HMC656-SX](#) [WA04P001XBTL](#) [MAAV-007941-TR3000](#) [WA04P004XBTL](#) [WA04P002XBTL](#) [HMC941A](#) [PAT0816-C-0DB-T5](#) [PAT0816-C-8DB-T5](#) [PAT1632-C-3DB-T1](#) [PAT1632-C-6DB-T1](#) [PAT1632-C-10DB-T1](#) [PAT1632-C-1DB-T1](#) [PAT0816-C-2DB-T5](#) [PAT0816-C-4DB-T5](#) [DHM510-0100-006](#) [CMD172](#) [MAT10300](#) [MAT10180](#) [MAATCC0007-TB](#)