

DATA SHEET

# SE5516A: Dual-Band 802.11a/b/g/n/ac WLAN Front-End Module

## Applications

- 802.11a/b/g/n/ac WLANs
- Computing, networking

## Features

- All RF ports matched to 50  $\Omega$
- Integrated 2.4 GHz PA, 5 GHz PA, transmit filter, transmit/receive switches, and diplexers
- Integrated power detector for each transmit chain
- Power:
  - +21 dBm, 802.11b, 11 Mbps, ACPR = +35 dBc
  - +18 dBm @ 3.0% EVM, 802.11n, 64 QAM, 2G
  - +16 dBm @ 3.0% EVM, 802.11n, 64 QAM, 5G
  - +16 dBm @ 1.8% EVM, 802.11ac, 256 QAM, 2G
  - +13 dBm @ 1.8% EVM, 802.11ac, 256 QAM, 5G
  - Single supply voltage: 3.3 V  $\pm$  10%
- Small LGA (24-pin, 4 x 4 mm) package (MSL3, 240  $^{\circ}$ C per JEDEC J-STD-020)

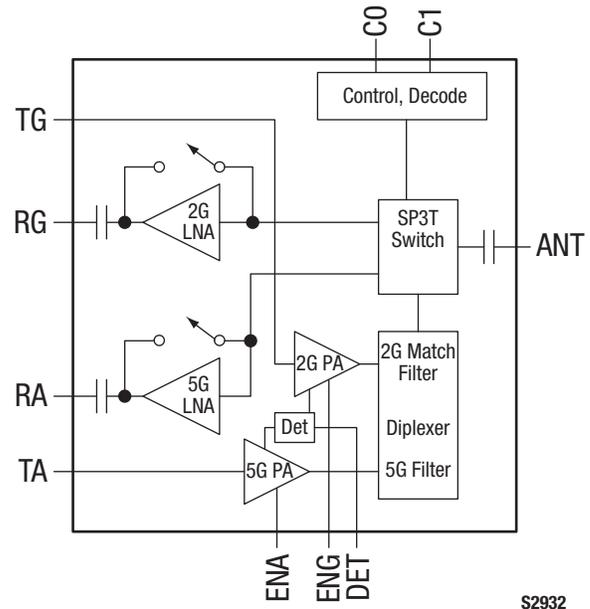


Figure 1. SE5516A Block Diagram



Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.

## Description

The SE5516A is a complete 802.11a/b/g/n/ac WLAN RF Front-End Module (FEM) that provides all the functionality of the Power Amplifiers (PAs), filtering, power detector, transmit/receive (T/R) switch, diplexers, and associated matching.

The device provides a complete 2.4 GHz and 5.0 GHz WLAN RF solution from the output of the transceiver to the antenna in an ultra-compact form factor. The SE5516A delivers < 2% EVM at rated AC power, which meets the stringent linearity conditions of the 802.11ac standard.

All RF ports are matched to 50  $\Omega$  to simplify the PCB layout and the interface to the transceiver. The device also includes a transmitter power detector with 20 dB of dynamic range for each transmit chain. Each PA has a separate digital enable control for transmitter on/off control. The power ramp rise/fall time is < 0.4  $\mu$ s.

The SE5516A also provides a notch filter from 3.260 to 3.267 GHz and 3.28 to 3.89 GHz before the input of each 2.4 GHz and 5.0 GHz PA, respectively.

The SE5516A is manufactured in a compact, 4 x 4 mm, 24-pin Land Grid Array (LGA) package.

A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

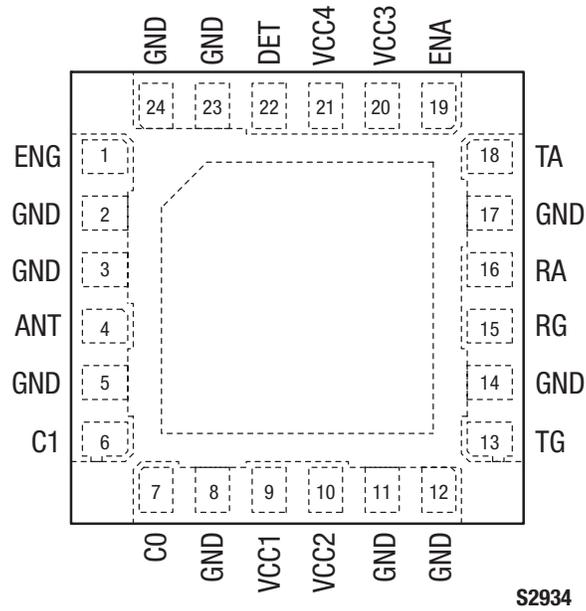


Figure 2. SE5516A Pinout (Top View)

Table 1. SE5516A Signal Descriptions

Pin	Name	Description	Pin	Name	Description
1	ENG	2.4 GHz PA enable	13	TG	2.0 GHz transmit RF input
2	GND	Ground	14	GND	Ground
3	GND	Ground	15	RG	2.0 GHz receive RF output
4	ANT	Antenna	16	RA	5.0 GHz receive RF output
5	GND	Ground	17	GND	Ground
6	C1	Switch control logic 1	18	TA	5.0 GHz transmit RF input
7	C0	Switch control logic 0	19	ENA	5.0 GHz PA enable
8	GND	Ground	20	VCC3	Supply voltage
9	VCC1	Supply voltage	21	VCC4	Supply voltage
10	VCC2	Supply voltage	22	DET	2.0/5.0 GHz power detector output
11	GND	Ground	23	GND	Ground
12	GND	Ground	24	GND	Ground

## Electrical and Mechanical Specifications

The absolute maximum ratings of the SE5516A are provided in Table 2. The recommended operating conditions are specified in Table 3.

Electrical specifications are provided in Tables 4 through 12. Typical performance characteristics of the SE5516A are illustrated in Figures 3 and 4.

**Table 2. SE5516A Absolute Maximum Ratings<sup>1</sup>**

Parameter	Symbol	Minimum	Maximum	Units
Supply voltage	V <sub>CC</sub>	-0.3	+3.6	V
PA enable voltage (pins ENA and ENG)		-0.3	+3.6	V
Maximum input power to TA and TG pins with ANT terminated in 6:1 load or better: @ +25°C @ -40°C	P <sub>IN</sub>		+12 +5	dBm dBm
Operating temperature	T <sub>A</sub>	-40	+85	°C
Storage temperature	T <sub>STG</sub>	-40	+150	°C
Switch logic control	C0, C1	0	3.6	V
LNA power (receive mode)	P <sub>RX</sub>		-3	dBm
Electrostatic discharge: ANT pin, Human Body Model (HBM), Class 1C All other pins, HBM, Class 1A	ESD		1000 250	V V

<sup>1</sup> Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

**ESD HANDLING:** *Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD handling precautions should be used at all times.*

**Table 3. SE5516A Recommended Operating Conditions**

Parameter	Symbol	Min	Typ	Max	Units
Supply voltage	V <sub>CC</sub>	3.0	3.3	3.6	V
Operating temperature	T <sub>A</sub>	-40	+25	+85	°C
Junction temperature	T <sub>J</sub>			+150	°C

**Table 4. SE5516A Electrical Specifications: DC Characteristics<sup>1</sup>**  
**(V<sub>CC</sub> = 3.3 V, T<sub>A</sub> = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Total 802.11a transmit supply current	I <sub>CC_A</sub>	No RF		175	210	mA
	I <sub>CC_A</sub>	P <sub>OUT</sub> = +16 dBm, 54 Mbps, OFDM signal, 64 QAM		230	255	mA
Total 802.11g transmit supply current	I <sub>CC_G</sub>	No RF		135	165	mA
	I <sub>CC_G</sub>	P <sub>OUT</sub> = +18 dBm, 54 Mbps, OFDM signal, 64 QAM		175	195	mA
Total 802.11b transmit supply current	I <sub>CC_B</sub>	P <sub>OUT</sub> = +21 dBm, 11 Mbps, CCK signal, BT = 0.45		200	220	mA
Total 802.11ac transmit supply current, 2G	I <sub>CC_AC2G</sub>	P <sub>OUT</sub> = +16 dBm, 256 QAM, MCS9, 40 MHz		155	175	mA
Total 802.11ac transmit supply current, 5G	I <sub>CC_AC5G</sub>	P <sub>OUT</sub> = +13 dBm, 256 QAM, MCS9, 80 MHz		190	215	mA
Total receive supply current, 2G	I <sub>CC_RX2</sub>	ENG = ENA = 0 V, LNA on, switch condition 1			12	mA
Total receive supply current, 5G	I <sub>CC_RX5</sub>	ENG = ENA = 0 V, LNA on, switch condition 4			12	mA
Total supply current	I <sub>CC_OFF</sub>	No RF, ENG = ENA = 0 V, switch condition 3 or 6			50	μA

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

**Table 5. SE5516A Electrical Specifications: Logic Characteristics<sup>1</sup>**  
**(V<sub>CC</sub> = 3.3 V, T<sub>A</sub> = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Logic high voltage for ENG and ENA (module on)	V <sub>ENH</sub>		1.8		V <sub>CC</sub>	V
Logic low voltage for ENG and ENA (module off)	V <sub>ENL</sub>		0		0.5	V
Input current logic high voltage (ENG and ENA)	I <sub>ENH</sub>			350	400	μA
Input current logic low voltage (ENG and ENA)	I <sub>ENL</sub>			0.2		μA

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

**Table 6. SE5516A Electrical Specifications: Switch Characteristics<sup>1</sup>**  
**(V<sub>CC</sub> = V<sub>EN</sub> = 3.3 V, T<sub>A</sub> = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Control voltage (on state)	V <sub>CTL_ON</sub>		3.0		3.6	V
Control voltage (off state)	V <sub>CTL_OFF</sub>		0		0.2	V
Low loss switch control voltage	SW <sub>ON</sub>	High state = V <sub>CTL_ON</sub> - V <sub>CTL_OFF</sub>	2.8		V <sub>CC</sub>	V
High loss switch control voltage	SW <sub>OFF</sub>	Low state = V <sub>CTL_OFF</sub> - V <sub>CTL_OFF</sub>	0		0.3	V
Switch control bias current RF applied No RF	I <sub>CTL_ON</sub>	C0 and C1 pins driven high			100 30	μA μA
Control input capacitance	C <sub>CTL</sub>				100	pF

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

**Table 7. SE5516A Electrical Specifications: 2.4 GHz Transmit Characteristics<sup>1</sup> (1 of 2)**  
**(V<sub>CC</sub> = ENG = C0 = 3.3 V, ENA = C1 = 0 V, T<sub>A</sub> = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Frequency range	f		2400		2500	MHz
Output power, 802.11g	P <sub>OUT_802.11G</sub>	54 Mbps OFDM signal, 64 QAM, DEVM = 3%, input signal EVM < 1%, 802.11g mask compliant		+18		dBm
Output power, 802.11n	P <sub>OUT_802.11N</sub>	OFDM signal, MCS7, HT40, DEVM = 3%, 802.11n mask compliant, ±11 MHz offset, RBW = 100 kHz, VBW = 30 kHz DEVM = 2.5%	+18	+17		dBm dBm
Output power, 802.11ac	P <sub>OUT_802.11AC</sub>	40 MHz, 256 QAM, MCS9, DEVM = 1.8%, 802.11ac mask compliant DEVM = 1.4%	+16	+15		dBm dBm
Output power, 802.11b	P <sub>OUT_802.11B</sub>	11 Mbps CCK signal, BT = 0.45, ACPR (±11 MHz offset) < -35, ACPR (±22 MHz offset) < -56		+21		dBm
1 dB compression point	P1dB		+24.0	+24.5		dBm
Small signal gain	IS21I		25		31	dB
Small signal gain variation over any 40 MHz channel	ΔIS21I	Switch state 2		0.5	1.0	dB
Small signal gain variation over band	ΔIS21I			1	2	dB
Small signal gain @ ½ VCO frequency	IS21I_1.6	1608 to 1656 MHz		12	20	dB
Small signal gain @ VCO frequency	IS21I_3.2	3216 to 3312 MHz 1206 to 1242 MHz 3618 to 3726 MHz			0 +12 -10	dB dB dB

**Table 7. SE5516A Electrical Specifications: 2.4 GHz Transmit Characteristics<sup>1</sup> (2 of 2)**  
**(Vcc = ENG = C0 = 3.3 V, ENA = C1 = 0 V, TA = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
2 <sup>nd</sup> and 3 <sup>rd</sup> harmonics	2fo, 3fo	P <sub>OUT</sub> ≤ +21 dBm, 1 Mbps, CCK			-48	dBm/MHz
		P <sub>OUT</sub> ≤ +18 dBm, 802.11g/n, all data rates			-50	dBm/MHz
Delay and rise/fall time	t <sub>DR</sub> , t <sub>DF</sub>	50% of V <sub>EN</sub> edge and 90/10% of final output power level			0.4	μs
Input return loss	S <sub>11</sub>			7		dB
Output return loss	S <sub>22</sub>			10		dB
Stability	STAB	CW, P <sub>OUT</sub> = +21 dBm, 0.1 to 21 GHz, load VSWR = 6:1	All non-harmonically related outputs <-42 dBm/MHz			
Ruggedness	Ru	TG = +12 dBm, ANT load varies over 6:1 VSWR	No irreversible damage			

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

**Table 8. SE5516A Electrical Specifications: 2.4 GHz Receive Characteristics<sup>1</sup>**  
**(Vcc = 3.3 V, Switch Condition 1, TA = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Frequency range	f		2400		2500	MHz
Small signal gain, LNA enabled	S <sub>21</sub>	2400 to 2485 MHz	10	13	15	dB
Small signal gain, bypass mode	S <sub>21</sub>	LNA bypassed, switch condition 3	-9.5	-8.5	-7.5	dB
Small signal gain, high band gain @ 5150 to 5850 MHz	S <sub>21</sub>	Switch condition 1		-17		dB
Small signal gain variation	Δ S <sub>21</sub>	2400 to 2485 MHz, over any 40 MHz band		0.25	0.50	dB
Noise figure	NF	De-embedded to device		2.5	2.8	dB
Input return loss	S <sub>11</sub>		5	10		dB
Output return loss	S <sub>22</sub>		8	10		dB
Third order input intercept point	IIP3	Switch condition 1		+2.4		dBm
1 dB input compression point	IP1dB	LNA enabled	-7.5			dBm
		LNA bypassed, switch condition 3		+8.0		dBm
Loopback isolation (ANT to RG pins)	LB	Switch condition 2, P <sub>OUT</sub> = P <sub>SAT</sub>	P <sub>SAT</sub> + 10	39		dB
Enable time	t <sub>EN</sub>	50% of C0 or C1 edge and 90/100% of final output power levels: (Note 2) (Note 3)			0.2	μs
					1.0	μs

<sup>1</sup> Performance is guaranteed only under the conditions listed in this Table.

<sup>2</sup> Timing applies when transitioning in either direction between switch conditions 1 and 3 (see Table 13).

<sup>3</sup> Timing applies when transitioning in either direction between switch conditions 2, 4, 5, or 6 and switch conditions 1 or 3 (see Table 13).

**Table 9. SE5516A Electrical Specifications: 5.0 GHz Transmit Characteristics<sup>1</sup>**  
**(V<sub>CC</sub> = ENA = CO = 3.3 V, ENG = C1 = 0 V, T<sub>A</sub> = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Frequency range	f		4900		5900	MHz
Output power, 802.11n	P <sub>OUT_802.11N</sub>	OFDM signal, MCS7, HT40, DEVM = 3%, 802.11n mask compliant, ±11 MHz offset, RBW = 100 kHz, VBW = 30 kHz DEVM = 2.7%	+16 +15			dBm dBm
Output power, 802.11ac	P <sub>OUT_802.11AC</sub>	80 MHz, 256 QAM, MCS9, DEVM = 1.8%, input signal EVM < 0.6%, 802.11ac mask compliant DEVM = 1.6%	+13 +12			dBm dBm
1 dB compression point	P1dB		+21.0	+22.5		dBm
Small signal gain	S <sub>21</sub>	In band @ 1.640 to 1.942 GHz @ 3.280 to 3.900 GHz	+25		+32 0 +2	dB dB dB
Small signal gain variation over 80 MHz channel	Δ S <sub>21</sub>				1	dB
Small signal gain variation over sub-bands	Δ S <sub>21</sub>	5.15 to 5.50 GHz and 5.70 to 5.90 GHz			2	dB
Isolation	S <sub>12</sub>	Switch state 5, 4.9 to 5.9 GHz			-45	dB
2 <sup>nd</sup> and 3 <sup>rd</sup> harmonics	2f <sub>o</sub> , 3f <sub>o</sub>	@ +20 dBm, MCS0			-50	dBm/MHz
Delay and rise/fall time	t <sub>DR</sub> , t <sub>DF</sub>	50% of V <sub>EN</sub> edge and 90/10% of final output power level			0.4	μs
Input return loss	S <sub>11</sub>			10		dB
Output return loss	S <sub>22</sub>			10		dB
Stability	STAB	64 QAM, P <sub>OUT</sub> = +16 dBm, 0.1 to 21 GHz, load VSWR = 6:1	All non-harmonically related outputs <-42 dBm/MHz			-
Ruggedness	Ru	T <sub>A</sub> = +12 dBm, ANT load varies over 6:1 VSWR	No irreversible damage			-

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

**Table 10. SE5516A Electrical Specifications: 5.0 GHz Receive Characteristics<sup>1</sup>**  
**(V<sub>CC</sub> = 3.3 V, Switch Condition 4, T<sub>A</sub> = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Frequency range	f		4900		5850	MHz
Small signal gain	S <sub>21</sub>	Switch condition 4: 4900 to 5850 MHz 2400 to 2500 MHz	+10	+12 -28	+14 -17	dB dB
Small signal gain, bypass mode	S <sub>21</sub>	LNA bypassed, switch condition 6	-10.5	-9.0	-7.5	dB
Small signal gain variation	Δ S <sub>21</sub>	4900 to 5850 MHz, over any 80 MHz band	-0.5		+0.5	dB
		Over entire band	-1.0		+1.0	dB
Noise figure	NF	De-embedded to device		2.8	3.0	dB
Input return loss	S <sub>21</sub>		8	10		dB
Output return loss	S <sub>22</sub>		6	10		dB
Third order input intercept point	IIP <sub>3</sub>	Switch condition 4		+3.5		dBm
1 dB input compression point	IP <sub>1dB</sub>	LNA enabled	-6			dBm
		LNA bypassed, switch condition 6	+6			dBm
Loopback isolation (ANT to RA pins)	LB	Switch condition 5, P <sub>OUT</sub> = P <sub>SAT</sub>		33		dB
Enable time	t <sub>EN</sub>	50% of C0 or C1 edge and 90/100% of final output power levels: (Note 2) (Note 3)			0.2 1.0	μs μs

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.  
<sup>2</sup> Timing applies when transitioning in either direction between switch conditions 4 and 6 (see Table 13).  
<sup>3</sup> Timing applies when transitioning in either direction between switch conditions 1, 2, 3, or 5 and switch conditions 4 or 6 (see Table 13).

**Table 11. SE5516A Electrical Specifications: 2.4 GHz Power Detector Characteristics<sup>1</sup>**  
**(V<sub>CC</sub> = 3.3 V, Switch Condition 2, T<sub>A</sub> = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Frequency range	f		2400		2500	MHz
Power detector range, peak power	PDR	Measured at ANT pin	0		+22	dBm
Output voltage @ P <sub>OUT</sub> = +22 dBm	PDV <sub>P22</sub>	Measured into 26.5 kΩ	0.70	0.83	0.95	V
Output voltage @ P <sub>OUT</sub> = +18 dBm	PDV <sub>P18</sub>	Measured into 26.5 kΩ		0.55		V
Output voltage, no RF output power	PDV <sub>PNO RF</sub>	Measured into 26.5 kΩ	0.15	0.18	0.22	V
Power detector low-pass filter -3 dB corner frequency	LPF-3DB	Measured into 26.5 kΩ	35			MHz
Power detector accuracy	PDET <sub>ACC</sub>	Measured into 3:1 load at ANT port	-1.5		+1.5	dB

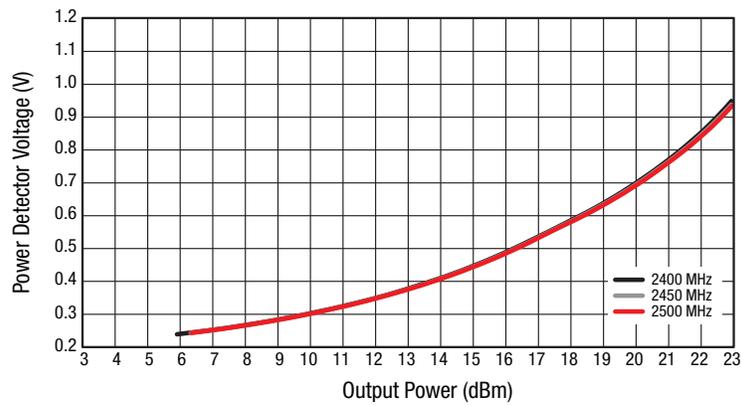
<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

**Table 12. SE5516A Electrical Specifications: 5.0 GHz Power Detector Characteristics<sup>1</sup>**  
**(V<sub>CC</sub> = 3.3 V, Switch Condition 5, T<sub>A</sub> = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

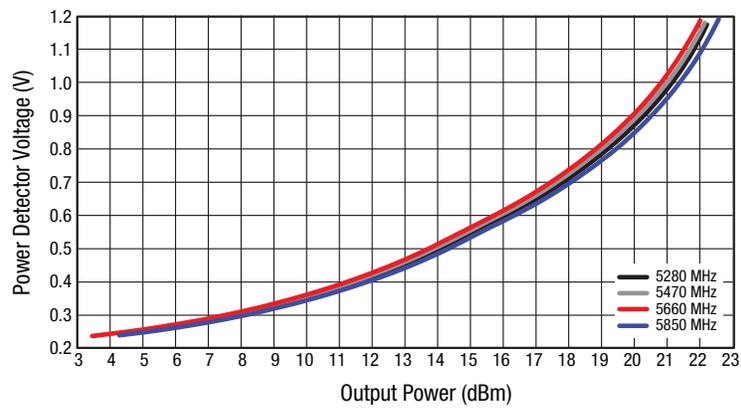
Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Frequency range	f		4900		5900	MHz
Power detector range, peak power	PDR	Measured at ANT pin	0		+21	dBm
Output voltage @ P <sub>OUT</sub> = +20 dBm	PDV <sub>P20</sub>	Measured into 26.5 kΩ:				
		5.15 to 5.35 GHz	0.80		1.00	V
		5.47 to 5.725 GHz	0.82		1.02	V
		5.725 to 5.875 GHz	0.75		0.95	V
Output voltage @ P <sub>OUT</sub> = +16 dBm	PDV <sub>P16</sub>	Measured into 26.5 kΩ		0.60		V
Output voltage, no RF output power	PDV <sub>PNO RF</sub>	Measured into 26.5 kΩ	0.15	0.18	0.22	V
Power detector low-pass filter -3 dB corner frequency	LPF-3DB	Measured into 26.5 kΩ	35			MHz
Power detector accuracy	PDET <sub>ACC</sub>	Measured into 3:1 load at ANT port	-1.5		+1.5	dB

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

## Typical Performance Characteristics



**Figure 3. Power Detector vs Output Power Over Frequency @ 2.5 GHz**



**Figure 4. Power Detector vs Output Power Over Frequency @ 5.0 GHz**

### Evaluation Board Description

The SE5516A Evaluation Board is used to test the performance of the SE5516A WLAN FEM. An Evaluation Board schematic diagram is provided in Figure 5. A photograph of the Evaluation Board is shown in Figure 6. Component values for the SE5516A Evaluation Board are listed in Table 14.

### Evaluation Board Setup Procedure

- Step 1: Connect system ground to pin 2 of connector J6.
- Step 2: Apply 3.3 V to pin 1 of connector J6.
- Step 3: Select a path according to the logic shown in Table 13.

### Package Dimensions

The PCB layout footprint for the SE5516A is provided in Figure 7. Typical part markings are shown in Figure 8. Package dimensions are shown in Figure 9, and tape and reel dimensions are provided in Figure 10.

### Package and Handling Information

Since the device package is sensitive to moisture absorption, it is baked and vacuum packed before shipping. Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

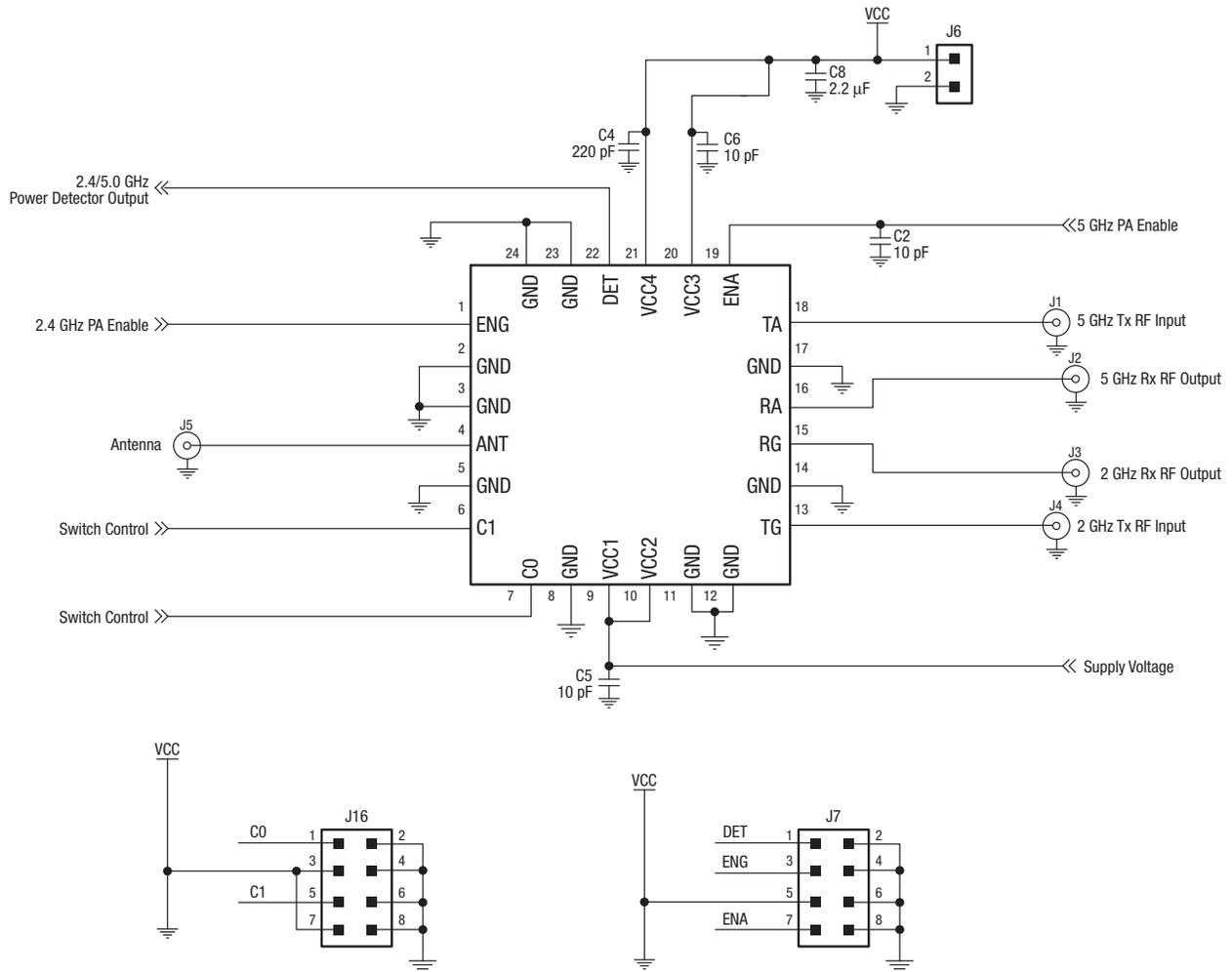
The SE5516A is rated to Moisture Sensitivity Level 3 (MSL3) at 240 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

**Table 13. SE5516A Evaluation Board Switch Control Logic<sup>1</sup>**

Condition	Logic				State		
	C0 (Pin 1, J16 Header)	C1 (Pin 5, J16 Header)	ENA (Pin 7, J7 Header)	ENG (Pin 3, J7 Header)	SP3T	LNA	Bypass
1	0	1	0	0	RG	RG enable	Open
2	0	0	0	1	TG	RG off	RG bypass
3	0	0	0	0	RG	RG off	RG bypass
4	1	1	0	0	RA	RA enable	Open
5	1	0	1	0	TA	RA off	RA bypass
6	1	0	0	0	RA	RA off	RA bypass

<sup>1</sup> "0" = 0 V. "1" = +3.3 V. Any state other than described in this table places the switch into an undefined state. An undefined state will not damage the device.



Note: Some component labels may be different than the corresponding component symbol shown here. Component values, however, are accurate as of the date of this Data Sheet.

S2935

Figure 5. SE5516A Evaluation Board Schematic

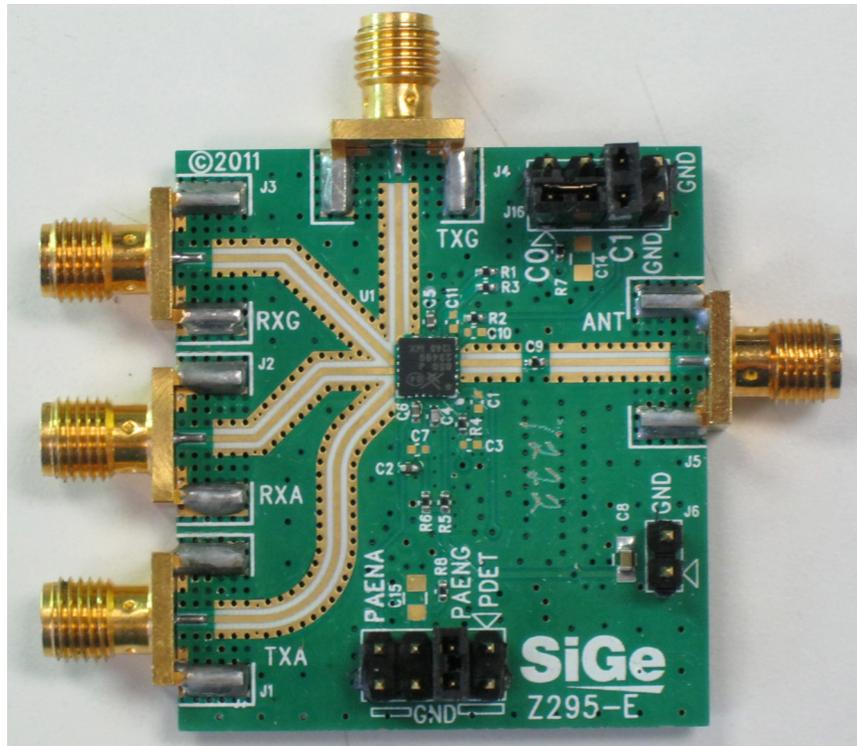
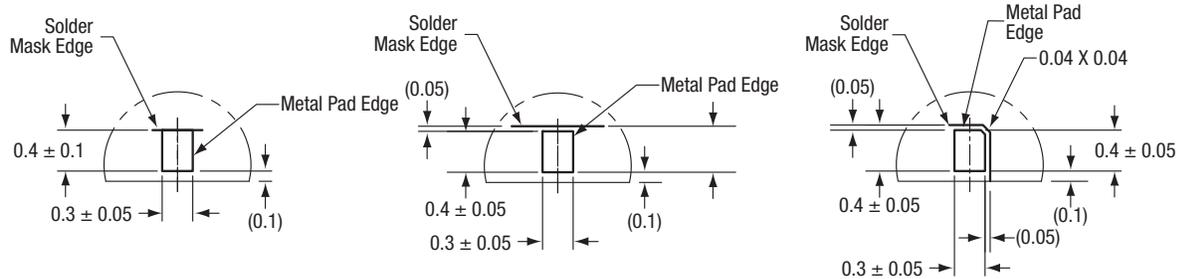
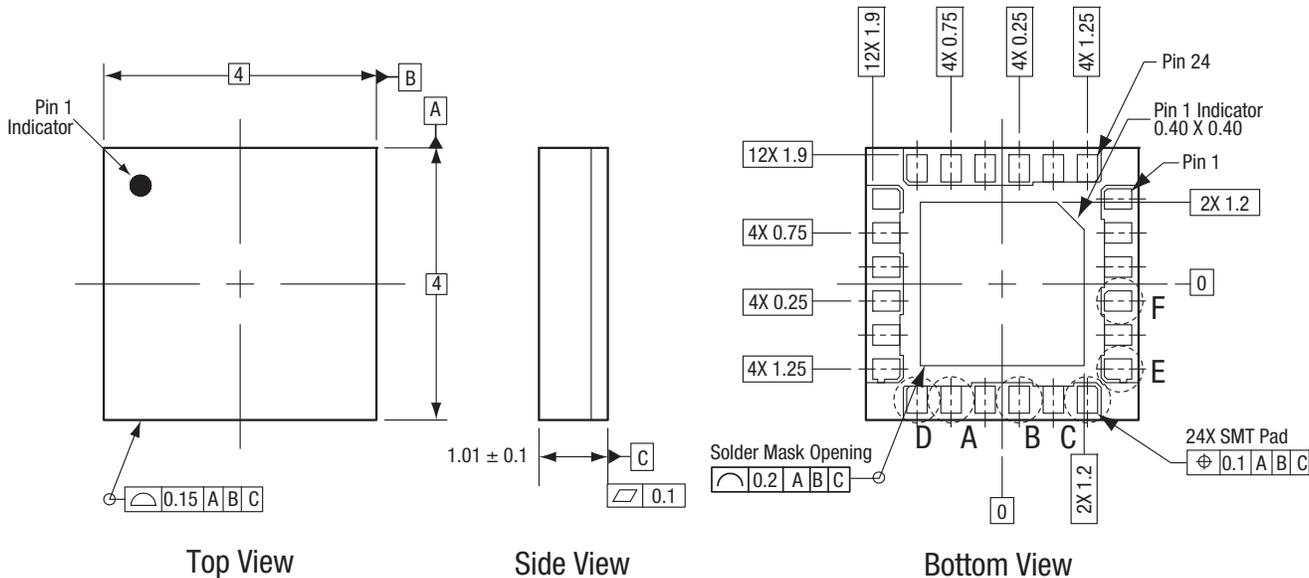


Figure 6. SE5516A Evaluation Board Photo

Table 14. SE5516A Evaluation Board Bill of Materials

Component	Size	Value	Vendor	Mfr Part Number	Comments
C2, C5, C6	0402	10 pF	Murata	GRM1555C1H100JZ01	Multilayer ceramic
C4	0402	220 pF	Murata	GRM1555C1H221JA01	Multilayer ceramic
C8	0805	2.2 $\mu$ F	Murata	GRM21BR71A225KA01L	Ceramic capacitor, 10 V, 10% X7R

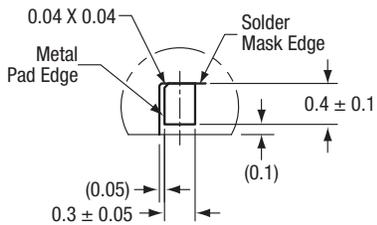




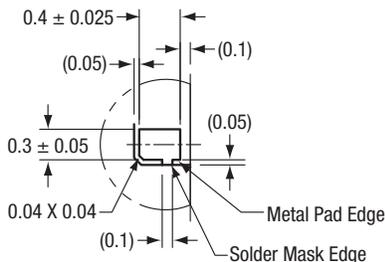
**Detail A**  
Pad  
Scale: 2X  
2X This rotation  
1X Rotated 180°  
2X Rotated 90° CW  
3X Rotated 90° CCW

**Detail B**  
Pad  
Scale: 2X  
2X This rotation  
3X Rotated 180°  
2X Rotated 90° CW

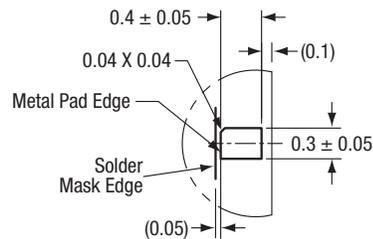
**Detail C**  
Pad  
Scale: 2X  
1X This rotation  
1X Rotated 180°  
1X Rotated 90° CCW  
1X Mirrored 45°



**Detail D**  
Pad  
Scale: 2X  
1X This rotation  
1X Rotated 180°



**Detail E**  
Pad  
Scale: 2X  
1X This rotation  
1X Mirrored 180°



**Detail F**  
Pad  
Scale: 2X  
1X This rotation

All measurements are in millimeters

Dimensioning and tolerancing according to ASME Y14.5M-1994

S2933

**Figure 9. SE5516A Package Dimensions**



**Ordering Information**

Part Number	Product Description	Evaluation Board Part Number
SE5516A	Dual-Band 802.11a/b/g/n/ac WLAN FEM	SE5516A-EK1

Copyright © 2012-2014, 2017, 2019 Skyworks Solutions, Inc. All Rights Reserved.

Information in this document is provided in connection with Skyworks Solutions, Inc. (“Skyworks”) products or services. These materials, including the information contained herein, are provided by Skyworks as a service to its customers and may be used for informational purposes only by the customer. Skyworks assumes no responsibility for errors or omissions in these materials or the information contained herein. Skyworks may change its documentation, products, services, specifications or product descriptions at any time, without notice. Skyworks makes no commitment to update the materials or information and shall have no responsibility whatsoever for conflicts, incompatibilities, or other difficulties arising from any future changes.

No license, whether express, implied, by estoppel or otherwise, is granted to any intellectual property rights by this document. Skyworks assumes no liability for any materials, products or information provided hereunder, including the sale, distribution, reproduction or use of Skyworks products, information or materials, except as may be provided in Skyworks Terms and Conditions of Sale.

THE MATERIALS, PRODUCTS AND INFORMATION ARE PROVIDED “AS IS” WITHOUT WARRANTY OF ANY KIND, WHETHER EXPRESS, IMPLIED, STATUTORY, OR OTHERWISE, INCLUDING FITNESS FOR A PARTICULAR PURPOSE OR USE, MERCHANTABILITY, PERFORMANCE, QUALITY OR NON-INFRINGEMENT OF ANY INTELLECTUAL PROPERTY RIGHT; ALL SUCH WARRANTIES ARE HEREBY EXPRESSLY DISCLAIMED. SKYWORKS DOES NOT WARRANT THE ACCURACY OR COMPLETENESS OF THE INFORMATION, TEXT, GRAPHICS OR OTHER ITEMS CONTAINED WITHIN THESE MATERIALS. SKYWORKS SHALL NOT BE LIABLE FOR ANY DAMAGES, INCLUDING BUT NOT LIMITED TO ANY SPECIAL, INDIRECT, INCIDENTAL, STATUTORY, OR CONSEQUENTIAL DAMAGES, INCLUDING WITHOUT LIMITATION, LOST REVENUES OR LOST PROFITS THAT MAY RESULT FROM THE USE OF THE MATERIALS OR INFORMATION, WHETHER OR NOT THE RECIPIENT OF MATERIALS HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

Skyworks products are not intended for use in medical, lifesaving or life-sustaining applications, or other equipment in which the failure of the Skyworks products could lead to personal injury, death, physical or environmental damage. Skyworks customers using or selling Skyworks products for use in such applications do so at their own risk and agree to fully indemnify Skyworks for any damages resulting from such improper use or sale.

Customers are responsible for their products and applications using Skyworks products, which may deviate from published specifications as a result of design defects, errors, or operation of products outside of published parameters or design specifications. Customers should include design and operating safeguards to minimize these and other risks. Skyworks assumes no liability for applications assistance, customer product design, or damage to any equipment resulting from the use of Skyworks products outside of stated published specifications or parameters.

Skyworks and the Skyworks symbol are trademarks or registered trademarks of Skyworks Solutions, Inc. or its subsidiaries in the United States and other countries. Third-party brands and names are for identification purposes only, and are the property of their respective owners. Additional information, including relevant terms and conditions, posted at [www.skyworksinc.com](http://www.skyworksinc.com), are incorporated by reference.

## X-ON Electronics

Largest Supplier of Electrical and Electronic Components

*Click to view similar products for [WiFi Development Tools - 802.11 category](#):*

*Click to view products by [Skyworks manufacturer](#):*

Other Similar products are found below :

[YSAEWIFI-1](#) [SKY65981-11EK1](#) [QPF7221PCK-01](#) [SIMSA915C-Cloud-DKL](#) [SIMSA433C-Cloud-DKL](#) [ISM43903-R48-EVB-E](#)  
[QPF4206BEVB01](#) [RN-G2SDK](#) [SKY85734-11EK1](#) [SKY85735-11EK1](#) [ENW49D01AZKF](#) [ESP-LAUNCHER](#) [MIKROE-2336](#)  
[EVAL\\_PAN1760EMK](#) [3210](#) [EVAL\\_PAN1026EMK](#) [ATWINC1500-XPRO](#) [2471](#) [DM990001](#) [WRL-13711](#) [2999](#) [ATWILC3000-SHLD](#)  
[DFR0321](#) [TEL0118](#) [3213](#) [DFR0489](#) [WRL-13804](#) [DEV-13907](#) [UP-3GHAT-A20-0001](#) [3405](#) [TEL0078](#) [2680](#) [2702](#) [2821](#) [3044](#) [3606](#) [3653](#)  
[4000](#) [4172](#) [4178](#) [4201](#) [4285](#) [4289](#) [CS-ANAVI-25](#) [CS-ANAVI-26](#) [CS-ANAVI-23](#) [CS-ANAVI-24](#) [CS-ANAVI-28](#) [CS-ANAVI-29](#) [CS-ANAVI-30](#)