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Siliup Semiconductor

SP4606BCP8

30V Complementary MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
30V	16m Ω @10V	6A
	24m Ω @4.5V	
-30V	40m Ω @-10V	-5A
	60m Ω @-4.5V	

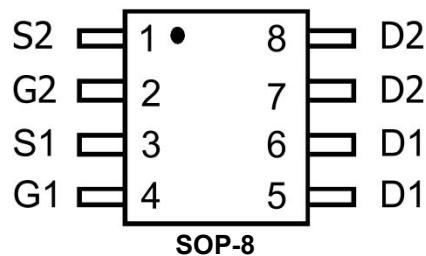
Feature

- TrenchFET Power MOSFET
- Excellent RDS(on) and Low Gate Charge

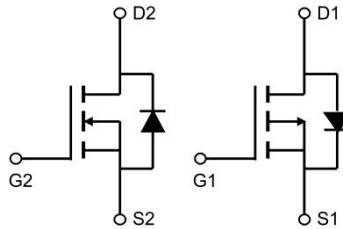
Application

- Load Switch for Portable Devices
- Battery Switch

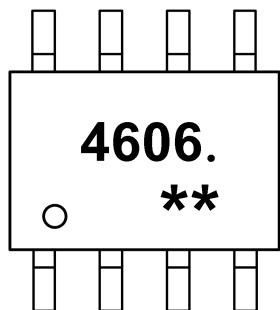
Package



Circuit diagram



Marking



4606. = Device code

** = Week Code



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Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value		Unit
		N-Channel	P-Channel	
Drain-Source Voltage	V _{DS}	30	-30	V
Gate-Source Voltage	V _{GS}	±20	±20	V
Continuous Drain Current(t≤10s)	I _D	6	-5	A
Power Dissipation(t≤10s)	P _D	2	2	W
Thermal Resistance from Junction to Ambient(t≤10s)	R _{θJA}	62.5		°C/W
Junction Temperature	T _J	150		°C
Storage Temperature	T _{STG}	-55~ +150		°C

N-Channel Electrical characteristics (T_A=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, ID = 250μA	30			V
Zero gate voltage drain current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V			1	μA
Gate-body leakage current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±0.1	μA
Gate threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , ID = 250μA	1.0	1.5	2.2	V
Drain-source on-resistance ¹⁾	R _{DS(on)}	V _{GS} = 10V, ID = 1A		16	20	mΩ
		V _{GS} = 4.5V, ID = 1A		24	32	
Dynamic characteristics²⁾						
Input Capacitance	C _{iss}	V _{DS} =15V , V _{GS} =0V , f=1MHz		513		pF
Output Capacitance	C _{oss}			69		
Reverse Transfer Capacitance	C _{rss}			51		
Switching Characteristics						
Total gate charge@4.5V	Q _g	V _{DS} =20V , V _{GS} =4.5V , ID=6A		5		nC
Gate-source charge	Q _{gs}			1.11		
Gate-drain charge	Q _{gd}			2.61		
Turn-on delay time	t _{d(on)}	V _{DD} =12V , V _{GS} =10V , RG=3.3 ID=6A		7.7		ns
Turn-on rise time	t _r			46		
Turn-off delay time	t _{d(off)}			11		
Turn-off fall time	t _f			3.6		
Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V , I _S =1A , T _J =25°C			1.2	V

Notes:

- 1) Pulse test: pulse width≤300μs, duty cycle≤2%.
- 2) Guaranteed by design, not subject to production testing.



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P-Channel Electrical characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-30			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = -24\text{V}, V_{\text{GS}} = 0\text{V}$			-1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
Gate threshold voltage ¹⁾	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-1	-1.6	-3	V
Drain-source on-resistance ¹⁾	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_D = -4.1\text{A}$		40	50	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_D = -3\text{A}$		60	80	
Forward transconductance ¹⁾	g_{FS}	$V_{\text{DS}} = -5\text{V}, I_D = -4\text{A}$	5.5			S
Dynamic characteristics²⁾						
Input Capacitance	C_{iss}	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		501		pF
Output Capacitance	C_{oss}			72		
Reverse Transfer Capacitance	C_{rss}			57		
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = -10\text{V}, R_L = 3.6\Omega, R_{\text{GEN}} = 3\Omega$		8.6		ns
Turn-on rise time	t_r			5.0		
Turn-off delay time	$t_{\text{d}(\text{off})}$			28.2		
Turn-off fall time	t_f			13.5		
Source-Drain Diode characteristics						
Diode Forward voltage	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_s = -1\text{A}$			-1.2	V

Notes:

- 1) Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- 2) These parameters have no way to verif.



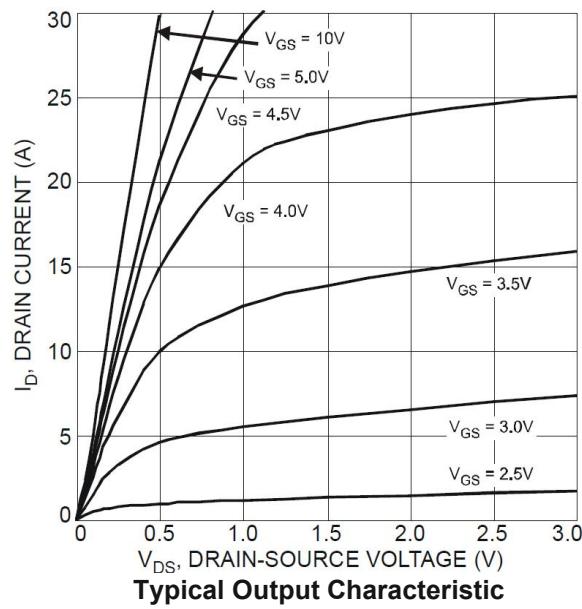
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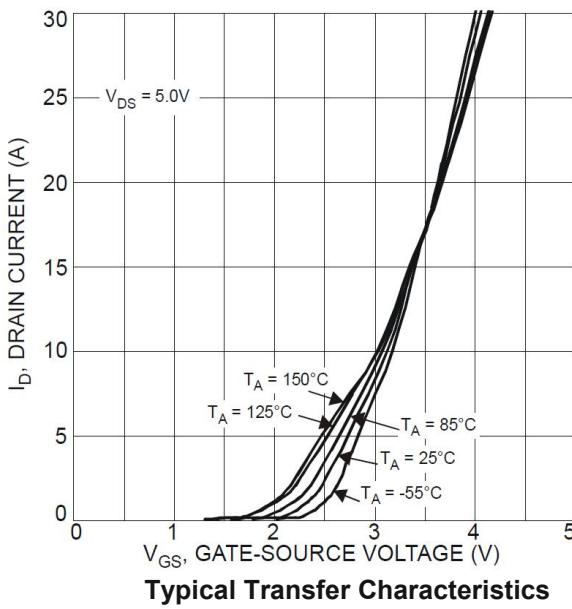
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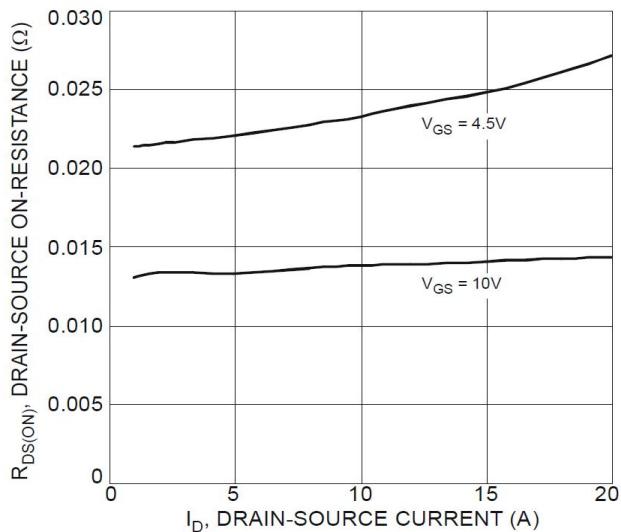
N-Channel Typical Characteristics



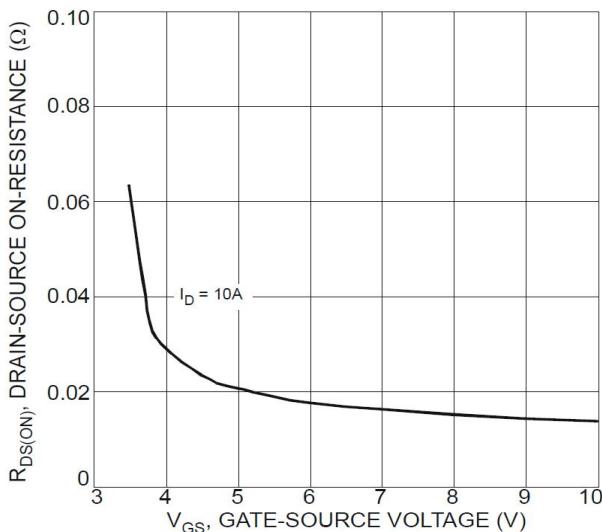
Typical Output Characteristic



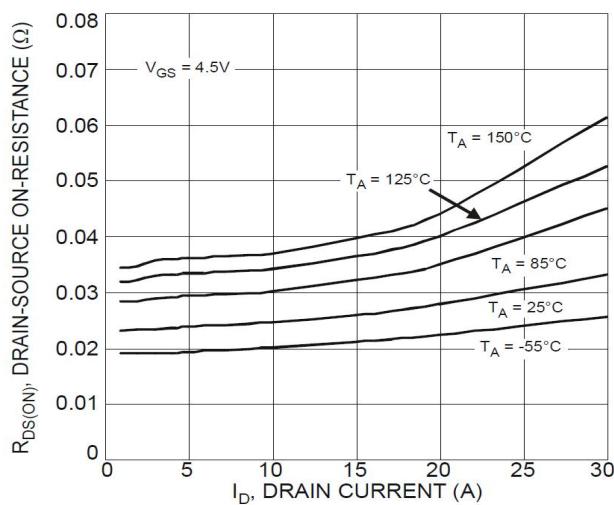
Typical Transfer Characteristics



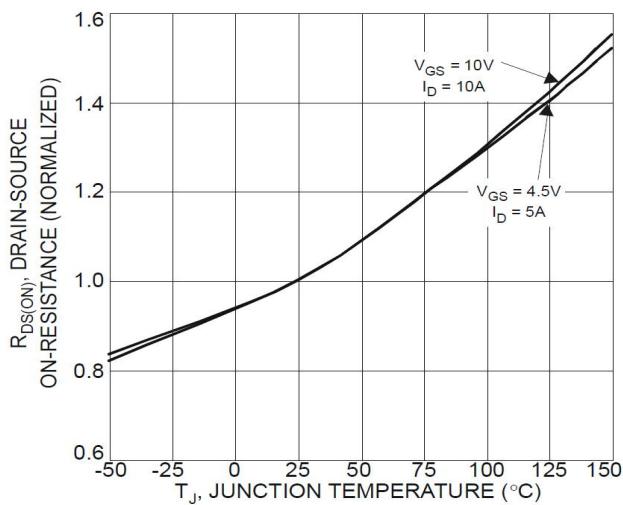
On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Drain Current and Temperature



On-Resistance Variation with Temperature

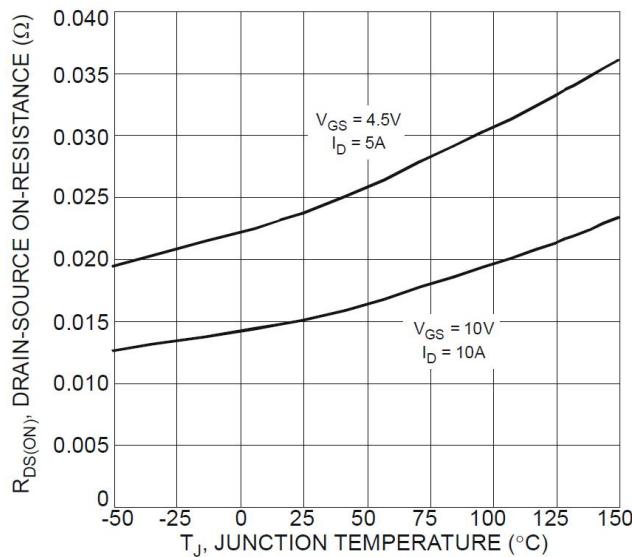


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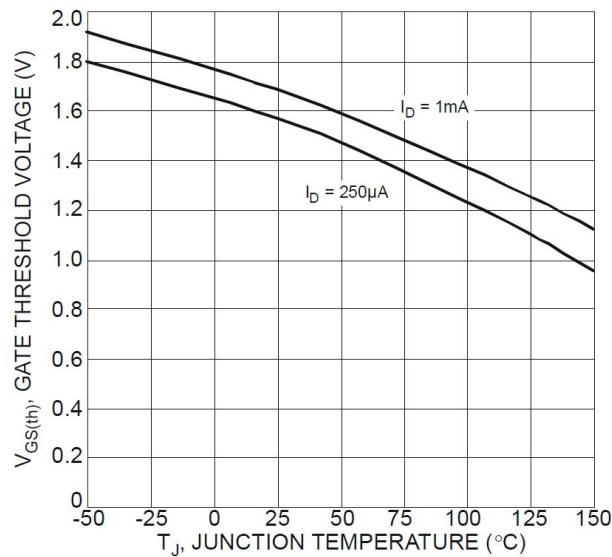
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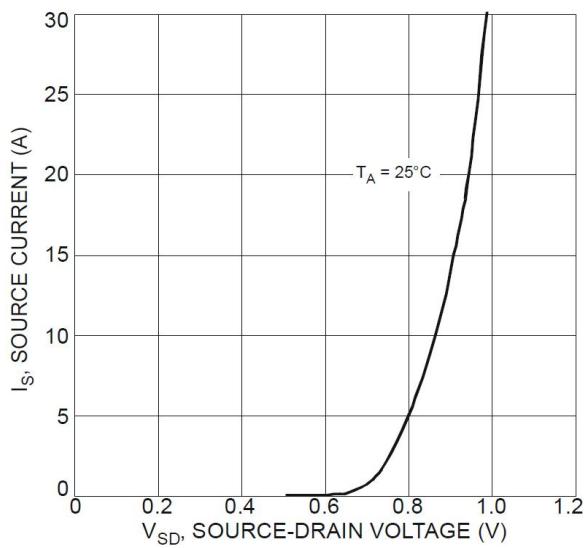
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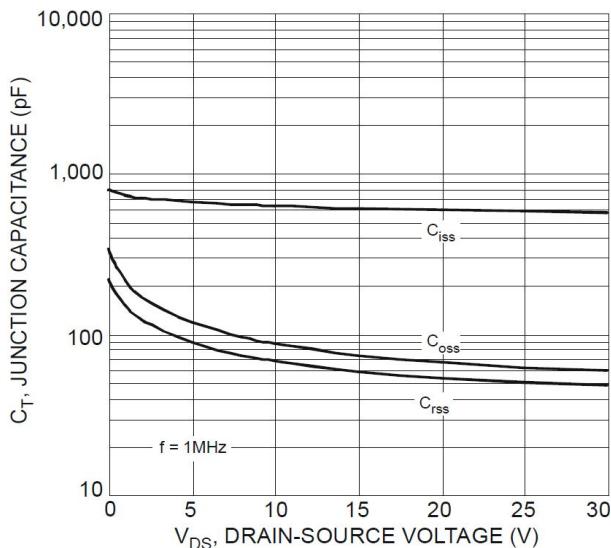
On-Resistance Variation with Temperature



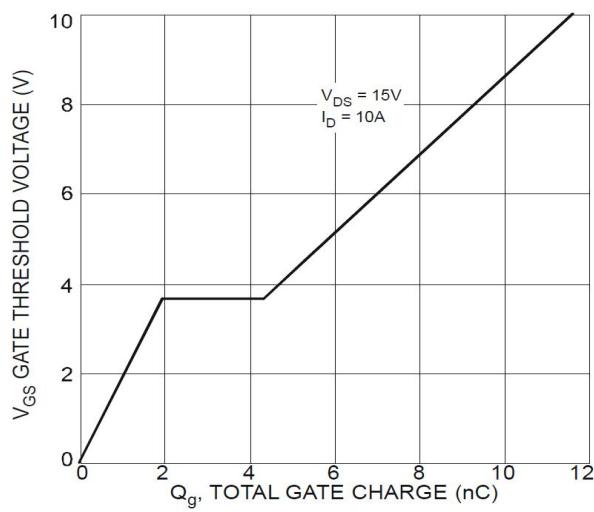
Gate Threshold Variation vs. Ambient Temperature



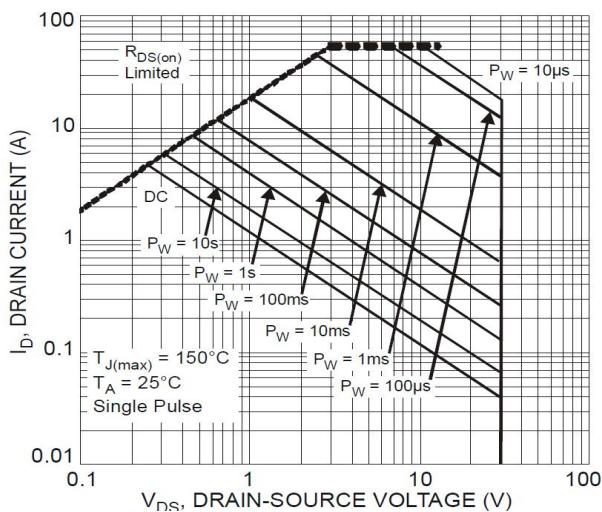
Diode Forward Voltage vs. Current



Typical Junction Capacitance



Gate Charge



SOA, Safe Operation Area



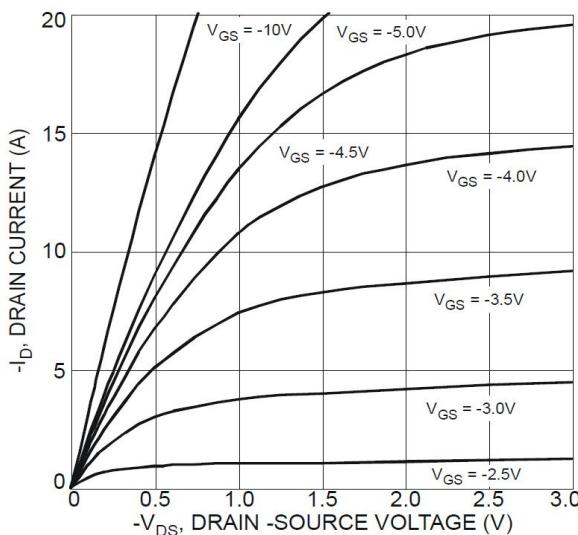
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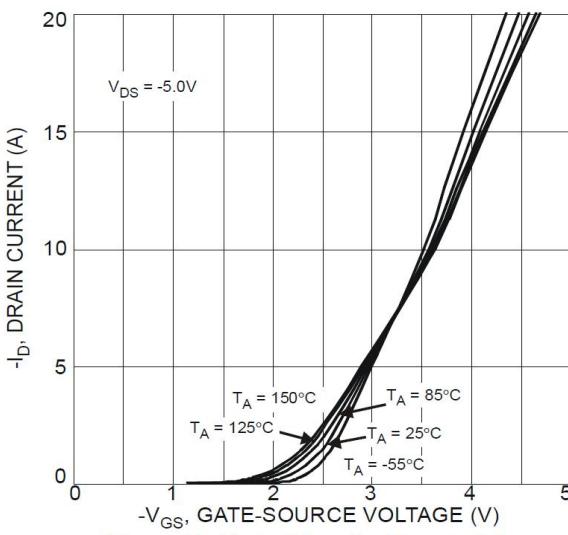
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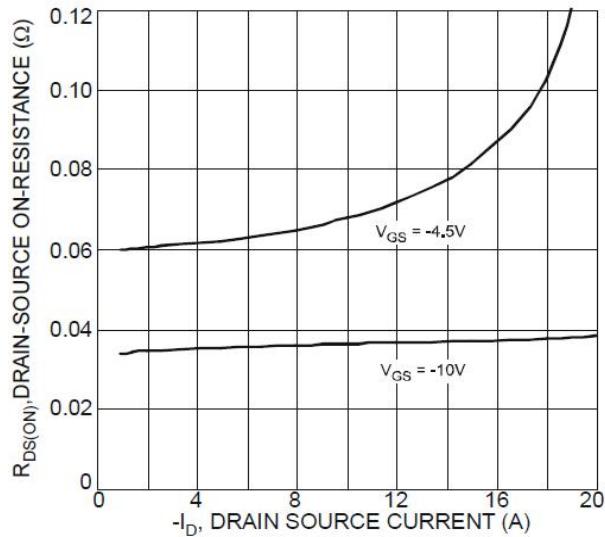
P-Channel Typical Characteristics



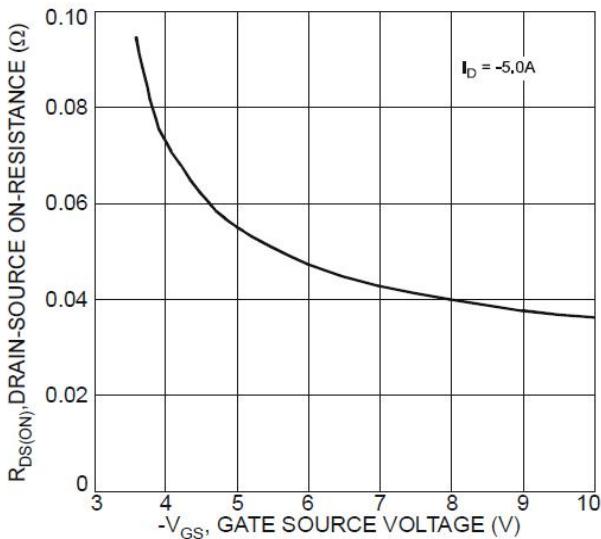
Typical Output Characteristics



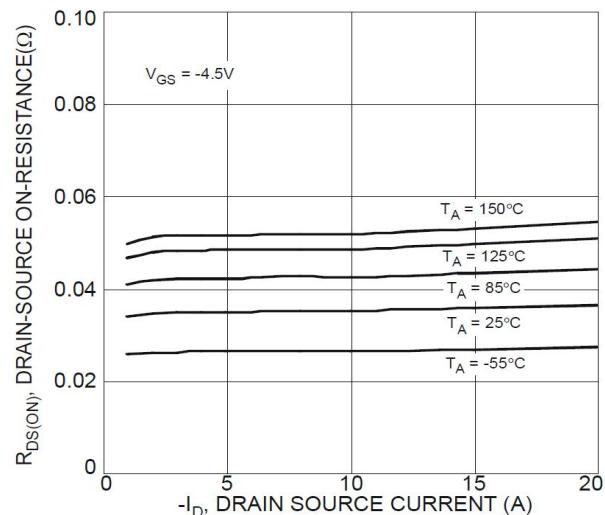
Typical Transfer Characteristics



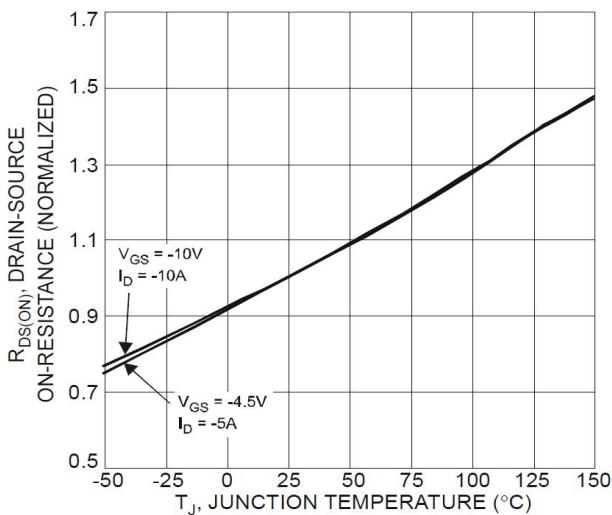
On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Drain Current and Temperature



On-Resistance Variation with Temperature

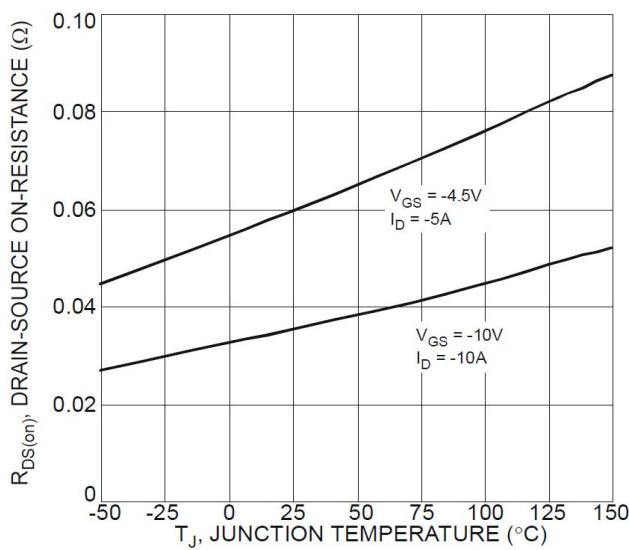


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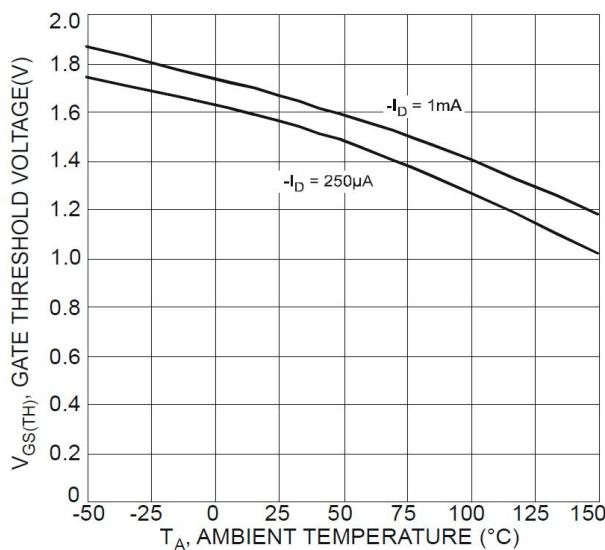
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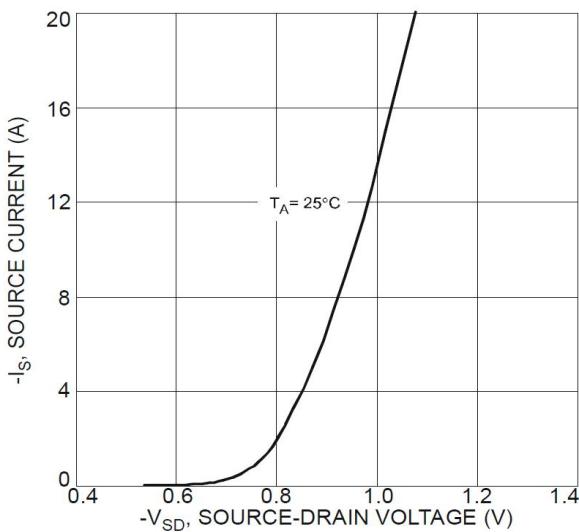
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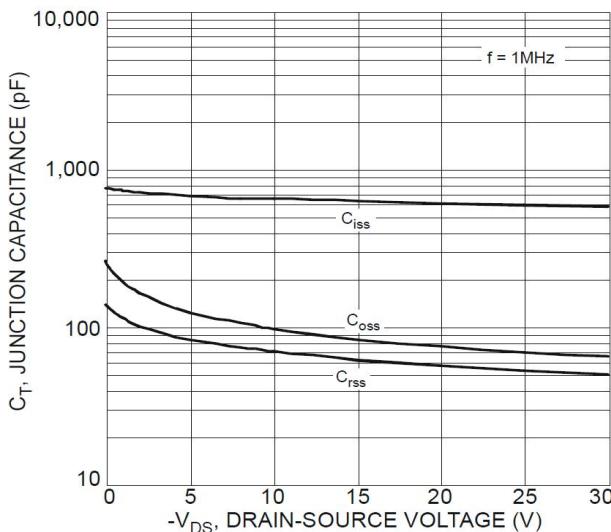
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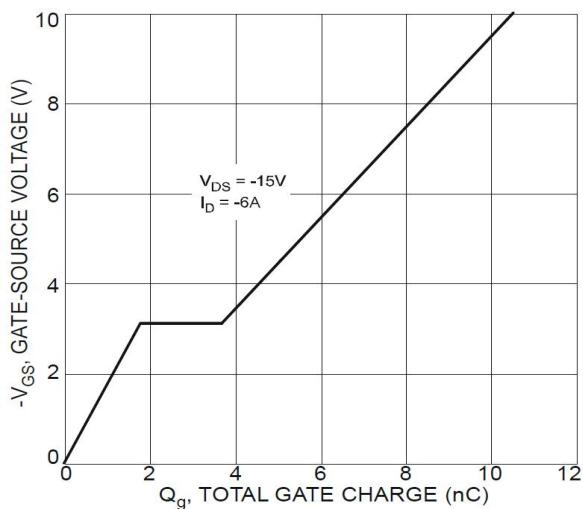
Gate Threshold Variation vs. Ambient Temperature



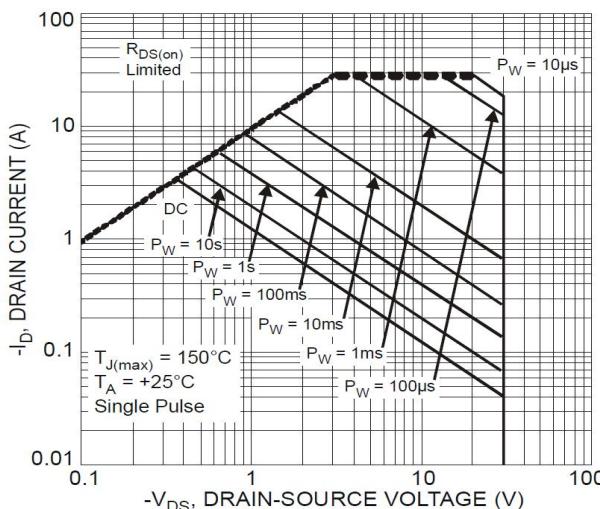
Diode Forward Voltage vs. Current



Typical Junction Capacitance



Gate-Charge Characteristics



SOA, Safe Operation Area

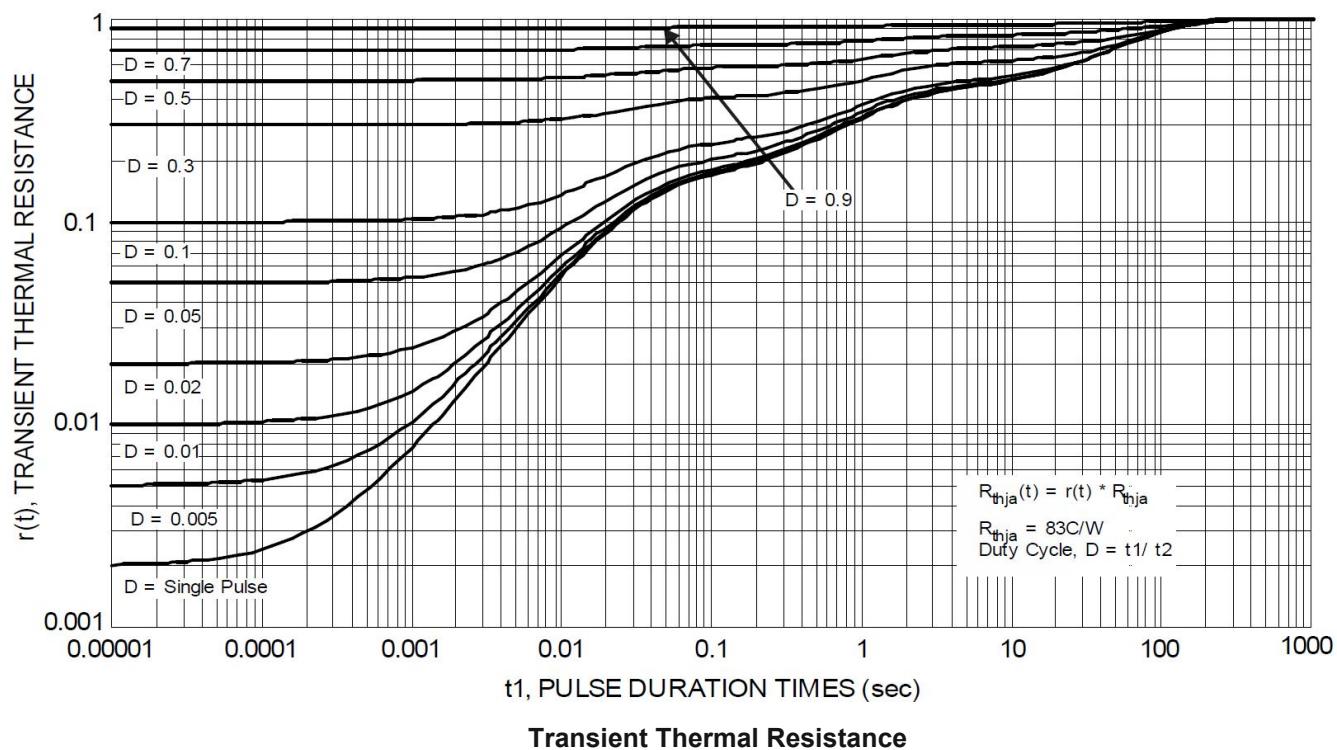


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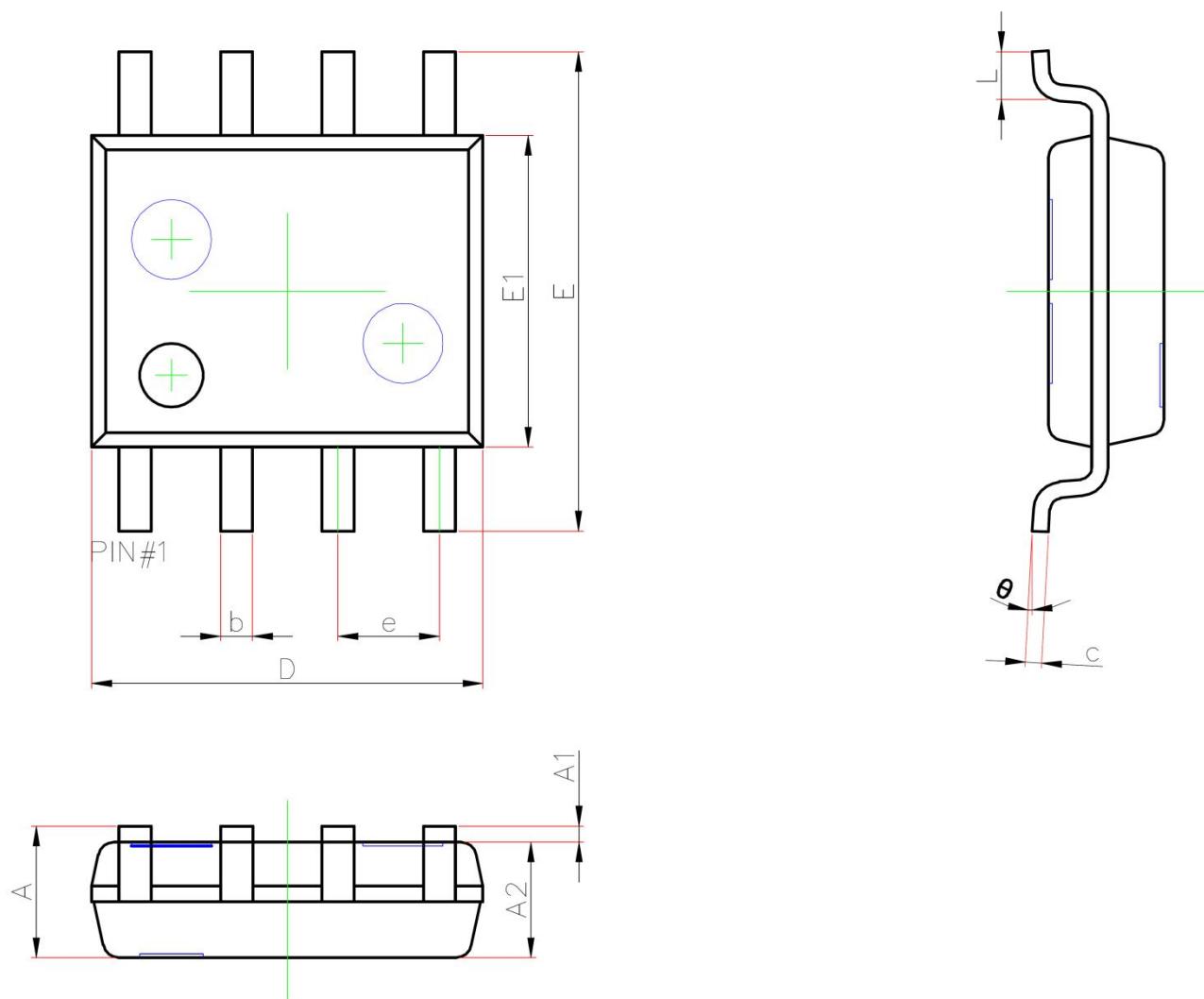
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SOP-8 Package Information



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	1.35	1.75
A1	0.10	0.25
A2	1.35	1.55
b	0.33	0.51
c	0.17	0.25
D	4.80	5.00
e	1.27 REF.	
E	5.80	6.20
E1	3.80	4.00
L	0.40	1.27
θ	0°	8°

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