

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
-20V	520mΩ@-4.5V	-0.66A
	700mΩ@-2.5V	



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Feature

- Surface Mount Package
- P-Channel Switch with Low $R_{DS(on)}$
- Operated at Low Logic Level Gate Drive
- ESD protected:2kV

Application

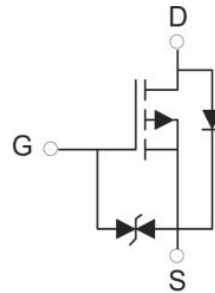
- Load/Power Switching
- Interfacing, Logic Switching
- Battery Management for Ultra Small Portable Electronics

Package



DFN1006-3B

Circuit diagram



Marking



04K =Device Code

Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 10	V
Continuous Drain Current	I_D	-0.66	A
Pulsed Drain Current	I_{DM}	-1.2	A
Power Dissipation	P_D	0.15	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	833	$^{\circ}C/W$
Junction Temperature	T_J	150	$^{\circ}C$
Storage Temperature	T_{STG}	-55~ +150	$^{\circ}C$

Electrical characteristics (T_A=25 °C, unless otherwise noted)

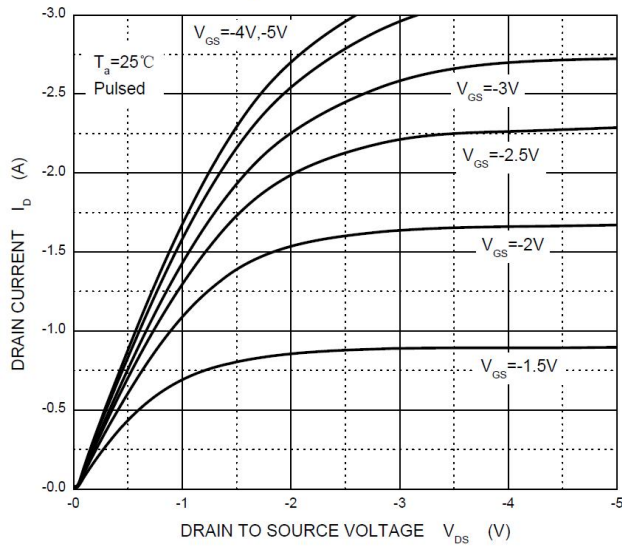
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-20			V
Zero gate voltage drain current	I _{DSS}	V _{DS} = -16V, V _{GS} = 0V			-1	μA
Gate-body leakage current	I _{GSS}	V _{GS} = ±10V, V _{DS} = 0V			±10	μA
Gate threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-0.35	-0.7	-1	V
Drain-source on-resistance ¹⁾	R _{DS(on)}	V _{GS} = -4.5V, I _D = -0.5A			0.52	Ω
		V _{GS} = -2.5V, I _D = -0.2A			0.7	
		V _{GS} = -1.8V, I _D = -0.1A		0.95		
Dynamic characteristics ²⁾						
Input Capacitance	C _{iss}	V _{DS} = -16V, V _{GS} = 0V, f = 1MHz		113		pF
Output Capacitance	C _{oss}			15		
Reverse Transfer Capacitance	C _{rss}			9		
Turn-on delay time	t _{d(on)}	V _{DS} = -10V, I _D = -200mA, V _{GS} = -4.5V, R _G = 10Ω		9		ns
Turn-on rise time	t _r			5.7		
Turn-off delay time	t _{d(off)}			32.6		
Turn-off fall time	t _f			20.3		
Source-Drain Diode characteristics						
Diode Forward voltage	V _{SD}	V _{GS} = 0V, I _S = -0.5 A			-1.2	V

Notes:

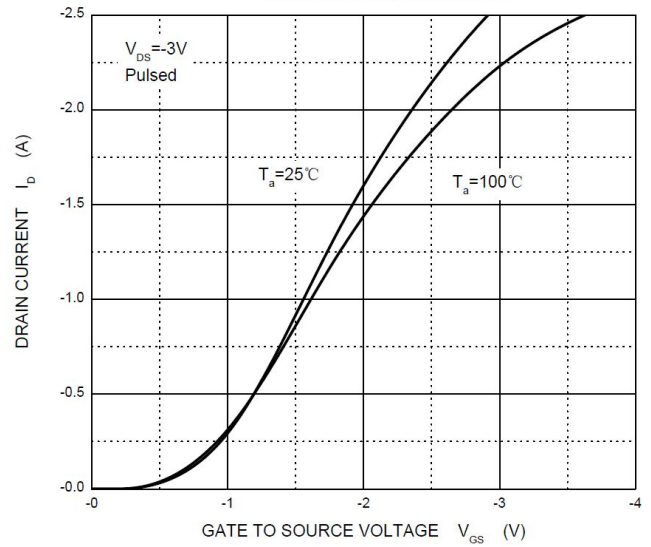
- 1) Pulse Test: Pulse Width < 300 μs , Duty Cycle $\leq 2\%$.
- 2) Guaranteed by design, not subject to production testing.

Typical Characteristics

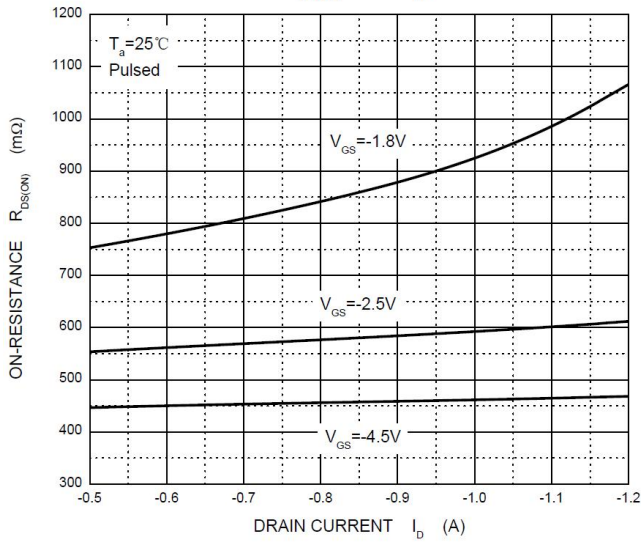
Output Characteristics



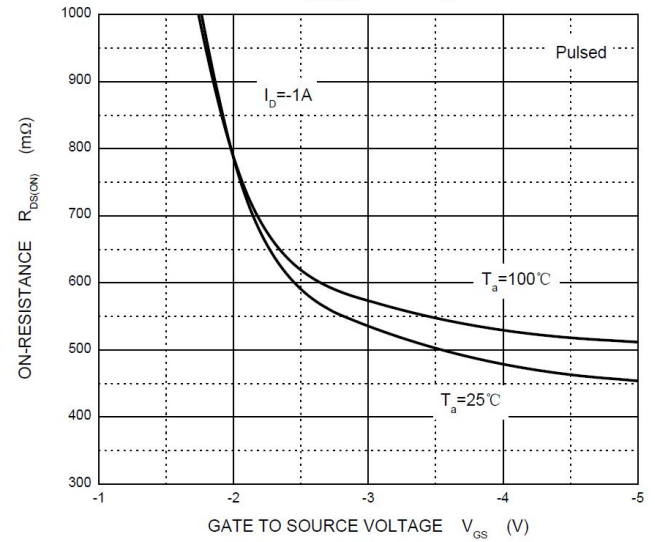
Transfer Characteristics



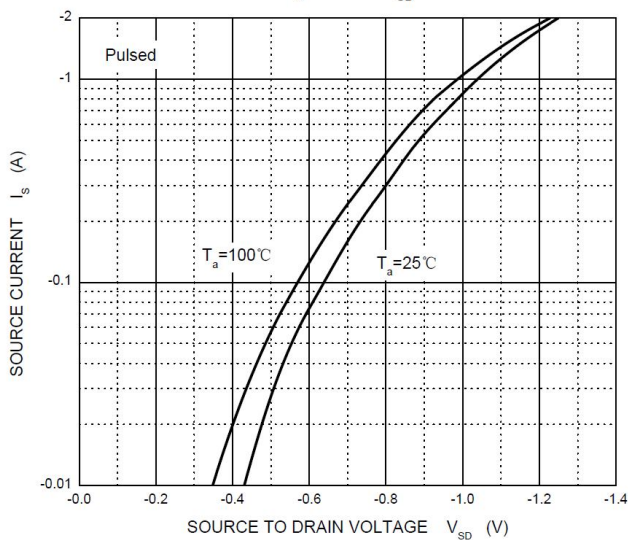
$R_{DS(ON)}$ — I_D



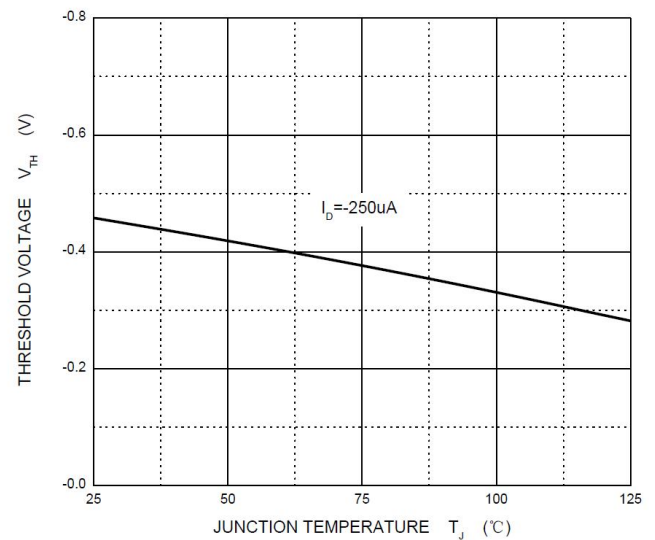
$R_{DS(ON)}$ — V_{GS}



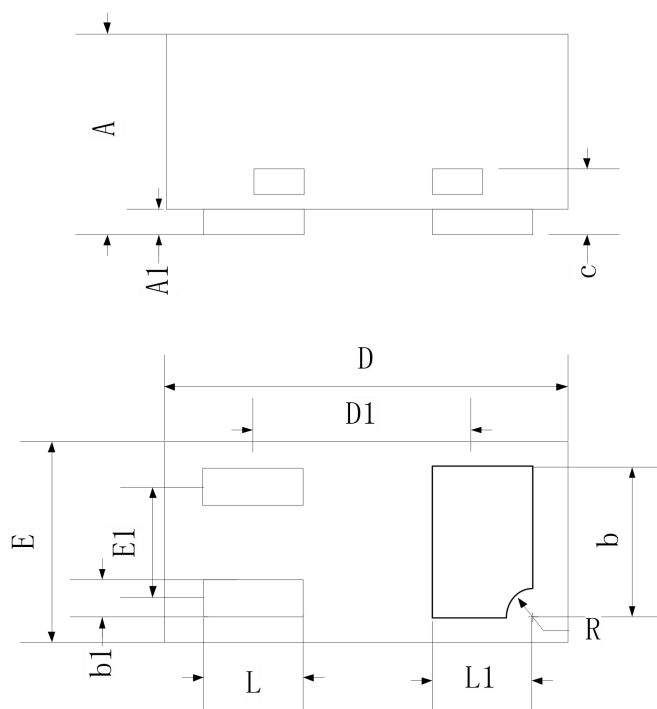
I_S — V_{SD}



Threshold Voltage

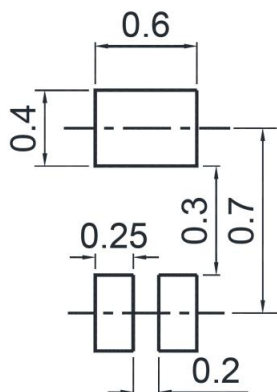


DFN1006-3B Package Information



Symbol	Dimensions in millimeters	
	Min.	Max.
A	0.36	0.40
A1	0	0.05
b	0.45	0.55
b1	0.1	0.2
c	0.08	0.18
D	0.95	1.05
D1	0.65	
E	0.55	0.65
E1	0.325	
L	0.2	0.3
L1	0.2	0.3
R	0.05	0.15

Recommended PCB Layout (Unit: mm)



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