

# User Guide to the LoRa® PicoCell Gateway V1.0

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# 1 Introduction

This user guide introduces the Semtech LoRa® PicoCell Gateway V1.0 reference design and how to set it up with a Raspberry Pi 2.



Figure 1: LoRa® PicoCell Gateway V1.0

The LoRa® PicoCell Gateway V1.0 (called here after “PicoCell GW”) is a multi-channel high performance transceiver designed to simultaneously receive several LoRa® packets using random spreading factors.

Three hardware reference designs are made available to address:

- Europe 868 MHz ISM band [863 MHz - 870 MHz]
- USA/Australia 915 MHz ISM band [902 MHz - 928 MHz]
- China 470 MHz ISM band [470 MHz - 510 MHz]

For countries with a comparable regulatory framework (no LBT), choose the design corresponding to the right frequency.

## 2 Hardware Presentation

### 2.1 Absolute Maximum Ratings

Table 1: Absolute Maximum Ratings

Item	Minimum	Typical	Maximum	Unit
Maximum Supply Voltage	-0.3	5.0	6.0	V
Operating Temperature	0	25	70	°C
Maximum RF Input Level			-10	dBm

#### Notice!

The PicoCell Gateway V1.0 with SX1308 chip is designed for indoor applications with a metal alloy housing. It is strongly recommended to maintain the ambient operating temperature below 40 °C, to ensure that an ambient operating temperature of 70 °C is not exceeded in the housing.

The operating temperature range can be extended to “-40 °C to 85 °C” if the PicoCell uses a SX1301 chip instead of a SX1308 chip. See *Section 2.4 LoRa® PicoCell Gateway Block Diagram 2.4*.

### 2.2 External Host Processor Connection

The LoRaWAN protocol can be run through an external host processor. On the PicoCell GW reference design, there is a USB port type A to interface with the external host processor. This USB port is also used to provide the main 5.0 V power supply:

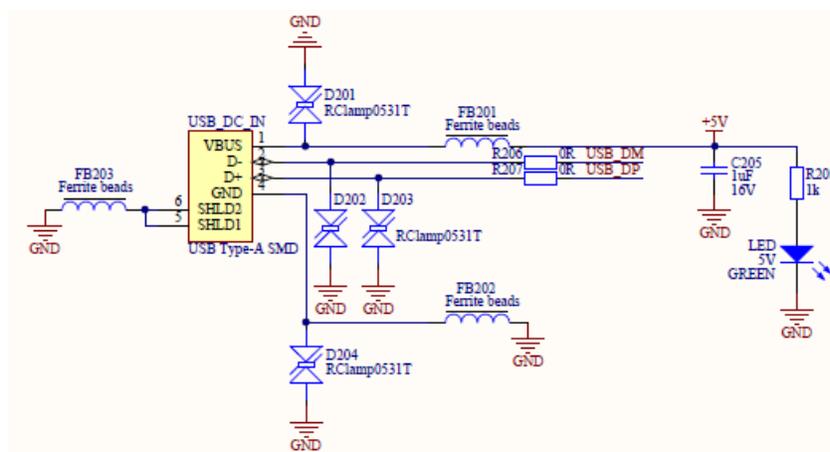


Figure 2: USB Port Type A for External Host Processor and Power Supply

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## 2.3 RF Front-End Architecture

The RF front-end architecture of the PicoCell GW displays the following characteristics:

- Half-duplex mode i.e. can't receive and transmit simultaneously
- Simultaneously receive 8 LoRa® channels multi-data rates (SF7 ~ SF12 / 125 kHz) + 2 mono-data rate (LoRa® 250 / 500 kHz and FSK 50 kbps)
- 3 ISM bands
  - o Europe 868MHz ISM band [863 MHz - 870 MHz]
  - o USA/Australia 915MHz ISM band [902 MHz - 928 MHz]
  - o China 470MHz ISM band [470 MHz - 510 MHz]
- Maximum transmit output power = +20dBm
- Typical sensitivity level:
  - o -139 dBm at SF12 BW 125 kHz
  - o -125 dBm at SF7 BW 125 kHz
  - o -109 dBm at FSK 50 kbps
- Ability to work in hostile RF environments such as close to cellular mobile phones, WiFi routers, Bluetooth devices

## 2.4 PicoCell Gateway Block Diagram

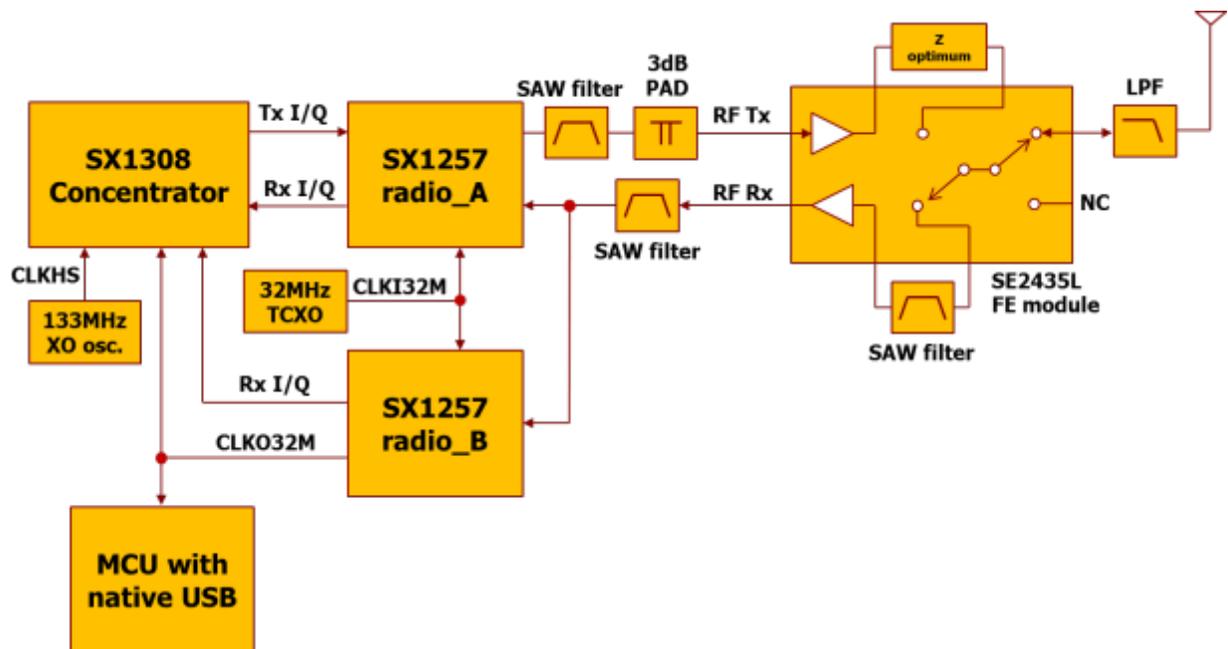


Figure 3: PicoCell Gateway V1.0 RF Block Diagram

- The SX1308 digital baseband chip is a massive digital signal processing engine which integrates the LoRa® Concentrator IP
- The two SX1257 transceivers are highly integrated RF front-end to digital I and Q modulator and demodulator. Two transceivers are used instead of one to be able to simultaneously receive 8 LoRa® 200 kHz channels. To address the Chinese market (470 MHz band), SX1255 transceivers are used instead of SX1257 transceivers.



## 2.6 Power Consumption

Table 2: Typical Current Consumption at 5.0 V

Mode	Description	Typical Current Consumption	Unit
8 Rx channels ON Tx OFF	HAL packet_forwarder	330	mA
8 Rx channels OFF Tx ON at 20 dBm	HAL util_tx_continuous	230	mA

The maximum current consumption (mA) in Rx mode with 8 channels ON is displayed in the table below:

Table 3: Maximum Current Consumption per Supply Rail in Rx Mode

Maximum current consumption (mA) in Rx mode with 8 channels ON

Part	Description	Current consumption per supply rail [mA]					Total
		1.8V	3.3V	3.3V	3.3V	2.0V	
		VCCDIG18	VCCDIG33	VCC_A	VCC_B	VCC_FEM	
SX1308	Baseband processor	800	10				
END4643A	133MHz oscillator		60				
END4243A	32MHz TCXO			2			
SX1257	Receiver A			25			
SX1257	Receiver B				25		
SE2435*	Front-end module (PA, LNA, switch)					7	
STM32F401CDU6	MCU		25				
Current on individual power supply [mA]		800	95	27	25	7	
Main Voltage [V]		5	5	5	5	5	
Regulator Voltage [V]		1.8	3.3	3.3	3.3	3.3	
Regulator efficiency** [%]		85	90	-	-	-	
Current @ +5.0V [mA]		339	70	27	25	7	468 [mA]
Power @ +5.0V [mW]		1695	350	135	125	35	2340 [mW]

\* Maximum estimated Current consumption with 20% margin taken on typical current value when max. not specified

\*\* Semtech SC283 step-down regulator

The maximum current consumption (mA) in Tx mode i.e. when downlink packet is sent by the concentrator board is displayed in the table below:

**Table 4: Maximum Current Consumption per Supply Rail in Tx Mode**

Maximum current consumption (mA) in Tx mode @ 20 dBm

Device	Description	Current consumption per supply rail [mA]					Total
		1.8V	3.3V	3.3V	3.3V	2.0V	
		VCCDIG18	VCCDIG33	VCC_A	VCC_B	VCC_FEM	
SX1308	Baseband processor	400	10				
END4643A	133MHz oscillator		0				
END4243A	32MHz TCXO			2			
SX1257	Receiver A			85			
SX1257	Receiver B				2		
SE2435*	Front-end module (PA, LNA, switch)					150	
STM32F401CDU6	MCU		25				
Current on individual power supply [mA]		400	35	87	2	150	
Main Voltage [V]		5	5	5	5	5	
Regulator Voltage [V]		1.8	3.3	3.3	3.3	3.3	
Regulator efficiency** [%]		85	90	-	-	-	
Current @ +5.0V [mA]		169	26	87	2	150	<b>434</b> [mA]
Power @ +5.0V [mW]		845	130	435	10	750	<b>2170</b> [mW]

\* Maximum estimated Current consumption with 20% margin taken on typical current value when max. not specified

\*\* Semtech SC283 step-down regulator

### 3 Software Overview

The PicoCell GW software can be split in three main parts:

- The **packet forwarder** is a program running on the host of a LoRa® gateway that forwards RF packets received by the concentrator to a server through an IP/UDP link, and emits RF packets that are sent by the server.
- The **picoGW\_hal** is a host driver/HAL to build a PicoCell GW which communicates through USB or UART with a concentrator board based on Semtech SX1308 multi-channel modem and SX1257/SX1255 RF transceivers. The library implements a USB CDC (virtual com port) to communicate with the embedded MCU.
- The **picoGW\_mcu** is an MCU driver/HAL for the PicoCell GW concentrator board. This program runs on MCU (STM32-F401CD). The embedded firmware takes in charge the power management of the SX1308 during the downlink to respect the 500 mA max power constraint in the USB plug. It also implements a USB CDC protocol to bridge commands coming from host to the SX1308 SPI interface.

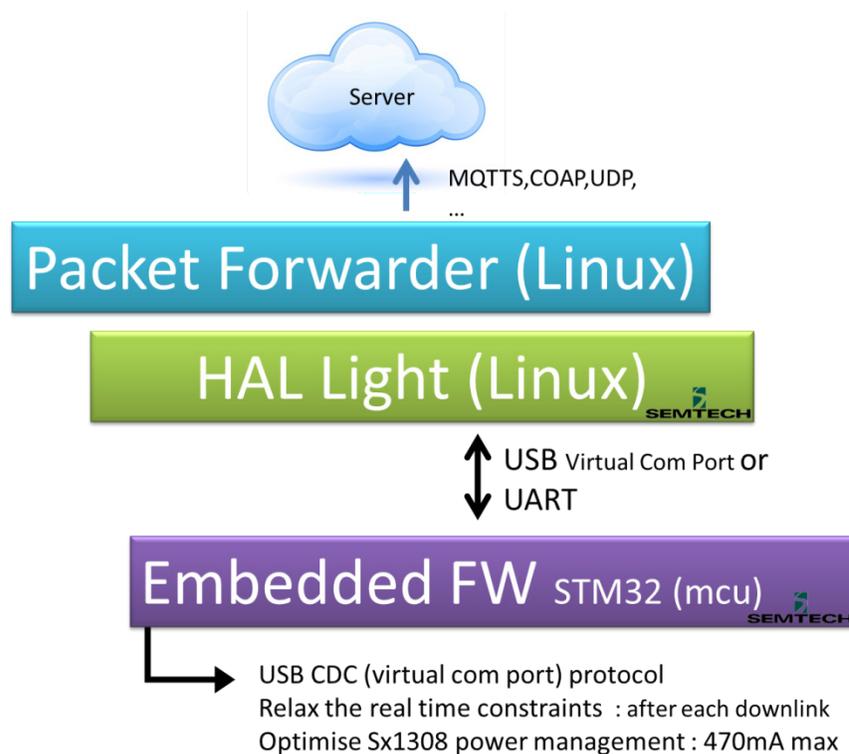


Figure 5: GW Software Overview

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The **packet\_forwarder** (gateway application) source code can be found under LoRa® Github:

[https://github.com/Lora-net/picoGW\\_packet\\_forwarder](https://github.com/Lora-net/picoGW_packet_forwarder)

For more details see the readme.md file in the picoGW\_packet\_forwarder directory.

The **picoGW\_hal** (SX130x control library) source code can be found under LoRa® Github:

[https://github.com/Lora-net/picoGW\\_hal](https://github.com/Lora-net/picoGW_hal)

For more details see the readme.md file in the picoGW\_hal directory.

The **picoGW\_mcu** driver/HAL source code can be found under LoRa® Github:

[https://github.com/Lora-net/picoGW\\_mcu](https://github.com/Lora-net/picoGW_mcu)

For more details see the readme.md file in the picoGW\_mcu directory.

The [PATH]/picoGW\_mcu/bin/ directory contains the precompiled binary files in .hex or .dfu (“Device Firmware Update”) formats. To load the binary file into the STM32F401CD target MCU, you can use a tool such as “dfu-util”: <http://dfu-util.sourceforge.net/>

For basic testing, utilities such as packet logger, util\_tx\_test, util\_tx\_continuous, are provided on the LoRa® Github repository:

[https://github.com/Lora-net/picoGW\\_hal](https://github.com/Lora-net/picoGW_hal)

[https://github.com/Lora-net/picoGW\\_packet\\_forwarder](https://github.com/Lora-net/picoGW_packet_forwarder)

### **Notice!**

The default configuration file “global\_conf.json” is given as an example and may need to be adapted to your design. Several configuration file examples are located in the following directory:

[PATH]/picoGW\_packet\_forwarder/lora\_pkt\_fwd/cfg/

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## 4 Use with Raspberry Pi

The Semtech LoRa® Concentrator reference design has been tested with Raspberry Pi 2 model B, Pi 3 model B as well as Pi zero w:

<https://www.raspberrypi.org/products/>

### 4.1 Raspberry Pi Connection

Simply connect the PicoCell GW to the Raspberry Pi through any of the USB Type A connectors as depicted on the picture below:



Figure 6: Raspberry Pi and PicoCell GW Connection

## 4.2 Raspberry Pi Image Software Installation

- Download the Raspbian image:
  - Go to address <https://www.raspberrypi.org/downloads/raspbian/>
  - Choose "RASPBIAN JESSIE LITE"
- Refer to following guide to setup your SD card with the downloaded image:  
<https://www.raspberrypi.org/documentation/installation/installing-images/>
  - Format the SD card:  
[https://www.sdcard.org/downloads/formatter\\_4/](https://www.sdcard.org/downloads/formatter_4/)

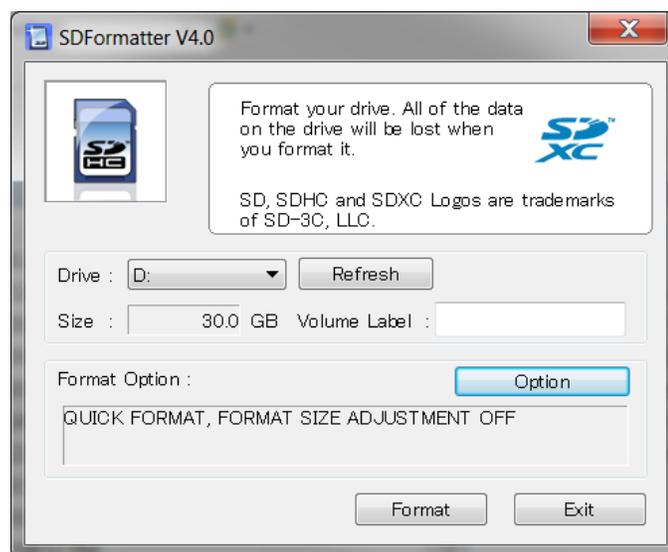


Figure 7: SDFormatter

- Write the image previously downloaded on the SD card:  
<https://sourceforge.net/projects/win32diskimager/>

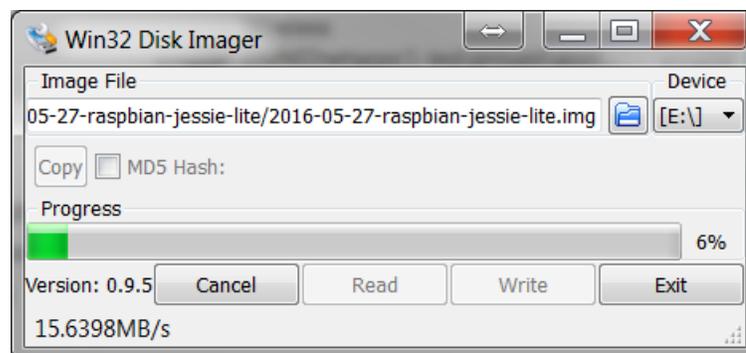


Figure 8: Win32 Disk Imager

## 4.3 Starting Raspberry Pi

Once the SD card is burned, insert it in the Raspberry Pi and choose a way to login Raspberry Pi:

- HDMI monitor and USB keyboard
- UART terminal (thanks to an extra USB to serial adapter)

Below is the description through an SSH client enabled from *raspi-config* tool, *Interfacing Option*

### 4.3.1 Login: pi and Password: raspberry

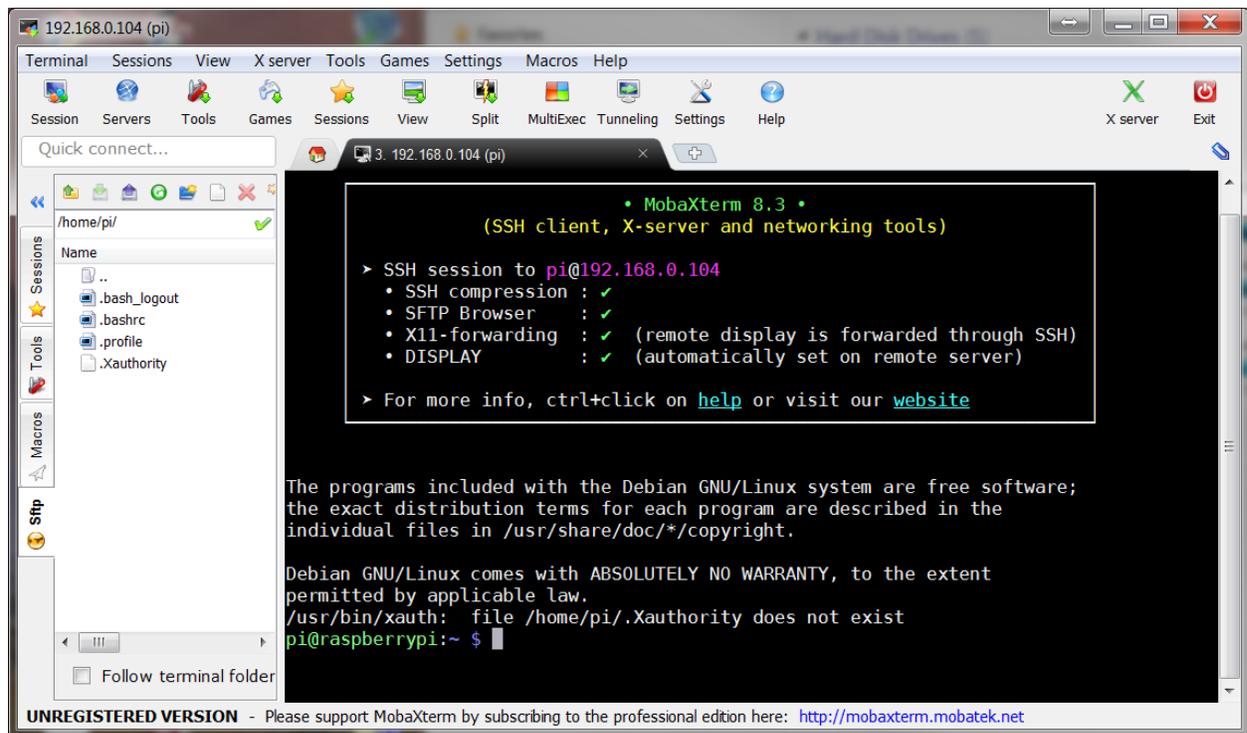


Figure 9: MobaXterm SSH Client

## 4.3.2 Resize Partition / FS

- On larger SD cards, the root partition can be resized to use extra space, using the *Expand Filesystem* option from raspi-config menu:

*\$ sudo raspi-config*

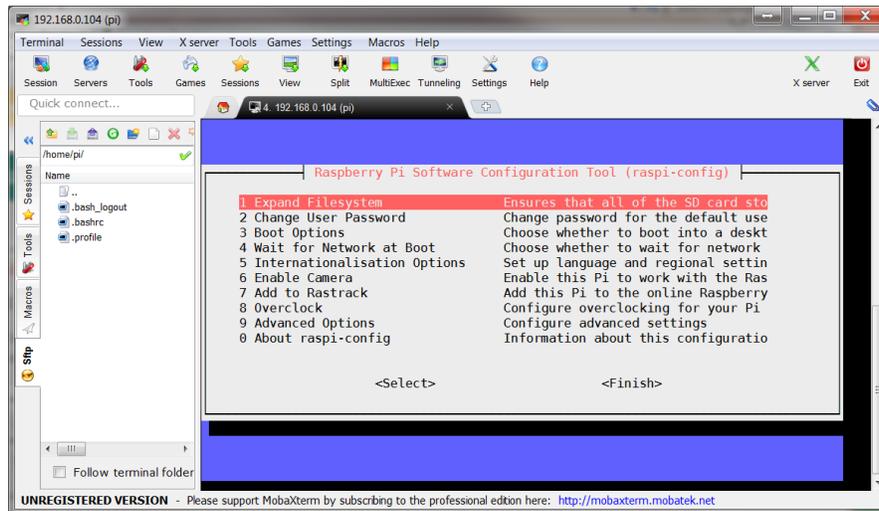


Figure 10: raspi-config Menu

- Select *1 Expand Filesystem* from raspi-config menu and press Enter:

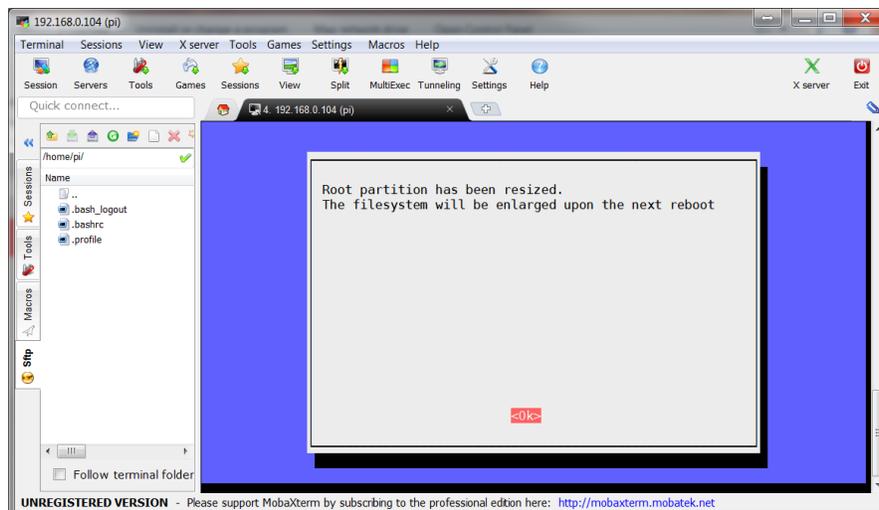


Figure 11: raspi-config "Expand Filesystem"

- The system must be then rebooted:

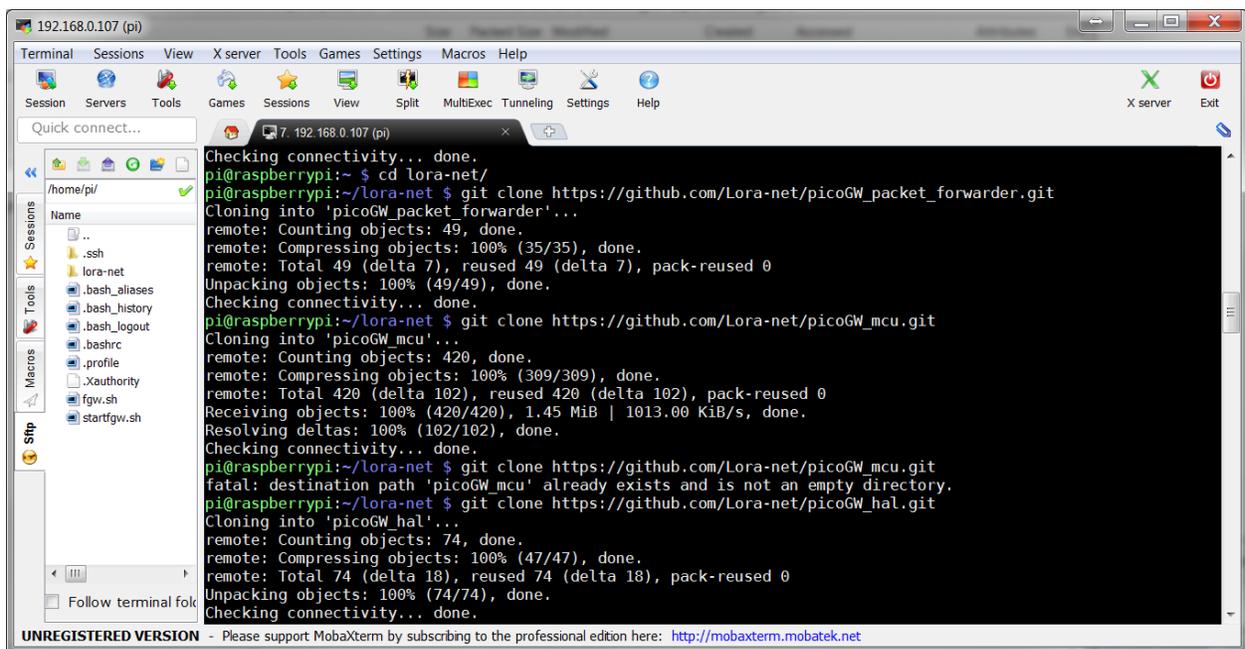
*\$ sudo reboot*

For more details, go to the following address:

<https://www.raspberrypi.org/documentation/configuration/raspi-config.md>

Get the latest Semtech software package from LoRa® Github (requires a connection to internet):

- `$ mkdir lora-net`
- `$ cd lora-net`
- `$ sudo apt-get update`
- `$ sudo apt-get install git`
- `$ git clone https://github.com/Lora-net/picoGW\_packet\_forwarder.git`
- `$ git clone https://github.com/Lora-net/picoGW\_mcu.git`
- `$ git clone https://github.com/Lora-net/picoGW\_hal.git`



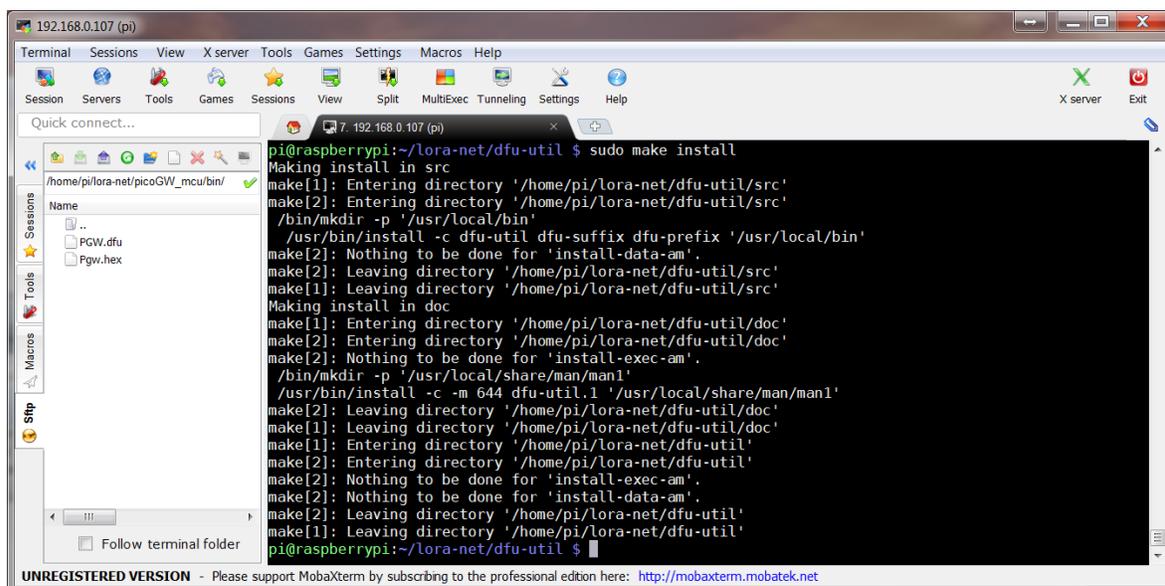
```
192.168.0.107 (pi)
Terminal Sessions View X server Tools Games Settings Macros Help
Session Servers Tools Games Sessions View Split MultiExec Tunneling Settings Help
Quick connect...
/home/pi/
Sessions
Name
..
.ssh
lora-net
Tools
.bash_aliases
.bash_history
.bash_logout
.bashrc
.profile
Xauthority
fgw.sh
startfgw.sh
Macros
Sftp
UNREGISTERED VERSION - Please support MobaXterm by subscribing to the professional edition here: http://mobaxterm.mobatek.net

Checking connectivity... done.
pi@raspberrypi:~ $ cd lora-net/
pi@raspberrypi:~/lora-net $ git clone https://github.com/Lora-net/picoGW_packet_forwarder.git
Cloning into 'picoGW_packet_forwarder'...
remote: Counting objects: 49, done.
remote: Compressing objects: 100% (35/35), done.
remote: Total 49 (delta 7), reused 49 (delta 7), pack-reused 0
Unpacking objects: 100% (49/49), done.
Checking connectivity... done.
pi@raspberrypi:~/lora-net $ git clone https://github.com/Lora-net/picoGW_mcu.git
Cloning into 'picoGW_mcu'...
remote: Counting objects: 420, done.
remote: Compressing objects: 100% (309/309), done.
remote: Total 420 (delta 102), reused 420 (delta 102), pack-reused 0
Receiving objects: 100% (420/420), 1.45 MiB | 1013.00 KiB/s, done.
Resolving deltas: 100% (102/102), done.
Checking connectivity... done.
pi@raspberrypi:~/lora-net $ git clone https://github.com/Lora-net/picoGW_mcu.git
fatal: destination path 'picoGW_mcu' already exists and is not an empty directory.
pi@raspberrypi:~/lora-net $ git clone https://github.com/Lora-net/picoGW_hal.git
Cloning into 'picoGW_hal'...
remote: Counting objects: 74, done.
remote: Compressing objects: 100% (47/47), done.
remote: Total 74 (delta 18), reused 74 (delta 18), pack-reused 0
Unpacking objects: 100% (74/74), done.
Checking connectivity... done.
```

Figure 12: Git Clones

## 4.3.3 Install dfu-util Tool

- `$ cd ~/lora-net/`
- `$ sudo apt-get install autoconf`
- `$ git clone https://git.code.sf.net/p/dfu-util/dfu-util`
- `$ cd dfu-util`
- `$ ./autogen.sh`
- `$ sudo apt-get install libusb-1.0-0-dev`
- `$ ./configure`
- `$ make`
- `$ sudo make install`



```
pi@raspberrypi:~/lora-net/dfu-util $ sudo make install
Making install in src
make[1]: Entering directory '/home/pi/lora-net/dfu-util/src'
make[2]: Entering directory '/home/pi/lora-net/dfu-util/src'
/bin/mkdir -p '/usr/local/bin'
/usr/bin/install -c dfu-util dfu-suffix dfu-prefix '/usr/local/bin'
make[2]: Nothing to be done for 'install-data-am'.
make[2]: Leaving directory '/home/pi/lora-net/dfu-util/src'
make[1]: Leaving directory '/home/pi/lora-net/dfu-util/src'
Making install in doc
make[1]: Entering directory '/home/pi/lora-net/dfu-util/doc'
make[2]: Entering directory '/home/pi/lora-net/dfu-util/doc'
make[2]: Nothing to be done for 'install-exec-am'.
/bin/mkdir -p '/usr/local/share/man/man1'
/usr/bin/install -c -m 644 dfu-util.1 '/usr/local/share/man/man1'
make[2]: Leaving directory '/home/pi/lora-net/dfu-util/doc'
make[1]: Leaving directory '/home/pi/lora-net/dfu-util/doc'
make[1]: Entering directory '/home/pi/lora-net/dfu-util'
make[2]: Entering directory '/home/pi/lora-net/dfu-util'
make[2]: Nothing to be done for 'install-exec-am'.
make[2]: Nothing to be done for 'install-data-am'.
make[2]: Leaving directory '/home/pi/lora-net/dfu-util'
make[1]: Leaving directory '/home/pi/lora-net/dfu-util'
pi@raspberrypi:~/lora-net/dfu-util $
```

Figure 13: Dfu-util Make Install

## 4.3.4 Load STM32F401CD MCU Binary

For the first time only, load the STM32F401CD MCU binary:

1. Press the "BOOT0" button of the PicoCell GW while plugging it to any USB port of the Raspberry Pi board:

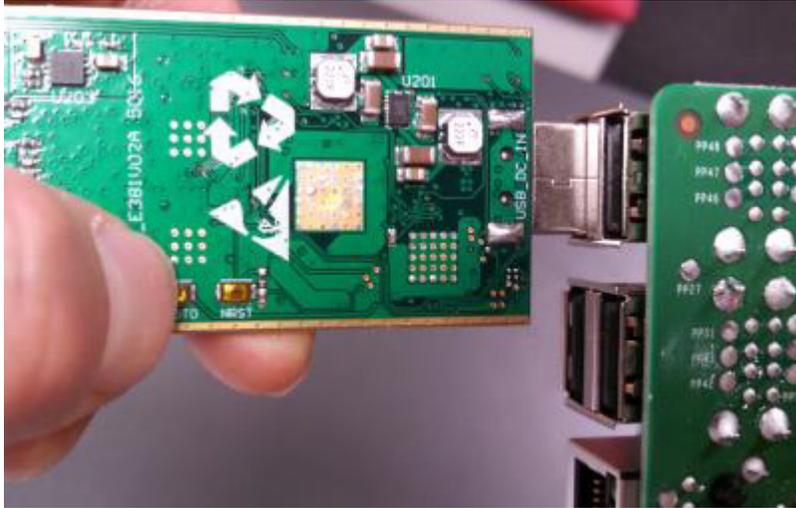


Figure 14: STM32 MCU Boot0 Button

2. On the Raspberry Pi, load the binary into the STM32F401CD MCU with the following command:

```
$ sudo /usr/local/bin/dfu-util -a 0 -D ~/lora-net/picoGW_mcu/bin/pgw_fw_usb.dfu
```

```
pi@raspberrypi:~/lora-net/picoGW_hal/util_boot $ sudo /usr/local/bin/dfu-util -a 0 -D ~/lora-net/picoGW_mcu/bin/pgw_fw_usb.dfu
dfu-util 0.9
Copyright 2005-2009 Weston Schmidt, Harald Welte and OpenMoko Inc.
Copyright 2010-2016 Tormod Volden and Stefan Schmidt
This program is Free Software and has ABSOLUTELY NO WARRANTY
Please report bugs to http://sourceforge.net/p/dfu-util/tickets/

Match vendor ID from file: 0483
Match product ID from file: 0000
Opening DFU capable USB device...
ID 0483:df11
Run-time device DFU version 011a
Claiming USB DFU Interface...
Setting Alternate Setting #0 ...
Determining device status: state = dfuERROR, status = 10
dfuERROR, clearing status
Determining device status: state = dfuIDLE, status = 0
dfuIDLE, continuing
DFU mode device DFU version 011a
Device returned transfer size 2048
DfuSe interface name: "Internal Flash"
file contains 1 DFU images
parsing DFU image 1
Target name: ST...
image for alternate setting 0, (1 elements, total size = 50932)
parsing element 1, address = 0x00000000, size = 50924
Download [=====] 100% 50924 bytes
Download done.
done parsing DfuSe file
pi@raspberrypi:~/lora-net/picoGW_hal/util_boot $
pi@raspberrypi:~/lora-net/picoGW_hal/util_boot $
```

Figure 15: Load STM32F401CD MCU Binary

For any future STM32F401CD MCU binary update, there should be no need to press the “BOOT0” button, simply connect the PicoCell GW to any USB port of the Raspberry Pi and type following commands:

- `$ lsusb`

To check Pico GW is recognized:

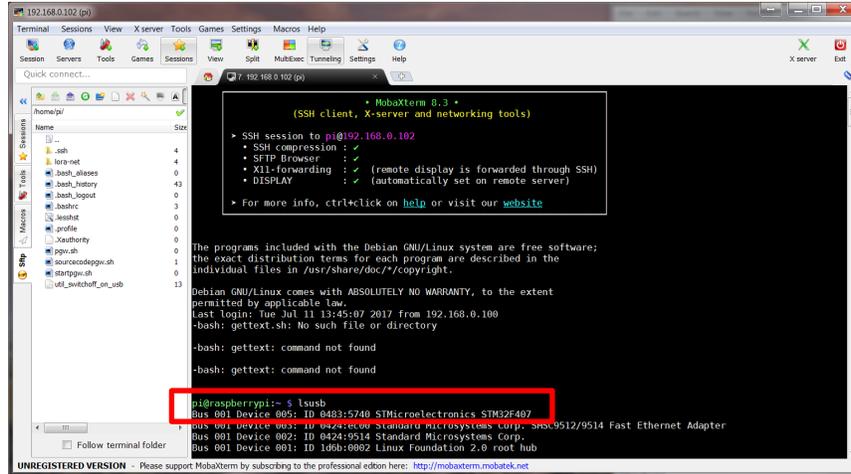


Figure 16: “lsusb” Command Result

- `$ dmesg`

To know the COM device to be used to access to the Pico GW (here “ttyACMo”):

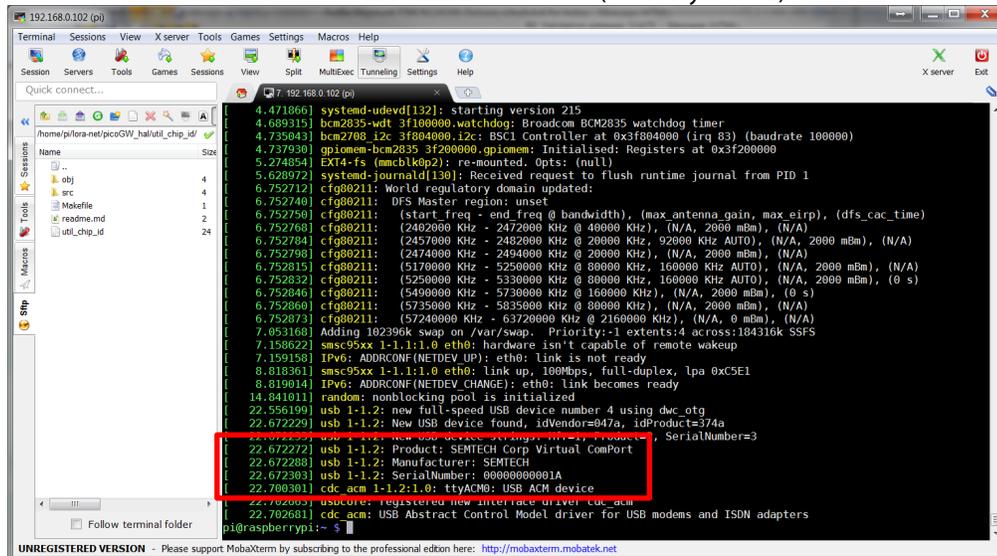


Figure 17: “dmesg” Command Result

- `$ cd ~/lora-net/picoGW_hal/util_boot`
- `$ make`

- `$. /util_boot -d /dev/ttyACM0`
- `$. lsusb`

To check Pico GW is in DFU (Device Firmware Update) mode:

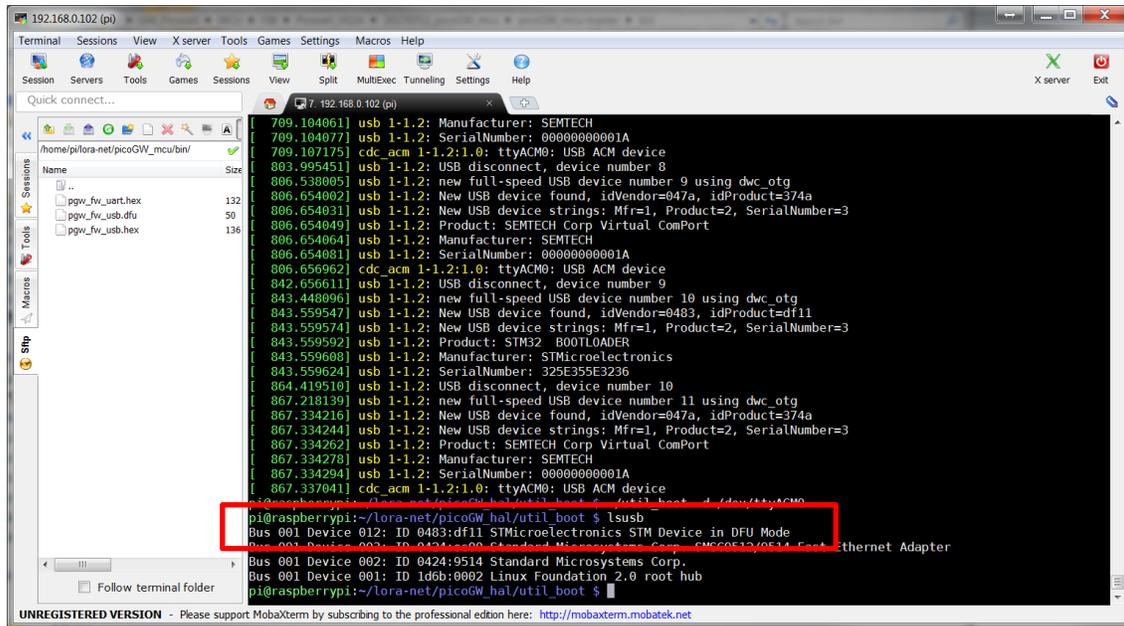


Figure 18: DFU Mode

- `$. sudo /usr/local/bin/dfu-util -a 0 -D ~/lora-net/picoGW_mcu/bin/pgw_fw_usb.dfu`

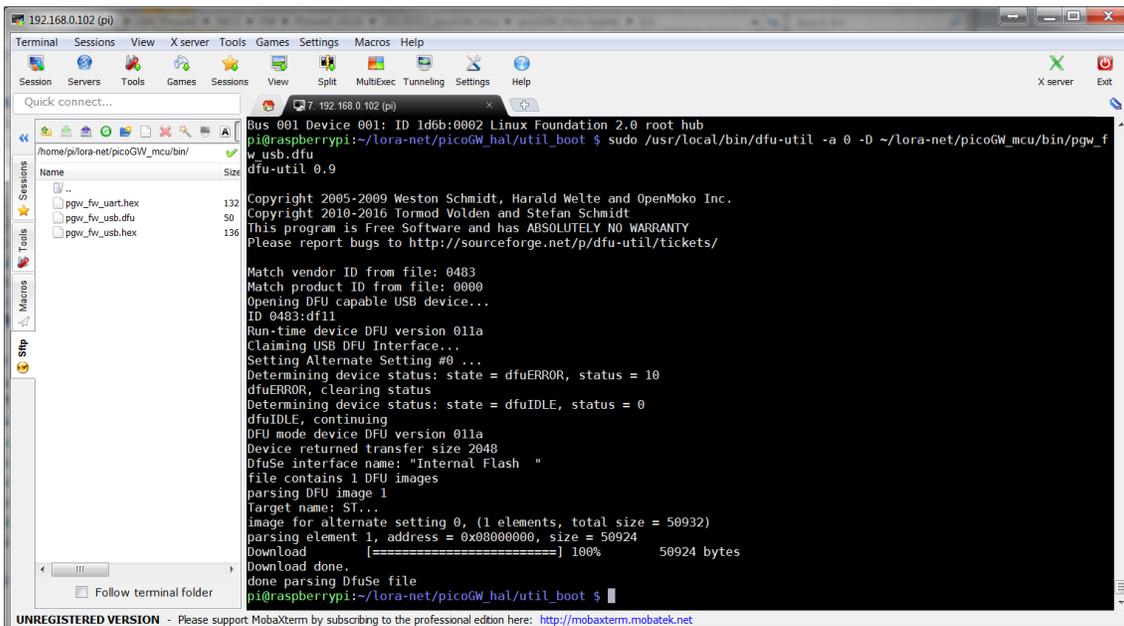


Figure 19: Update STM32F401CD MCU Binary

---

## Notice!

To exit the DFU (Device Firmware Update) mode, you need to unplug and plug again the PicoCell GW to the Raspberry Pi board. You can also do it by controlling the USB power of the Raspberry Pi board through a tool such as "hub-ctrl": <https://github.com/codazoda/hub-ctrl.c>

## 4.3.5 Compile Semtech HAL + Packet Forwarder

- `$ cd ~/lora-net/picoGW_hal`
- `$ make clean all`
- `$ cd ~/lora-net/picoGW_packet_forwarder`
- `$ make clean all`

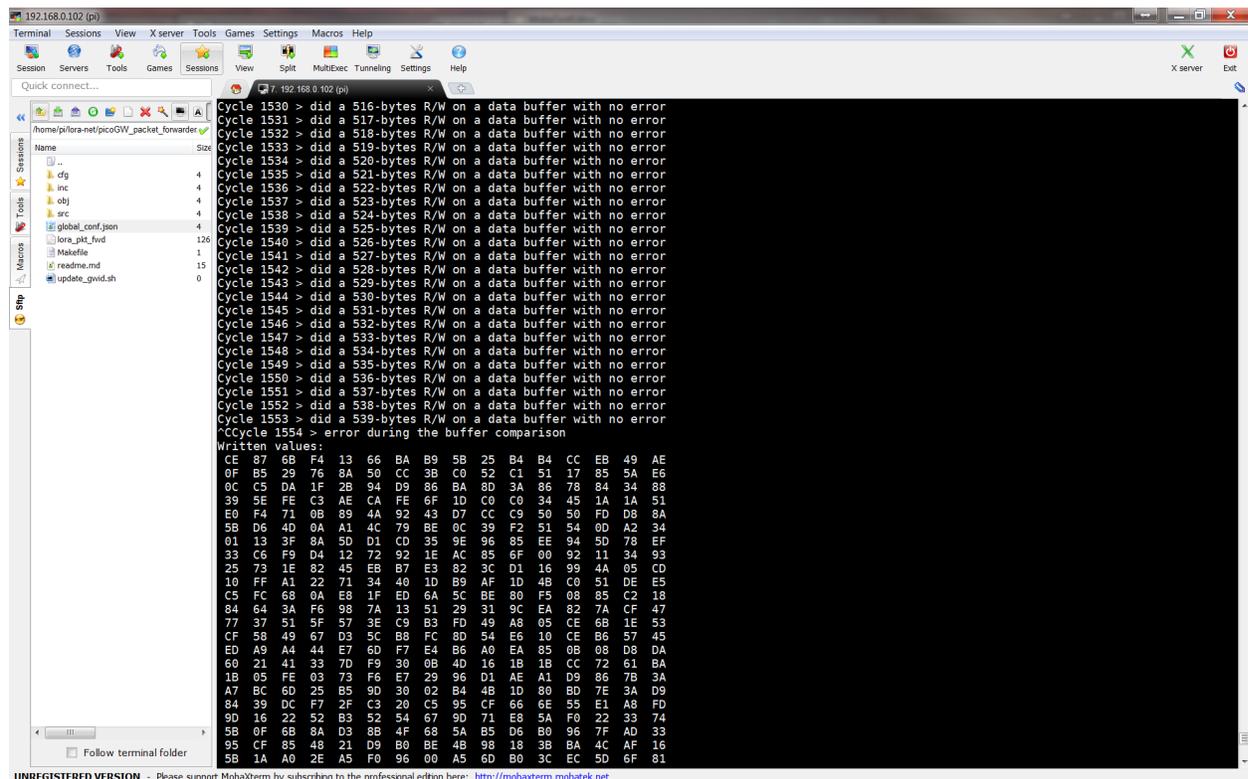
## 4.3.6 Semtech HAL Compilation Check

The program `util_com_stress` is used to check the reliability of the link between the host platform (on which the program is run) and the LoRa® concentrator register file that is the interface through which all interactions with the LoRa® concentrator happen.

The tests run endlessly or until an error is detected: press Ctrl+C to stop the application.

- `$ cd ~/lora-net/picoGW_hal/util_com_stress`
- `$ ./util_com_stress -t 4 -d /dev/ttyACMO`

The output looks like this:



```
Cycle 1530 > did a 516-bytes R/W on a data buffer with no error
Cycle 1531 > did a 517-bytes R/W on a data buffer with no error
Cycle 1532 > did a 518-bytes R/W on a data buffer with no error
Cycle 1533 > did a 519-bytes R/W on a data buffer with no error
Cycle 1534 > did a 520-bytes R/W on a data buffer with no error
Cycle 1535 > did a 521-bytes R/W on a data buffer with no error
Cycle 1536 > did a 522-bytes R/W on a data buffer with no error
Cycle 1537 > did a 523-bytes R/W on a data buffer with no error
Cycle 1538 > did a 524-bytes R/W on a data buffer with no error
Cycle 1539 > did a 525-bytes R/W on a data buffer with no error
Cycle 1540 > did a 526-bytes R/W on a data buffer with no error
Cycle 1541 > did a 527-bytes R/W on a data buffer with no error
Cycle 1542 > did a 528-bytes R/W on a data buffer with no error
Cycle 1543 > did a 529-bytes R/W on a data buffer with no error
Cycle 1544 > did a 530-bytes R/W on a data buffer with no error
Cycle 1545 > did a 531-bytes R/W on a data buffer with no error
Cycle 1546 > did a 532-bytes R/W on a data buffer with no error
Cycle 1547 > did a 533-bytes R/W on a data buffer with no error
Cycle 1548 > did a 534-bytes R/W on a data buffer with no error
Cycle 1549 > did a 535-bytes R/W on a data buffer with no error
Cycle 1550 > did a 536-bytes R/W on a data buffer with no error
Cycle 1551 > did a 537-bytes R/W on a data buffer with no error
Cycle 1552 > did a 538-bytes R/W on a data buffer with no error
Cycle 1553 > did a 539-bytes R/W on a data buffer with no error
^Ccycle 1554 > error during the buffer comparison
Written values:
CE 87 68 F4 13 66 BA B9 5B 25 B4 B4 CC EB 49 AE
0F B5 29 76 8A 50 CC 38 C0 52 C1 51 17 85 5A E6
0C C5 DA 1F 2B 94 D9 86 BA 8D 3A 86 78 84 34 88
39 5E FE C5 AE CA FE 6F 1D C0 C0 34 45 1A 1A 51
E0 F4 71 0B 89 4A 92 43 D7 CC C9 50 5D F0 D8 8A
5B D6 4D 0A A1 4C 79 BE 0C 39 F2 51 54 0D A2 34
01 13 3F 8A 5D D1 CD 35 9E 96 85 EE 94 5D 78 EF
33 C6 F9 D4 12 72 92 1E AC 85 6F 00 92 11 34 93
25 73 1E 82 45 EB B7 E3 82 3C D1 16 99 4A 05 CD
1B FF A1 22 71 34 40 1D B9 AF 1D 4B C0 51 DE E5
C5 FC 68 0A E8 1F ED 6A 5C BE 80 F5 09 85 C2 18
84 64 3A F6 98 7A 13 51 29 31 9C FA 82 7A CF 47
77 37 51 5F 57 3E C9 B3 FD 49 A8 05 CE 6B 1E 53
CF 58 49 67 D3 5C B8 FC 8D 54 E6 10 CE BE 57 45
ED A9 A4 44 E7 6D F7 E4 B6 A0 EA 85 0B 08 D8 DA
60 21 41 33 7D F9 30 0B 4D 16 1B 1B CC 72 61 BA
1B 05 FE 03 73 F6 E7 29 96 D1 AE A1 D9 86 7B 3A
A7 BC 6D 25 B5 9D 30 02 B4 4B 1D 80 BD 7E 3A D9
84 39 DC F7 2F C3 20 C5 95 CF 66 6E 55 E1 A8 FD
9D 16 22 52 B3 52 54 67 9D 71 E8 5A F0 22 33 74
5B 0F 6B 8A D3 8B 4F 68 5A B5 D6 B0 96 7F AD 33
95 CF 85 48 21 D9 B0 BE 4B 98 18 3B BA 4C AF 16
5B 1A A0 2E A5 F0 96 00 A5 6D B0 3C EC 5D 6F 81
```

Figure 20: Util\_com\_stress

## 4.3.7 Set a Unique ID to the Gateway

The PicoCell GW has a unique ID given at production. This ID can be used as a 64-bit MAC address for the PicoCell GW.

```
$ cd ~/lora-net/picoGW_hal/util_chip_id
```

```
$/util_chip_id -d /dev/ttyACM0
```

Return a unique ID like the following:

```
3535303229002e00
```

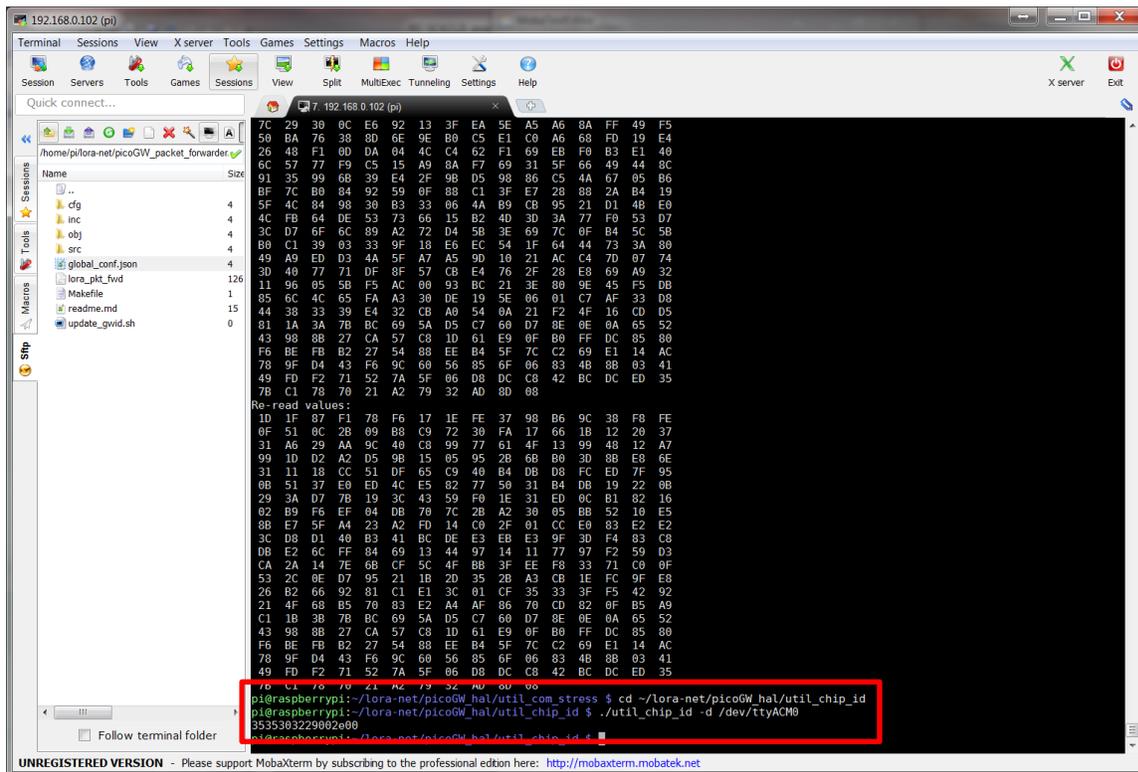


Figure 21: Util\_chip\_id

The gateway ID could be then replaced in the `global_conf.json` file within the repository:

```
~/lora-net/picoGW_packet_forwarder/lora_pkt_fwd
```

```
{"gateway_conf": {  
  "gateway_ID": "3535303229002e00"  
}  
}
```

---

The user can also create a local\_conf.json file in the repository:

*~/lora-net/picoGW\_packet\_forwarder/lora\_pkt\_fwd*

with the following line

```
{"gateway_conf": {  
  "gateway_ID": "3535303229002e00"  
}  
}
```

## 4.3.8 Run Basic Packet Forwarder

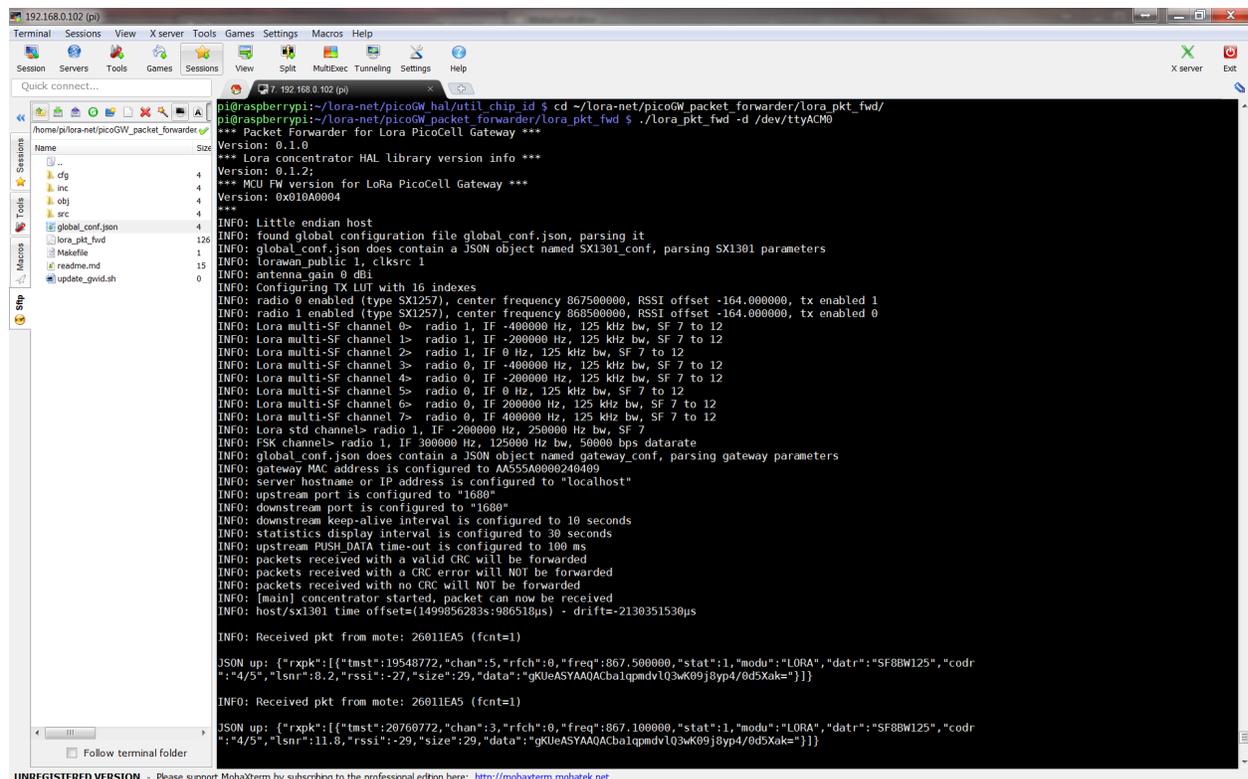
The Basic Packet Forwarder is a program running on the host of a LoRa® Gateway that forward RF packets received by the concentrator to a server through an IP/UDP link, and emits RF packets that are sent by the server.

Run Basic Packet Forwarder for a functional check:

```
$ cd ~/lora-net/picoGW_packet_forwarder/lora_pkt_fwd/
```

```
$. /lora_pkt_fwd -d /dev/ttyACM0
```

The output looks like this:



```
pi@raspberrypi:~/lora-net/picoGW_hal/util_chip_id $ cd ~/lora-net/picoGW_packet_forwarder/lora_pkt_fwd/
pi@raspberrypi:~/lora-net/picoGW_packet_forwarder/lora_pkt_fwd $ ./lora_pkt_fwd -d /dev/ttyACM0
*** Packet Forwarder for LoRa PicoCell Gateway ***
Version: 0.1.0
*** Lora concentrator HAL library version info ***
*** MCU FW version for LoRa PicoCell Gateway ***
Version: 0x010A0004
***
INFO: Little endian host
INFO: found global configuration file global_conf.json, parsing it
INFO: global_conf.json does contain a JSON object named SX1301_conf, parsing SX1301 parameters
INFO: lorawan_public 1, clksrc 1
INFO: antenna_gain 0 dBi
INFO: Configuring TX LUT with 16 indexes
INFO: radio 0 enabled (type SX1257), center frequency 867500000, Rssi offset -164.000000, tx enabled 1
INFO: radio 1 enabled (type SX1257), center frequency 868500000, Rssi offset -164.000000, tx enabled 0
INFO: Lora multi-SF channel 0> radio 1, IF -400000 Hz, 125 kHz bw, SF 7 to 12
INFO: Lora multi-SF channel 1> radio 1, IF -200000 Hz, 125 kHz bw, SF 7 to 12
INFO: Lora multi-SF channel 2> radio 1, IF 0 Hz, 125 kHz bw, SF 7 to 12
INFO: Lora multi-SF channel 3> radio 0, IF -400000 Hz, 125 kHz bw, SF 7 to 12
INFO: Lora multi-SF channel 4> radio 0, IF -200000 Hz, 125 kHz bw, SF 7 to 12
INFO: Lora multi-SF channel 5> radio 0, IF 0 Hz, 125 kHz bw, SF 7 to 12
INFO: Lora multi-SF channel 6> radio 0, IF 200000 Hz, 125 kHz bw, SF 7 to 12
INFO: Lora multi-SF channel 7> radio 0, IF 400000 Hz, 125 kHz bw, SF 7 to 12
INFO: Lora std channels> radio 1, IF -200000 Hz, 250000 Hz bw, SF 7
INFO: FSK channels> radio 1, IF 300000 Hz, 125000 Hz bw, 50000 bps datarate
INFO: global_conf.json does contain a JSON object named gateway_conf, parsing gateway parameters
INFO: gateway MAC address is configured to AA555A0000240409
INFO: server hostname or IP address is configured to "localhost"
INFO: upstream port is configured to "1689"
INFO: downstream port is configured to "1689"
INFO: downstream keep-alive interval is configured to 10 seconds
INFO: statistics display interval is configured to 30 seconds
INFO: upstream PUSH_DATA time-out is configured to 100 ms
INFO: packets received with a valid CRC will be forwarded
INFO: packets received with a CRC error will NOT be forwarded
INFO: [main] concentrator started, packet can now be received
INFO: host/sx1301 time offset=(1499856283s:986510µs) - drift=-2130351530µs

INFO: Received pkt from mote: 26011EA5 (fcnt=1)

JSON up: [{"pk": [{"tst":19548772,"chan":5,"fch":0,"freq":867.500000,"stat":1,"modu":"LORA","datr":"SF8BW12S","codr":"4/5","lsnr":8.2,"rssi":-27,"size":29,"data":"gkUeASYAAQACba1qpmv1Q3wK09j8yp4/0d5Xak="}]]

INFO: Received pkt from mote: 26011EA5 (fcnt=1)

JSON up: [{"pk": [{"tst":20760772,"chan":3,"fch":0,"freq":867.100000,"stat":1,"modu":"LORA","datr":"SF8BW12S","codr":"4/5","lsnr":11.8,"rssi":-29,"size":29,"data":"gkUeASYAAQACba1qpmv1Q3wK09j8yp4/0d5Xak="}]]
```

Figure 22: Basic Packet Forwarder

---

## 5 JSON file for RF Parameter Tuning

Edit the file `~/lora-net/picoGW_packet_forwarder/lora_pkt_fwd/global_conf.json` to update the following RF parameters:

- *freq*, *radio* and *if* to set frequency channels
  - o Frequency channels = [*freq* of selected *radio* + *if*] in Hz
- *rssi\_offset* to tune SX1257 + SX1308 RSSI
- 16 available gain tables *tx\_lut\_0* until *tx\_lut\_15* to tune Tx output power thanks to the 4

following parameters:

- o *pa\_gain*: no possible PA gain setting on PicoCell GW V1.0, so keep it to 0
- o *mix\_gain*: 11 possible mixer gain settings from 5 (min. gain) to 15 (max. gain)
- o *rf\_power*: RF output power target in dBm
- o *dig\_gain*: 4 possible digital gain settings from 0 (max. gain) to 3 (min. gain)

Within a Tx gain table index, the setting {*pa\_gain*, *mix\_gain*, *dig\_gain*} must correspond to the RF output power target defined in the parameter *rf\_power*.

A typical PicoCell GW *global\_conf.json* file looks like this:

```
{
  "SX1301_conf": {
    "lorawan_public": false,
    "clksrc": 1, /* radio_1 provides clock to concentrator */
    "antenna_gain": 0, /* antenna gain, in dBi */
    "radio_0": {
      "enable": true,
      "type": "SX1257",
      "freq": 867500000,
      "rssi_offset": -164.0,
      "tx_enable": true,
      "tx_freq_min": 863000000,
      "tx_freq_max": 870000000
    },
    "radio_1": {
      "enable": true,
      "type": "SX1257",
      "freq": 868500000,
      "rssi_offset": -164.0,
      "tx_enable": false
    },
    "chan_multiSF_0": {
      /* Lora MAC channel, 125kHz, all SF, 868.1 MHz */
      "enable": true,
      "radio": 1,
      "if": -400000
    },
    "chan_multiSF_1": {
```

```

/* Lora MAC channel, 125kHz, all SF, 868.3 MHz */
"enable": true,
"radio": 1,
"if": -200000
},
"chan_multiSF_2": {
/* Lora MAC channel, 125kHz, all SF, 868.5 MHz */
"enable": true,
"radio": 1,
"if": 0
},
"chan_multiSF_3": {
/* Lora MAC channel, 125kHz, all SF, 867.1 MHz */
"enable": true,
"radio": 0,
"if": -400000
},
"chan_multiSF_4": {
/* Lora MAC channel, 125kHz, all SF, 867.3 MHz */
"enable": true,
"radio": 0,
"if": -200000
},
"chan_multiSF_5": {
/* Lora MAC channel, 125kHz, all SF, 867.5 MHz */
"enable": true,
"radio": 0,
"if": 0
},
"chan_multiSF_6": {
/* Lora MAC channel, 125kHz, all SF, 867.7 MHz */
"enable": true,
"radio": 0,
"if": 200000
},
"chan_multiSF_7": {
/* Lora MAC channel, 125kHz, all SF, 867.9 MHz */
"enable": true,
"radio": 0,
"if": 400000
},
"chan_Lora_std": {
/* Lora MAC channel, 250kHz, SF7, 868.3 MHz */
"enable": true,
"radio": 1,
"if": -200000,
"bandwidth": 250000,
"spread_factor": 7
},
"chan_FSK": {
/* FSK 50kbps channel, 868.8 MHz */
"enable": true,
"radio": 1,
"if": 300000,
"bandwidth": 125000,
"datarate": 50000
},
"tx_lut_0": {
/* TX gain table, index 0 */
"pa_gain": 0,
"mix_gain": 5,
"rf_power": 9,
"dig_gain": 3
},
"tx_lut_1": {
/* TX gain table, index 1 */

```

```

    "pa_gain": 0,
    "mix_gain": 5,
    "rf_power": 9,
    "dig_gain": 3
  },
  "tx_lut_2": {
    /* TX gain table, index 2 */
    "pa_gain": 0,
    "mix_gain": 5,
    "rf_power": 9,
    "dig_gain": 3
  },
  "tx_lut_3": {
    /* TX gain table, index 3 */
    "pa_gain": 0,
    "mix_gain": 5,
    "rf_power": 9,
    "dig_gain": 3
  },
  "tx_lut_4": {
    /* TX gain table, index 4 */
    "pa_gain": 0,
    "mix_gain": 5,
    "rf_power": 9,
    "dig_gain": 3
  },
  "tx_lut_5": {
    /* TX gain table, index 5 */
    "pa_gain": 0,
    "mix_gain": 5,
    "rf_power": 9,
    "dig_gain": 3
  },
  "tx_lut_6": {
    /* TX gain table, index 6 */
    "pa_gain": 0,
    "mix_gain": 5,
    "rf_power": 9,
    "dig_gain": 3
  },
  "tx_lut_7": {
    /* TX gain table, index 7 */
    "pa_gain": 0,
    "mix_gain": 6,
    "rf_power": 11,
    "dig_gain": 3
  },
  "tx_lut_8": {
    /* TX gain table, index 8 */
    "pa_gain": 0,
    "mix_gain": 5,
    "rf_power": 13,
    "dig_gain": 2
  },
  "tx_lut_9": {
    /* TX gain table, index 9 */
    "pa_gain": 0,
    "mix_gain": 8,
    "rf_power": 14,
    "dig_gain": 3
  },
  "tx_lut_10": {
    /* TX gain table, index 10 */
    "pa_gain": 0,
    "mix_gain": 6,
    "rf_power": 15,

```

```

    "dig_gain": 2
  },
  "tx_lut_11": {
    /* TX gain table, index 11 */
    "pa_gain": 0,
    "mix_gain": 6,
    "rf_power": 16,
    "dig_gain": 1
  },
  "tx_lut_12": {
    /* TX gain table, index 12 */
    "pa_gain": 0,
    "mix_gain": 9,
    "rf_power": 17,
    "dig_gain": 3
  },
  "tx_lut_13": {
    /* TX gain table, index 13 */
    "pa_gain": 0,
    "mix_gain": 10,
    "rf_power": 18,
    "dig_gain": 3
  },
  "tx_lut_14": {
    /* TX gain table, index 14 */
    "pa_gain": 0,
    "mix_gain": 11,
    "rf_power": 19,
    "dig_gain": 3
  },
  "tx_lut_15": {
    /* TX gain table, index 15 */
    "pa_gain": 0,
    "mix_gain": 12,
    "rf_power": 20,
    "dig_gain": 3
  }
},

"gateway_conf": {
  "gateway_ID": "AA555A0000240409",
  /* change with default server address/ports, or overwrite in local_conf.json */
  "server_address": "localhost",
  "serv_port_up": 1680,
  "serv_port_down": 1680,
  /* adjust the following parameters for your network */
  "keepalive_interval": 10,
  "stat_interval": 30,
  "push_timeout_ms": 100,
  /* forward only valid packets */
  "forward_crc_valid": true,
  "forward_crc_error": false,
  "forward_crc_disabled": false
}
}
}

```

---

## 6 References

- [1] SX1308 information: <http://www.semtech.com/wireless-rf/rf-transceivers/sx1308/>
- [2] SX1257 datasheet: <http://www.semtech.com/images/datasheet/sx1257.pdf>

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## 7 Revision History

Version	Date	Modifications
1.0	May 2017	First Release
1.1	July 2017	Section 3: addition of of UART support Figure 5 updated accordingly "SX1301" replaced by "SX1308" Section 4.3.4, step 2: linux command change Figure 15 updated accordingly New Figure 16: "lsusb" command result New Figure 17: "dmesg" command result New linux command to get the COM device New Figure 18: DFU mode New Figure 19: Update STM32F401CD MCU Binary Linux commands change (add -d option parameter + .dfu name) Section 4.3.6: linux command change (add -d option parameter) + Figure 20: Util_com_stress updated accordingly Section 4.3.7: linux command change (add -d option parameter) Figure 21: Util_chip_id updated accordingly Section 4.3.8: linux command change (add -d option parameter) Figure 22: Basic Packet Forwarder updated accordingly

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## 8 Glossary

<b>BB</b>	BaseBand
<b>BoM</b>	Bill Of Materials
<b>BW</b>	BandWidth
<b>CLK</b>	Clock
<b>CW</b>	Continuous Wave
<b>ETSI</b>	European Telecommunications Standard Institute
<b>DFU</b>	Device Firmware Update
<b>EU</b>	Europe
<b>EUI</b>	Extended Unique Identifier
<b>GB</b>	GigaByte
<b>GPS</b>	Global Positioning System
<b>GW</b>	GateWay
<b>HAL</b>	Hardware Abstraction Layer
<b>HDMI</b>	High-Definition Multimedia Interface
<b>HW</b>	HardWare
<b>IP</b>	Intellectual Property
<b>ISM</b>	Industrial, Scientific and Medical applications
<b>LAN</b>	Local Area Network
<b>LBT</b>	Listen Before Talk
<b>LO</b>	Local Oscillator
<b>LoRa®</b>	LOng RANge modulation technique
<b>LoRaWAN</b>	LoRa® low power Wide Area Network protocol
<b>LPF</b>	Low Pass Filter
<b>LSB</b>	Least Significant Bit
<b>LUT</b>	Look Up Table
<b>MAC</b>	Media Access Control address
<b>MCU</b>	Micro-Controller Unit
<b>MPU</b>	Micro-Processing Unit
<b>PA</b>	Power Amplifier
<b>RSSI</b>	Received Signal Strength Indication
<b>RF</b>	Radio-Frequency
<b>RX</b>	Receiver
<b>SAW</b>	Surface Acoustic Wave filter
<b>SD Card</b>	Secure Digital Card
<b>SF</b>	Spreading Factor
<b>SPI</b>	Serial Peripheral Interface
<b>SPDT</b>	Single-Pole, Double-Throw switch
<b>SSH</b>	Secure SHell
<b>SW</b>	SoftWare
<b>TX</b>	Transmitter
<b>UART</b>	Universal Asynchronous Receiver/Transmitter
<b>UDP</b>	User Datagram Protocol
<b>USB</b>	Universal Serial Bus



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