



**RF360  
Europe GmbH**

## **Data sheet**

**SAW duplexer**  
Small cell & femtocell  
LTE band 30

Part number: B8207

Ordering code: B39242B8207P810

Date: July 16, 2020

Version: 2.0

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**Table of contents**

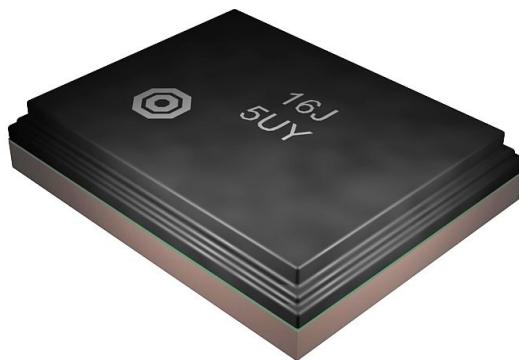
1 <a href="#">Application</a> .....	4
2 <a href="#">Features</a> .....	4
3 <a href="#">Package</a> .....	5
4 <a href="#">Pin configuration</a> .....	5
5 <a href="#">Matching circuit</a> .....	6
6 <a href="#">Characteristics</a> .....	7
7 <a href="#">Maximum ratings</a> .....	10
8 <a href="#">Transmission coefficients</a> .....	11
9 <a href="#">Reflection coefficients</a> .....	14
10 <a href="#">EVMs</a> .....	15
11 <a href="#">Packing material</a> .....	16
12 <a href="#">Marking</a> .....	20
13 <a href="#">Soldering profile</a> .....	21
14 <a href="#">Annotations</a> .....	22
15 <a href="#">Cautions and warnings</a> .....	23
16 <a href="#">ESD protection of SAW filters</a> .....	24
17 <a href="#">Important notes</a> .....	25

## 1 Application

- Low-loss SAW duplexer for small cell & femtocell systems
- Usable pass band 10 MHz
- RX = Uplink = 2305 MHz – 2315 MHz
- TX = Downlink = 2350 MHz – 2360 MHz
- Ultra low temperature drift

## 2 Features

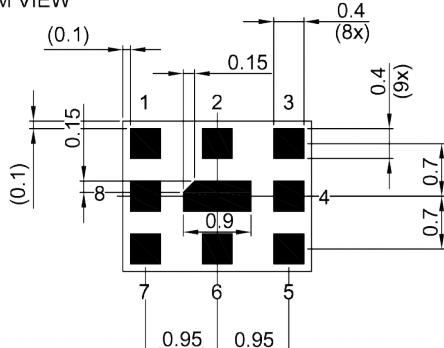
- Package size  $2.5 \pm 0.1$  mm  $\times$   $2.0 \pm 0.1$  mm
- Package height 0.5 mm (max.)
- Approximate weight 0.01 g
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 2a (MSL2a)



**Figure 1:** Picture of component with example of product marking.

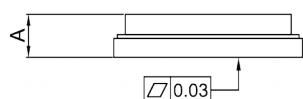
### 3 Package

### BOTTOM VIEW

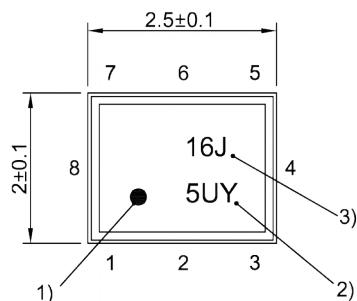


Pad and pitch tolerance  $\pm 0.05$

### SIDE VIEW

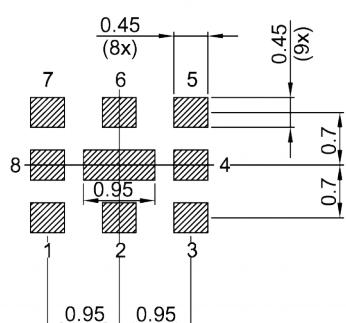


### TOP VIEW



- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number

Land pattern  
THRU VIEW



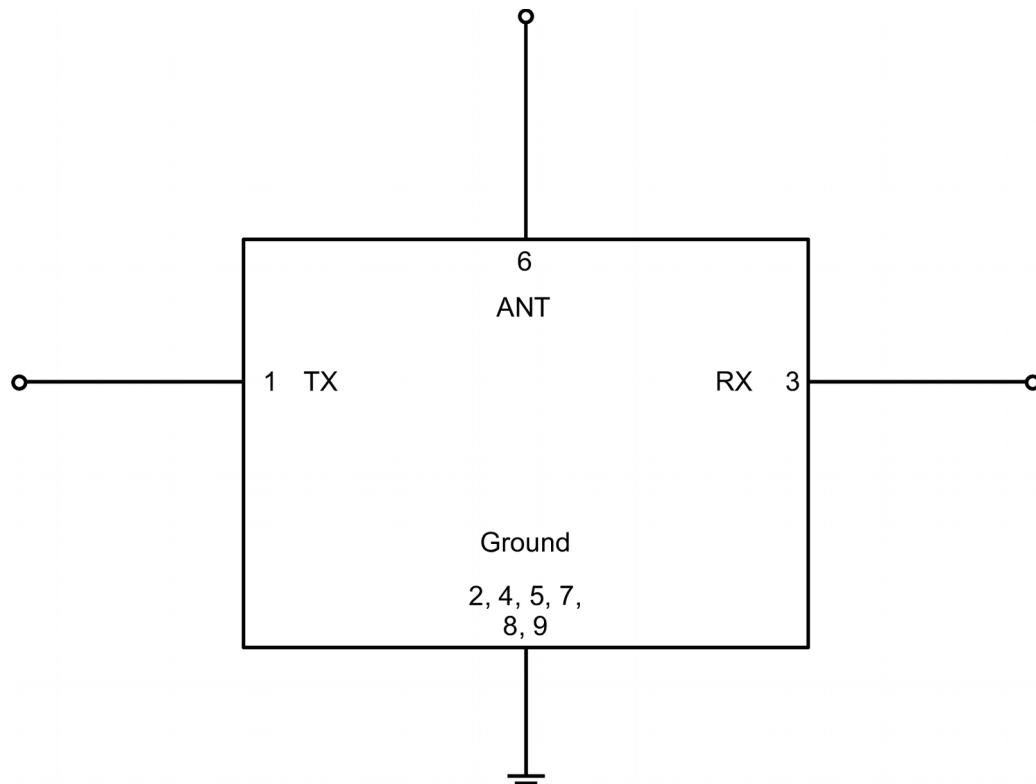
Landing pad tolerance -0.02

**Figure 2:** Drawing of package with package height A = 0.5 mm (max.). See Sec. Package information (p. 23).

## 4 Pin configuration

- 1 TX
- 3 RX
- 6 ANT
- 2, 4, 5, 7, 8, 9 Ground

## 5 Matching circuit



**Figure 3:** Schematic of matching circuit. No external matching components required.

## 6 Characteristics

### 6.1 TX – ANT

Temperature range for specification	$T_{\text{SPEC}}$	= $-40^{\circ}\text{C}^{\text{1)}} \dots +95^{\circ}\text{C}^{\text{1)}}$
TX terminating impedance	$Z_{\text{TX}}$	= $50\ \Omega$
ANT terminating impedance	$Z_{\text{ANT}}$	= $50\ \Omega$
RX terminating impedance	$Z_{\text{RX}}$	= $50\ \Omega$

Characteristics TX – ANT			min. for $T_{\text{SPEC}}$	typ. @ $+25^{\circ}\text{C}$	max. for $T_{\text{SPEC}}$	
<b>Center frequency</b>		$f_c$	—	2355	—	MHz
<b>Maximum insertion attenuation</b>		$\alpha_{\text{max}}$	—	2.0	2.8	dB
<b>Amplitude ripple (p-p)</b>	2350 ... 2360	MHz	$\Delta\alpha$	0.2	1.0	dB
<b>Maximum VSWR</b>			$\text{VSWR}_{\text{max}}$			
@ TX port	2350 ... 2360	MHz	—	1.4	2.0	
@ ANT port	2350 ... 2360	MHz	—	1.4	2.0	
<b>Maximum error vector magnitude</b>			$\text{EVM}_{\text{max}}^{\text{2)}}$			
	2352.5 ... 2357.5	MHz	—	0.5	2.5	%
<b>Minimum attenuation</b>			$\alpha_{\text{min}}$			
	10 ... 698	MHz	40	43	—	dB
	698 ... 896	MHz	38	41	—	dB
	1570 ... 1610	MHz	38	41	—	dB
	1850 ... 1915	MHz	38	41	—	dB
	1930 ... 1995	MHz	40	43	—	dB
	2305 ... 2315	MHz	50	54	—	dB
	2320 ... 2332.5	MHz	10	18	—	dB
	2400 ... 2496	MHz	40	45	—	dB
	2496 ... 2690	MHz	40	44	—	dB
	3550 ... 3700	MHz	40	50	—	dB
	4700 ... 4720	MHz	40	55	—	dB
	5150 ... 5850	MHz	20	55	—	dB

<sup>1)</sup> Same specification value for reduced temperature range -10 to  $85^{\circ}\text{C}$  thanks to ultra low temperature drift.

<sup>2)</sup> Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.

## 6.2 ANT – RX

Temperature range for specification	$T_{\text{SPEC}}$	= $-40^{\circ}\text{C}^{\text{1)}$ ... $+95^{\circ}\text{C}^{\text{1)}$
TX terminating impedance	$Z_{\text{TX}}$	= $50\ \Omega$
ANT terminating impedance	$Z_{\text{ANT}}$	= $50\ \Omega$
RX terminating impedance	$Z_{\text{RX}}$	= $50\ \Omega$

Characteristics ANT – RX			min. for $T_{\text{SPEC}}$	typ. @ $+25^{\circ}\text{C}$	max. for $T_{\text{SPEC}}$		
<b>Center frequency</b>		$f_c$	—	2310	—	MHz	
<b>Maximum insertion attenuation</b>		$\alpha_{\text{max}}$	—	2.0	3.0	dB	
<b>Amplitude ripple (p-p)</b>	2305... 2315	MHz	$\Delta\alpha$	—	0.3	1.0	dB
<b>Maximum VSWR</b>	2305... 2315	MHz	$\text{VSWR}_{\text{max}}$	—	—	—	
@ ANT port	2305... 2315	MHz	—	1.4	2.0	—	
@ RX port	2305... 2315	MHz	—	1.4	2.0	—	
<b>Maximum error vector magnitude</b>	2307.5... 2312.5	MHz	$\text{EVM}_{\text{max}}^{\text{2)}}$	—	0.5	2.5	%
<b>Minimum attenuation</b>		$\alpha_{\text{min}}$	—	—	—	—	
	10... 663	MHz	39	42	—	dB	
	663... 894	MHz	38	41	—	dB	
	1710... 1780	MHz	40	46	—	dB	
	1850... 1915	MHz	40	48	—	dB	
	1930... 1990	MHz	40	52	—	dB	
	2110... 2200	MHz	20	42	—	dB	
	2200... 2280	MHz	20	30	—	dB	
	2280... 2285	MHz	10	18	—	dB	
	2332.5... 2345	MHz	10	18	—	dB	
	2350... 2360	MHz	55	57	—	dB	
	2400... 2496	MHz	40	54	—	dB	
	2496... 2630	MHz	40	54	—	dB	
	2630... 2690	MHz	33	37	—	dB	
	4700... 4720	MHz	20	31	—	dB	
	5150... 5850	MHz	20	34	—	dB	

<sup>1)</sup> Same specification value for reduced temperature range -10 to  $85^{\circ}\text{C}$  thanks to ultra low temperature drift.<sup>2)</sup> Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.

### 6.3 TX – RX

Temperature range for specification	$T_{\text{SPEC}}$	= $-40^{\circ}\text{C}^{\text{1)}} \dots +95^{\circ}\text{C}^{\text{1)}}}$
TX terminating impedance	$Z_{\text{TX}}$	= $50\ \Omega$
ANT terminating impedance	$Z_{\text{ANT}}$	= $50\ \Omega$
RX terminating impedance	$Z_{\text{RX}}$	= $50\ \Omega$

Characteristics TX – RX		min. for $T_{\text{SPEC}}$	typ. @ $+25^{\circ}\text{C}$	max. for $T_{\text{SPEC}}$	
<b>Minimum isolation</b>	$\alpha_{\text{min}}$				
	2305... 2315 MHz	50	54	—	dB
	2350... 2360 MHz	55	59	—	dB

<sup>1)</sup> Same specification value for reduced temperature range -10 to  $85^{\circ}\text{C}$  thanks to ultra low temperature drift.

## 7 Maximum ratings

Operable temperature	$T_{OP} = -40 \text{ }^{\circ}\text{C} \dots +95 \text{ }^{\circ}\text{C}$	
Storage temperature	$T_{STG}^{1)} = -40 \text{ }^{\circ}\text{C} \dots +95 \text{ }^{\circ}\text{C}$	
DC voltage	$ V_{DC} ^{2)} = 0 \text{ V}$	
ESD voltage		
	$V_{ESD}^{3)} = 250 \text{ V}$	Human body model.
	$V_{ESD}^{4)} = 150 \text{ V}$	Machine model.
Input power	$P_{IN}$	
@ TX port: 2350 ... 2360 MHz	28 dBm <sup>5), 6)</sup>	5 MHz LTE downlink signal (25 RB) for 100000 h @ 55 °C. $P_{IN}$ 28 dBm average – 39 dBm peak. Source and load impedance 50Ω.
@ RX port: 2305 ... 2315 MHz	25 dBm <sup>5), 7)</sup>	5 MHz LTE uplink signal (25 RB) for 5000 h @ 55 °C. $P_{IN}$ 25 dBm average – 33 dBm peak. Source and load impedance 50Ω.

<sup>1)</sup> Not valid for packaging material. Storage temperature for packaging material is –25 °C to +40 °C.

<sup>2)</sup> In case of applied DC voltage blocking capacitors are mandatory.

<sup>3)</sup> According to JESD22-A114F (HBM – Human Body Model), 1 negative & 1 positive pulse.

<sup>4)</sup> According to JESD22-A115B (MM – Machine Model), 10 negative & 10 positive pulses.

<sup>5)</sup> Expected lifetime according to simulation and wear out models.

<sup>6)</sup>  $T_{SPEC}$  is the ambient temperature of the PCB at component position. Specified min./max values from section 6 "characteristics" for maximum input power 28 dBm are valid for temperature up to 55 °C.

<sup>7)</sup>  $T_{SPEC}$  is the ambient temperature of the PCB at component position. Specified min./max values from section 6 "characteristics" for maximum input power 25 dBm are valid for temperature up to 95 °C.

## 8 Transmission coefficients

### 8.1 TX – ANT

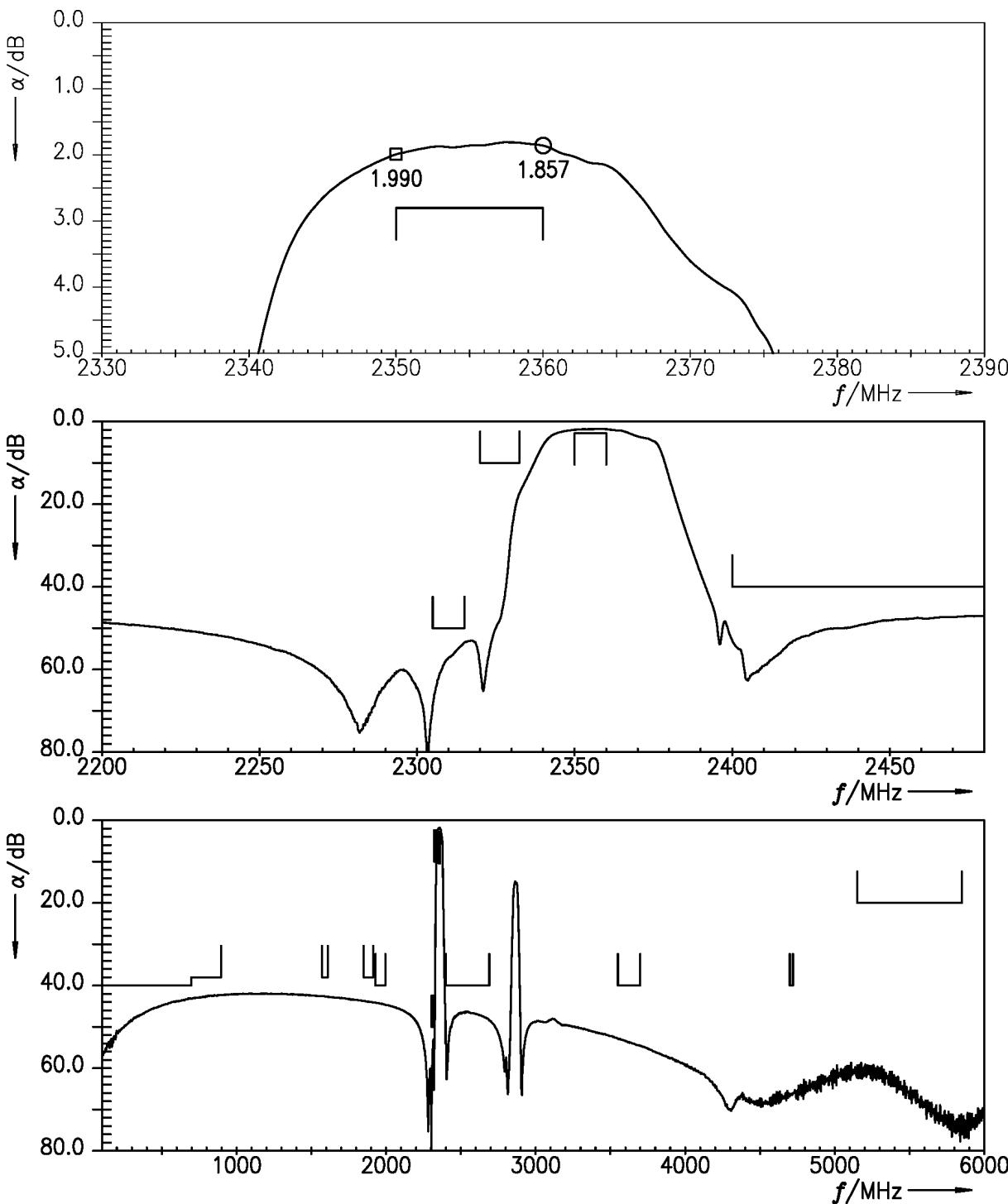


Figure 4: Attenuation TX – ANT.

## 8.2 ANT – RX

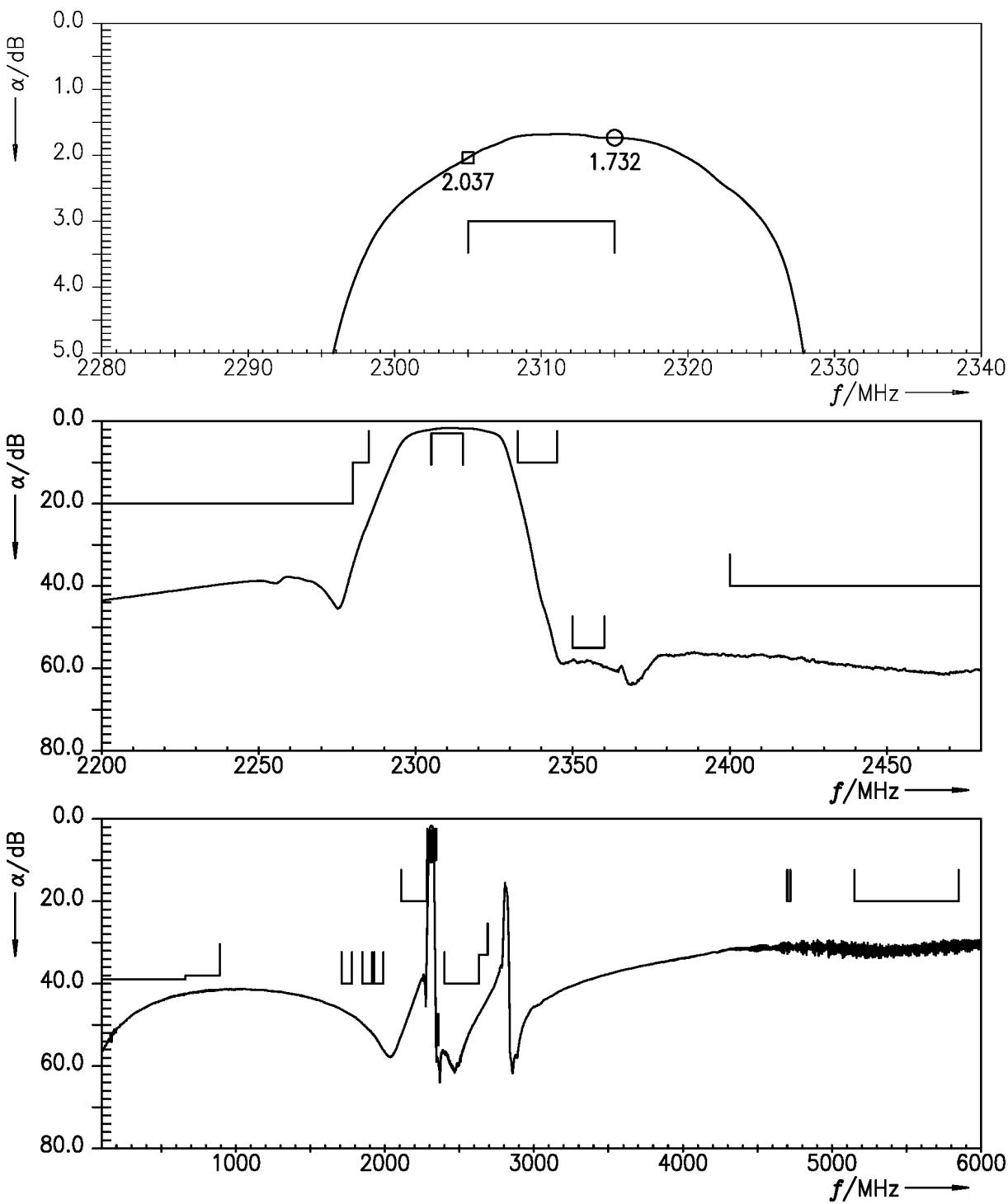


Figure 5: Attenuation ANT – RX.

## 8.3 TX – RX

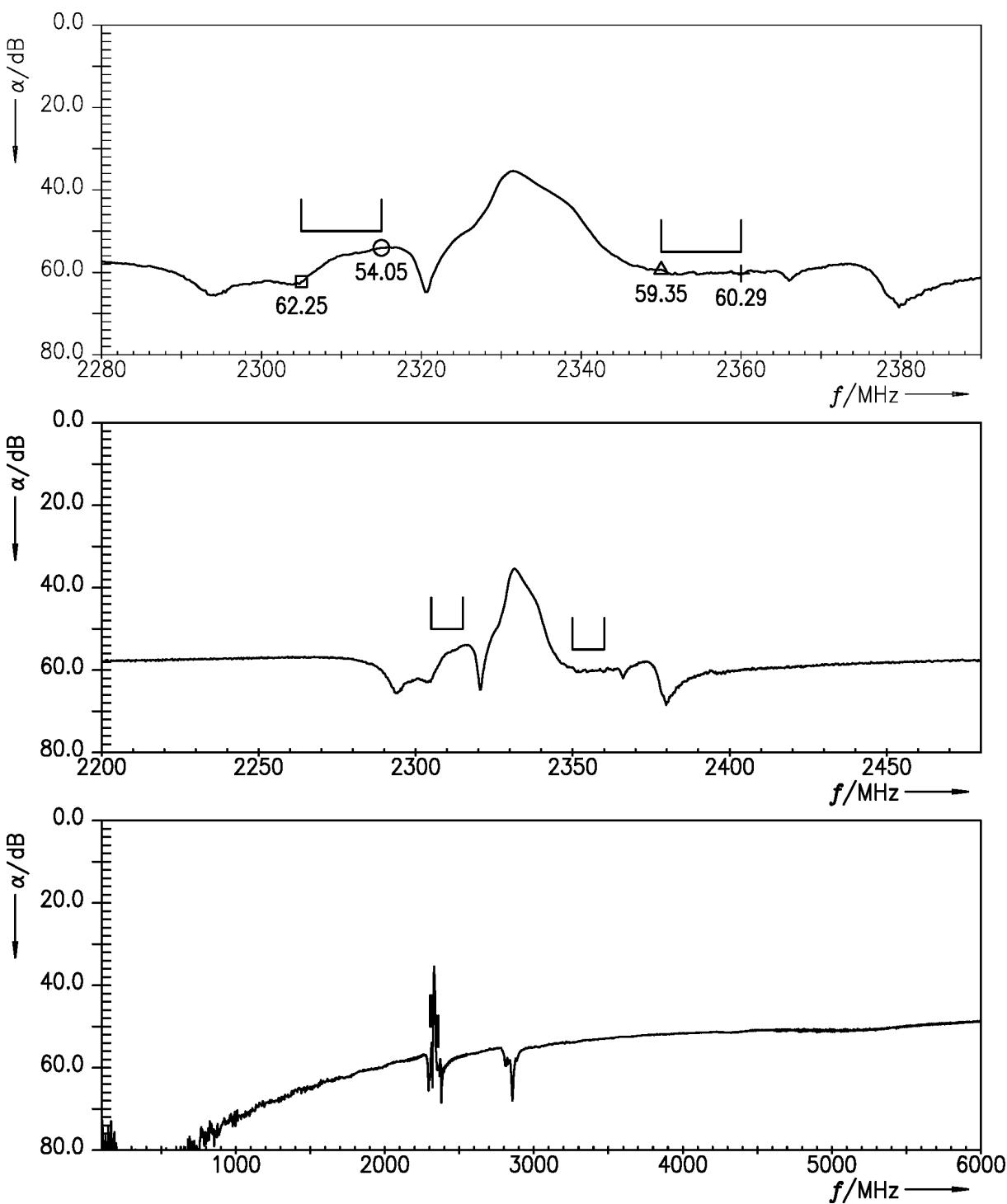


Figure 6: Isolation TX – RX.

## 9 Reflection coefficients

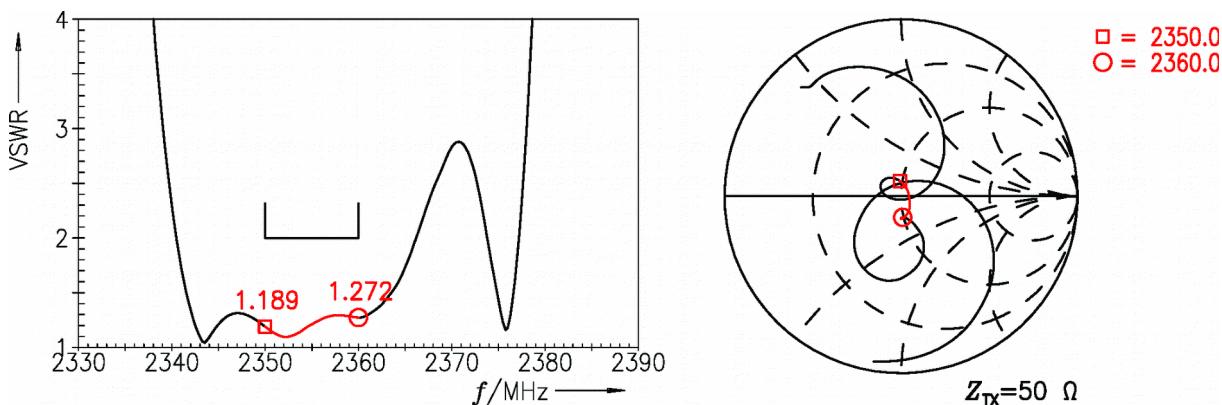


Figure 7: Reflection coefficient at TX port.

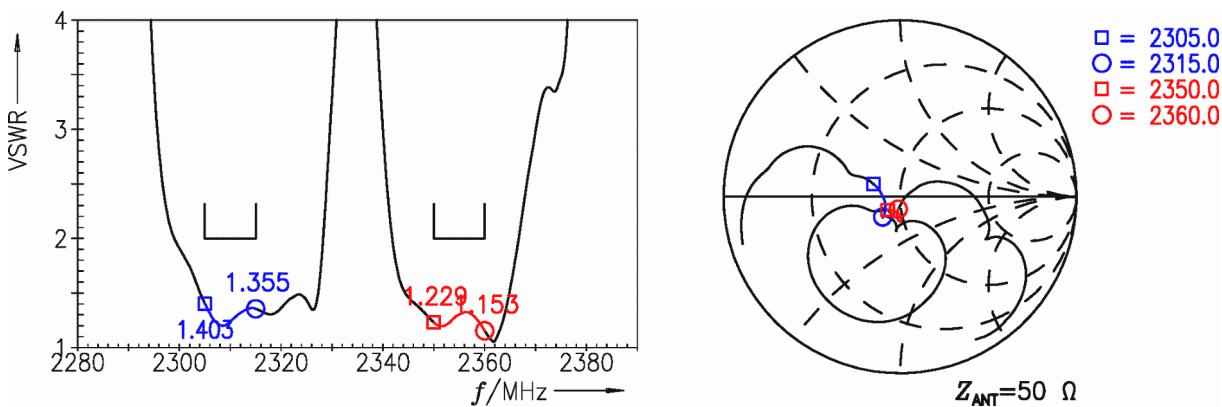


Figure 8: Reflection coefficient at ANT port.

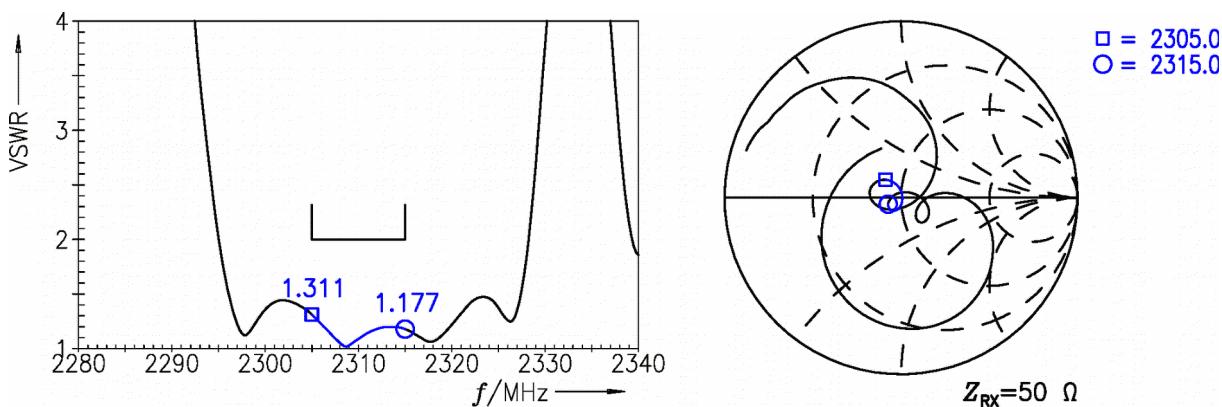
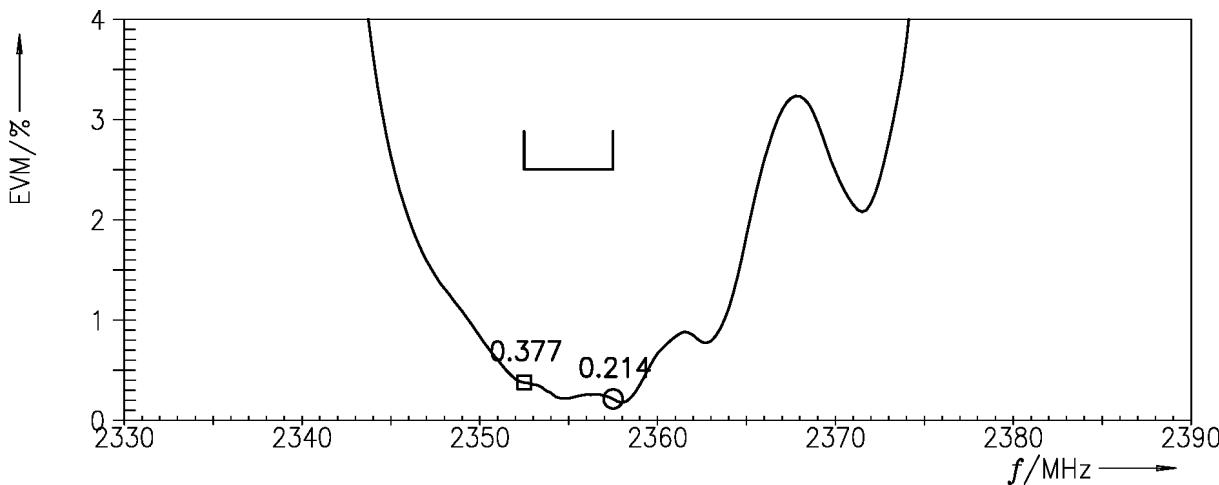
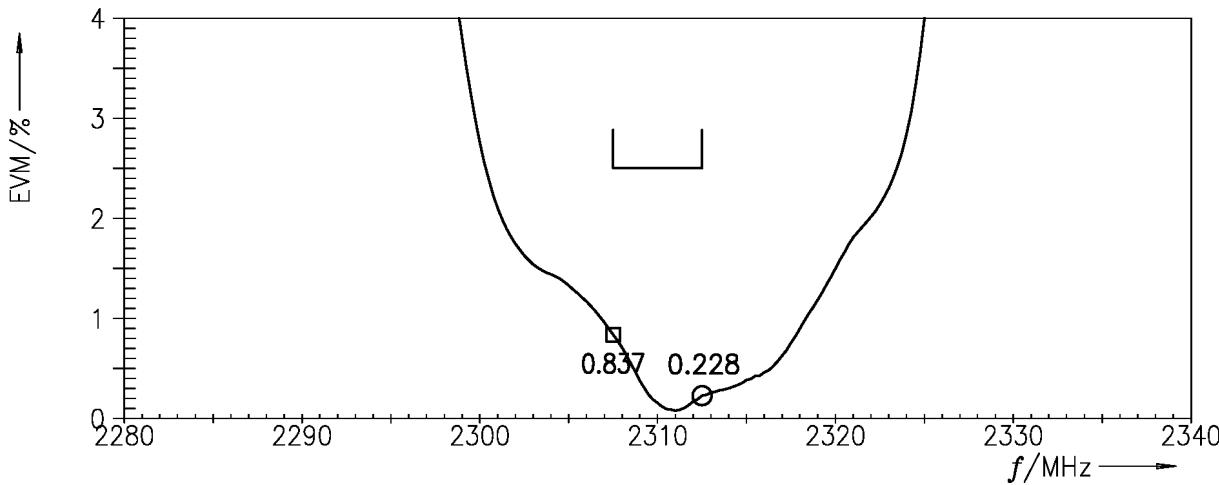
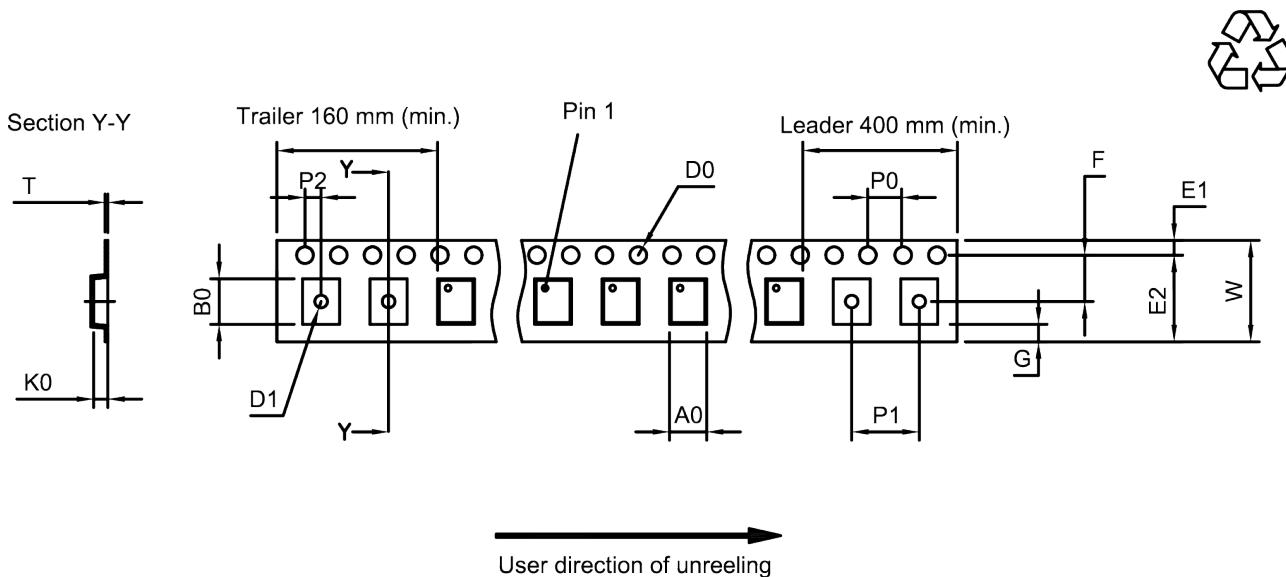


Figure 9: Reflection coefficient at RX port.

**10 EVMs****10.1 TX – ANT****Figure 10:** Error vector magnitude TX – ANT.**10.2 ANT – RX****Figure 11:** Error vector magnitude ANT – RX.

## 11 Packing material

### 11.1 Tape



**Figure 12:** Drawing of tape (first-angle projection) for illustration only and not to scale. The valid tape dimensions are listed in Table 1.

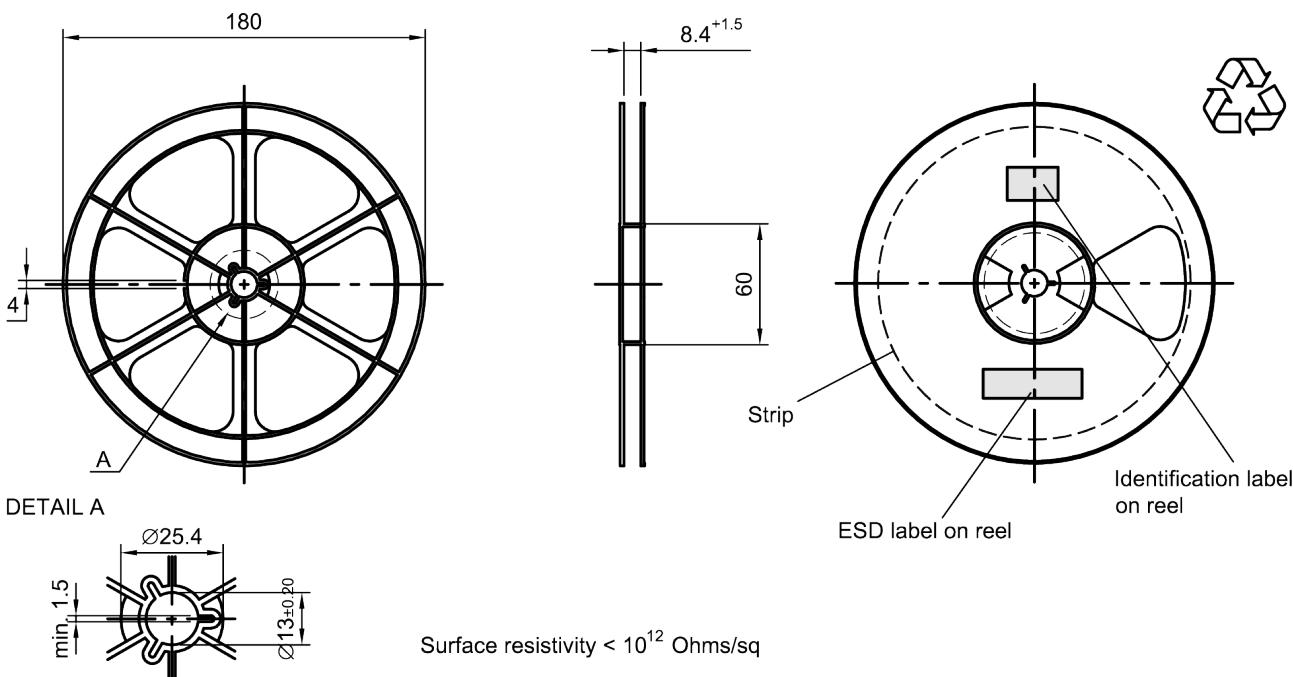
$A_0$	$2.25 \pm 0.05$ mm
$B_0$	$2.75 \pm 0.05$ mm
$D_0$	$1.5 \pm 0.1/-0$ mm
$D_1$	1.0 mm (min.)
$E_1$	$1.75 \pm 0.1$ mm

$E_2$	6.25 mm (min.)
$F$	$3.5 \pm 0.05$ mm
$G$	0.75 mm (min.)
$K_0$	$0.6 \pm 0.05$ mm
$P_0$	$4.0 \pm 0.1$ mm

$P_1$	$4.0 \pm 0.1$ mm
$P_2$	$2.0 \pm 0.05$ mm
$T$	$0.25 \pm 0.03$ mm
$W$	$8.0 \pm 0.3/-0.1$ mm

**Table 1:** Tape dimensions.

#### 11.2 Reel with diameter of 180 mm



**Figure 13:** Drawing of reel (first-angle projection) with diameter of 180 mm.

Dimensions [mm]

X = 220+5

Y = 235+5

Sealing area 10±3

Printing on vacuumbag

Vacuumbag

Sealing area

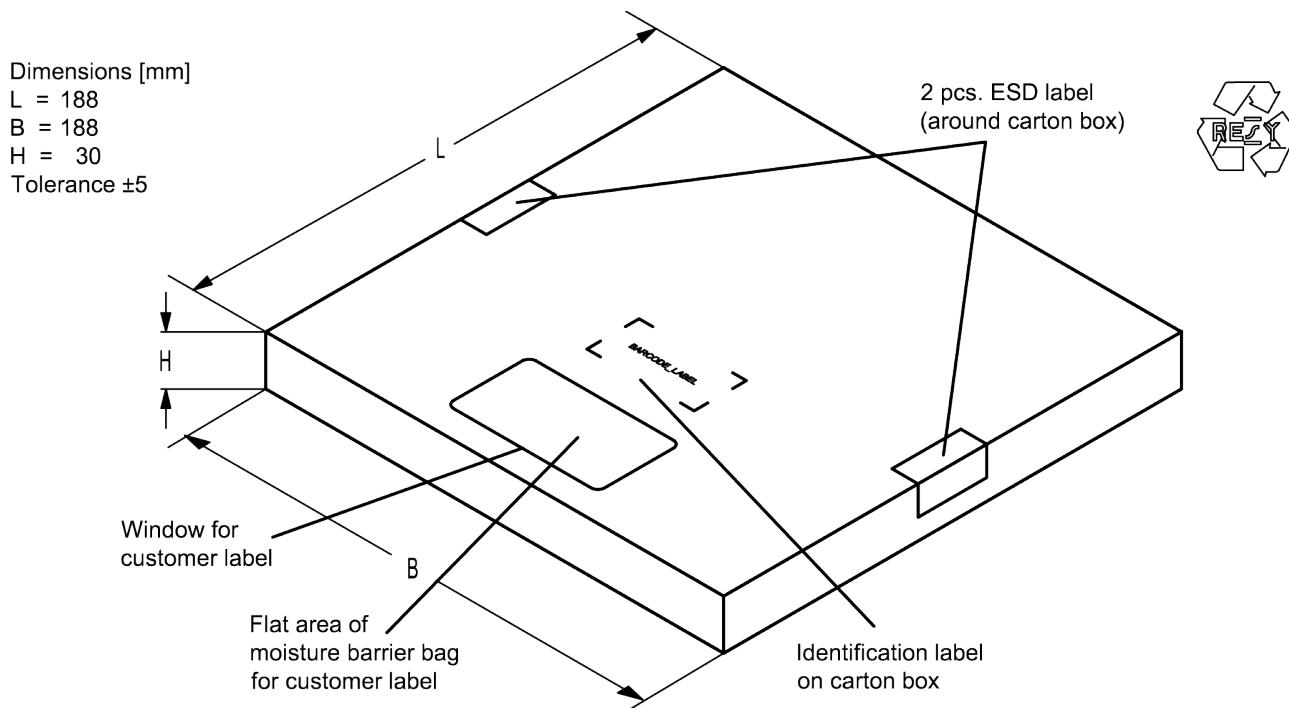
Drypack in vacuumbag

Identification label on vacuumbag

Humidity indicator in vacuumbag

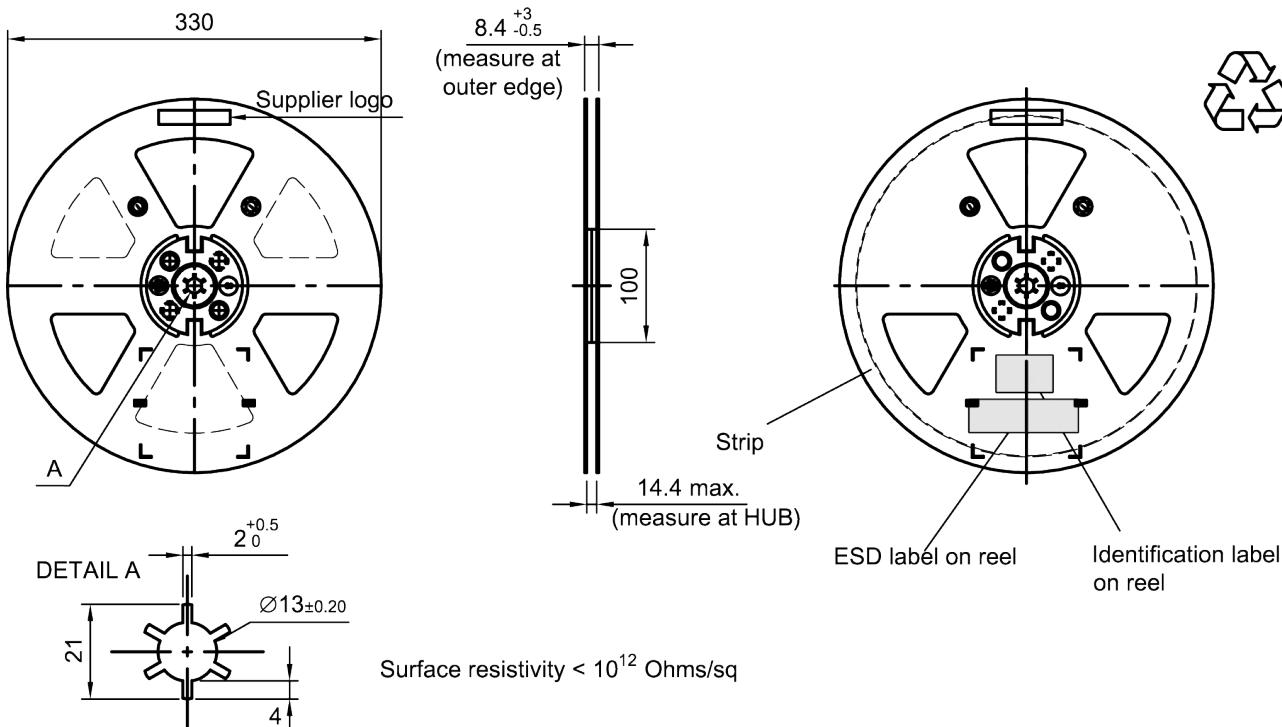


**Figure 14:** Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.



**Figure 15:** Drawing of folding box for reel with diameter of 180 mm.

### 11.3 Reel with diameter of 330 mm



**Figure 16:** Drawing of reel (first-angle projection) with diameter of 330 mm.

Dimensions [mm]

X = 400+5

Y = 418+5

Sealing area  $10\pm 3$

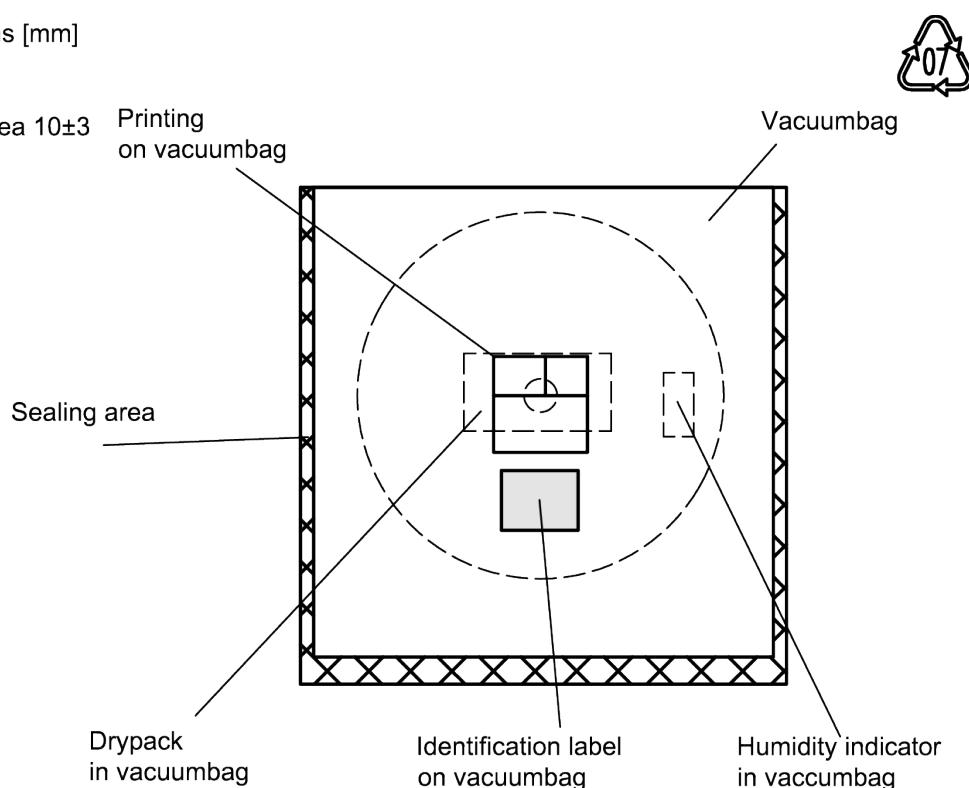


Figure 17: Drawing of moisture barrier bag (MBB) for reel with diameter of 330 mm.

Dimensions [mm]

L = 335

B = 338

H = 36 (for 8 mm tape width)  
40 (for 12 mm tape width)

Tolerance  $\pm 5$

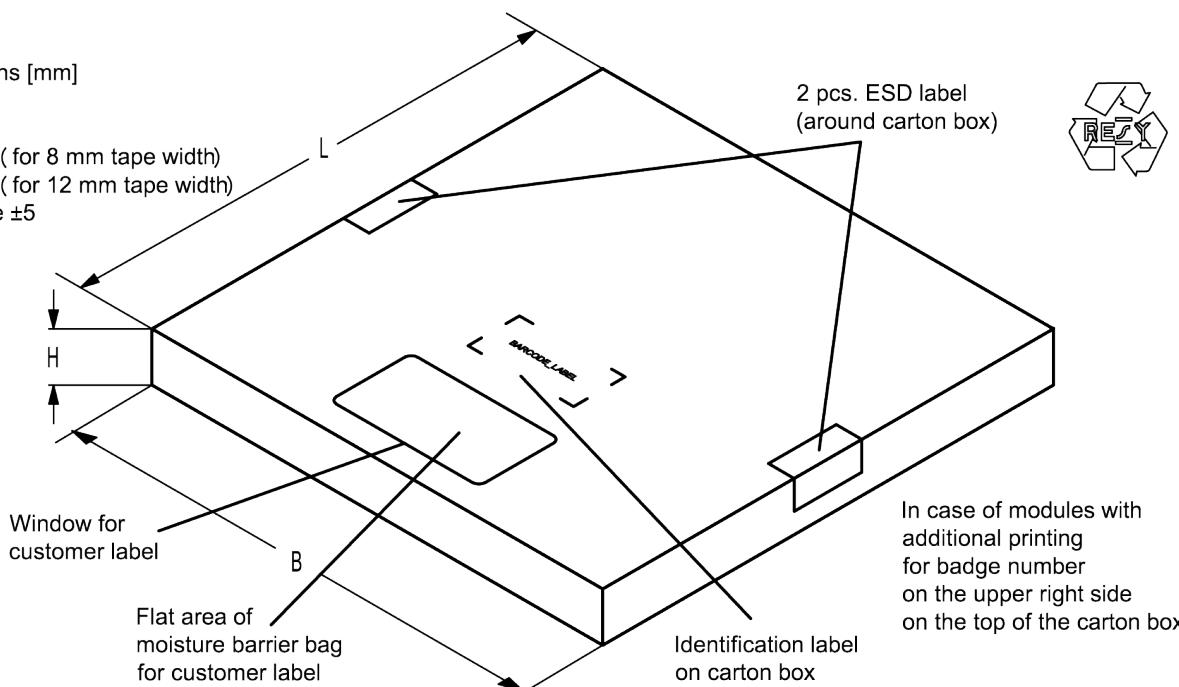


Figure 18: Drawing of folding box for reel with diameter of 330 mm.

## 12 Marking

Products are marked with product type number and lot number encoded according to Table 2:

### ■ Type number:

The 4 digit type number of the ordering code,  
is encoded by a special BASE32 code into a 3 digit marking.

e.g., B3xxxxB**1234**xxxx,

Example of decoding type number marking on device  
**16J** => **1234**  
 $1 \times 32^2 + 6 \times 32^1 + 18 \text{ (=J)} \times 32^0$  = **1234**

The BASE32 code for product type B8207 is 80F.

in decimal code.

### ■ Lot number:

The last 5 digits of the lot number,  
are encoded based on a special BASE47 code into a 3 digit marking.

e.g., **12345**,

Example of decoding lot number marking on device  
**5UY** => **12345**  
 $5 \times 47^2 + 27 \text{ (=U)} \times 47^1 + 31 \text{ (=Y)} \times 47^0$  = **12345**

in decimal code.

Adopted BASE32 code for type number			
Decimal value	Base32 code	Decimal value	Base32 code
0	0	16	G
1	1	17	H
2	2	18	J
3	3	19	K
4	4	20	M
5	5	21	N
6	6	22	P
7	7	23	Q
8	8	24	R
9	9	25	S
10	A	26	T
11	B	27	V
12	C	28	W
13	D	29	X
14	E	30	Y
15	F	31	Z

Adopted BASE47 code for lot number			
Decimal value	Base47 code	Decimal value	Base47 code
0	0	24	R
1	1	25	S
2	2	26	T
3	3	27	U
4	4	28	V
5	5	29	W
6	6	30	X
7	7	31	Y
8	8	32	Z
9	9	33	b
10	A	34	d
11	B	35	f
12	C	36	h
13	D	37	n
14	E	38	r
15	F	39	t
16	G	40	v
17	H	41	\
18	J	42	?
19	K	43	{
20	L	44	}
21	M	45	<
22	N	46	>
23	P		

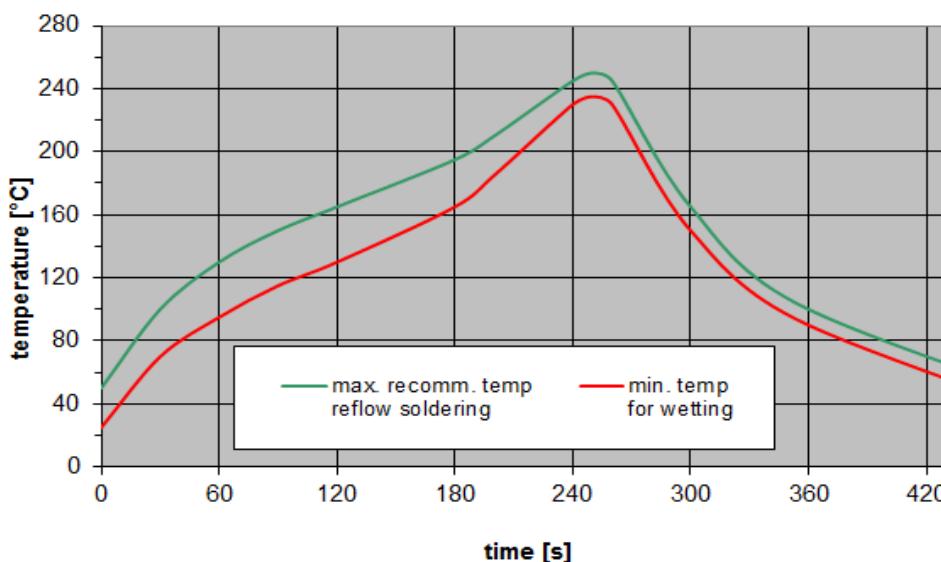
**Table 2:** Lists for encoding and decoding of marking.

### 13 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3<sup>rd</sup> edit and IPC/JEDEC J-STD-020B.

ramp rate	$\leq 3$ K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
$T > 220$ °C	30 s to 70 s
$T > 230$ °C	min. 10 s
$T > 245$ °C	max. 20 s
$T \geq 255$ °C	–
peak temperature $T_{\text{peak}}$	250 °C +0/-5 °C
wetting temperature $T_{\text{min}}$	230 °C +5/-0 °C for 10 s $\pm 1$ s
cooling rate	$\leq 3$ K/s
soldering temperature $T$	measured at solder pads

**Table 3:** Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).



**Figure 19:** Recommended reflow profile for convection and infrared soldering – lead-free solder.

## 14 Annotations

### 14.1 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

### 14.2 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local RF360 sales office.

### 14.3 Ordering codes and packing units

Ordering code	Packing unit
B39242B8207P810	5000 pcs

**Table 4:** Ordering codes and packing units.

## 15 Cautions and warnings

### 15.1 Display of ordering codes for RF360 products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of RF360, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under <https://rffe.qualcomm.com/>.

### 15.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

### 15.3 Moldability

Before using in overmolding environment, please contact your local RF360 sales office.

### 15.4 Package information

#### Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on RF360 internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of RF360, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

#### Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

#### Projection method

Unless otherwise specified first-angle projection is applied.

## 16 ESD protection of SAW filters

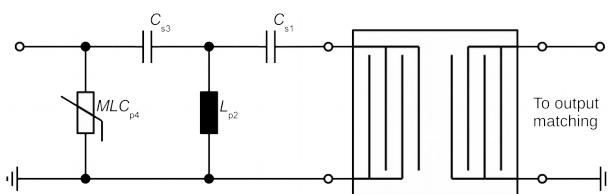
SAW filters are **Electro Static Discharge** sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, “ESD matching” has to be ensured at that filter port, where electrostatic discharge is expected.

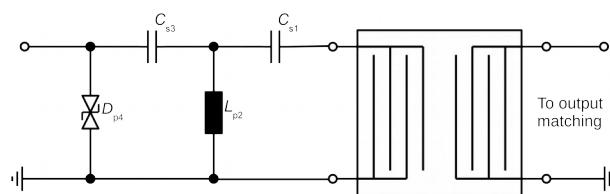
Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended “ESD matching” topologies.

For wide band filters the high-pass ESD matching structure needs to be at least of 3<sup>rd</sup> order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.

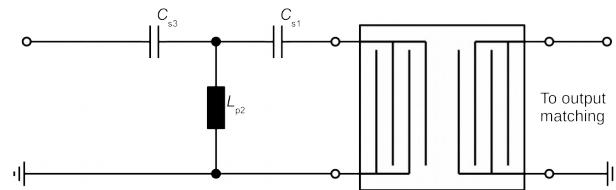


**Figure 20:** MLC varistor plus ESD matching.



**Figure 21:** Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified “ESD matching” topologies can be used alternatively.



**Figure 22:** 3<sup>rd</sup> order high-pass structure for basic ESD protection.

In all three figures the shunt inductor  $L_{p2}$  could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to RF360 Application report: “**ESD protection for SAW filters**”. This report can be found under <https://rffe.qualcomm.com>.

## 17 Important notes

The following applies to all products named in this publication:

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