



RAYSTAR

RAYSTAR Optronics, Inc.
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RL0F00043000WGAABSA00

SPECIFICATION

Summary

4.3 Inch Smart Display Feature

1. DC 5V working voltage.
2. Self-testing after booting function.
3. CAN bus communication interface.
4. Supports Custom CAN ID protocol, default baud rate at 250KB.
5. Built in flash memory, store the fonts and pictures.
6. Support capacitive touch panel (CTP).
7. Smart Display scenario is slave device display and action from Master Device instruction.
8. Embedded buzzer controlled by Master Device.
9. Demo set HOST can be used on multiple platforms, such as Computer (with USB to CAN Dongle), MCU, Raspberry Pi (with PiCAN2).

Product information

Mechanical Data

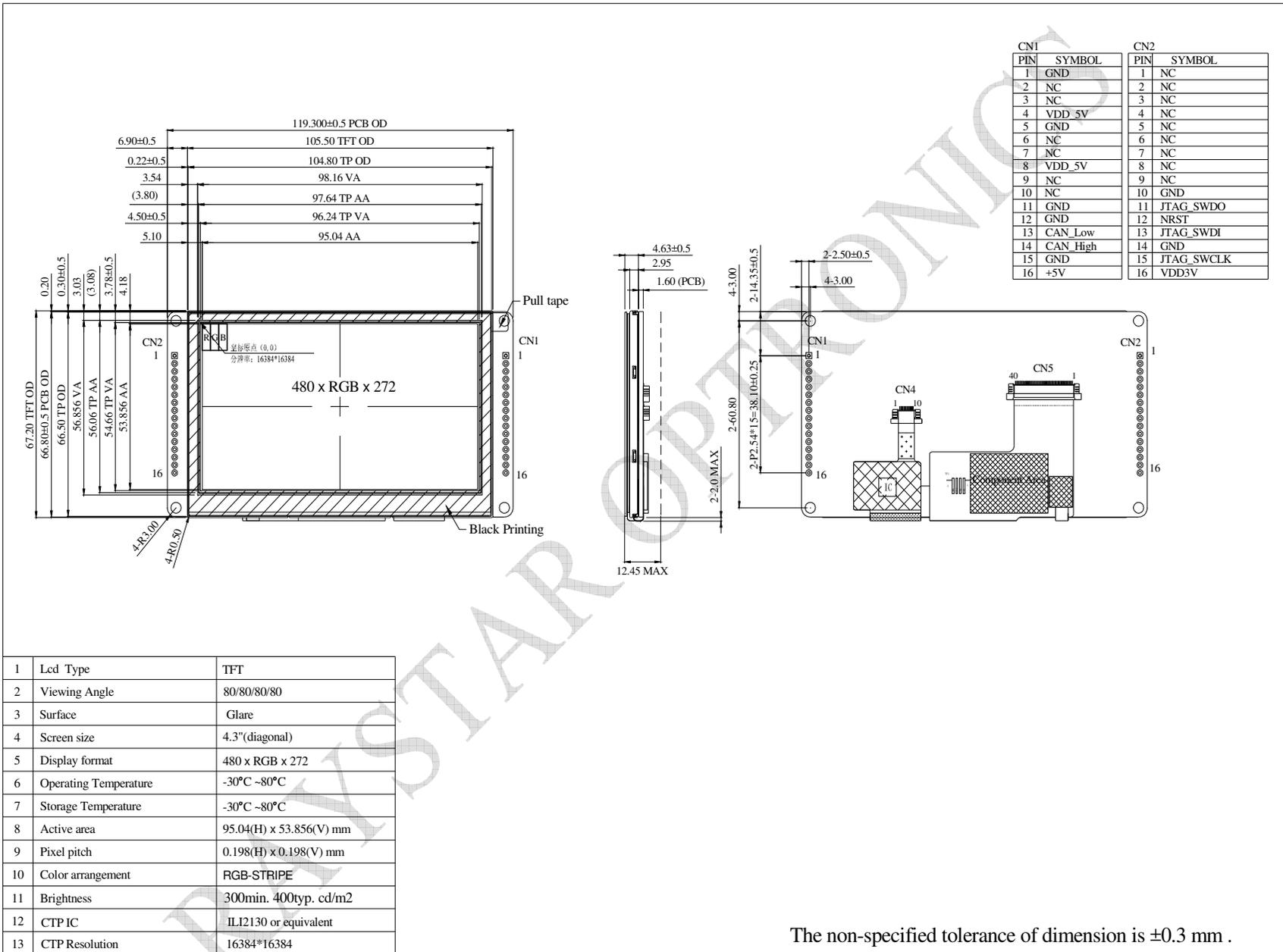
| Item | Standard Value | Unit |
|-----------------|-----------------------------|------|
| LCD panel | 105.5(W) × 67.2(H) × 4.6(D) | mm |
| PCB | 119.3(W) × 67.2(H) × 1.6 | mm |
| Housing outline | NA | mm |

General information

| Item | Standard Value | Unit |
|-------------------------|-----------------------------------|------|
| Operating voltage | 5 | Vdc |
| Communication Interface | CAN bus differential ± 3.3 | Vpp |
| MCU | STM32F750 | N/A |
| Flash Memory | 16 | MB |
| SDRAM Frequency | 108 | MHz |
| LCD display size | 4.3 | inch |
| Dot Matrix | 480 × RGB × 272(TFT) | dot |
| Module dimension | 105.5(W) × 67.2(H) × 4.6(D) | mm |
| Active area | 95.04(W) × 53.856 (H) | mm |
| Dot pitch | 0.066 (W) × 0.198(H) | mm |
| LCD type | TFT, Normally Black, Transmissive | |
| View Direction | 80/80/80/80 | |
| Aspect Ratio | 16:9 | |
| Touch Panel | With PCAP | |
| Surface | Glare | |

Contour Drawing

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The non-specified tolerance of dimension is ±0.3 mm .

Absolute Maximum Ratings

| Item | Symbol | Min | Typ | Max | Unit |
|-----------------------|--------|-----|-----|-----|------|
| Operating Temperature | TOP | -30 | — | +80 | °C |
| Storage Temperature | TST | -30 | — | +80 | °C |

Electrical Characteristics

| Item | Symbol | Min | Typ | Max | Unit |
|----------------|--------|-----|-----|-----|------|
| Supply Voltage | VCC | 4.5 | 5 | 5.5 | V |
| Supply Current | ICC | - | 330 | | mA |

BOM

| Item | Description |
|------|----------------------|
| LCM | RFE43AW-AWW-DNG |
| PCBA | SV10004R300WA00N0100 |

Interface

CN1 definition:

| Pin | Symbol | Function | Remark |
|------|--------|------------------------|--------|
| 1 | GND | GND | Output |
| 2-3 | NC | | |
| 4 | +5V | +5V | Output |
| 5 | GND | GND | Output |
| 6-7 | NC | | |
| 8 | +5V | +5V | Output |
| 9-10 | NC | Connection | |
| 11 | GND | GND | Output |
| 12 | GND | GND | Output |
| 13 | CAN_L | Differential signal D- | I/O |
| 14 | CAN_H | Differential signal D+ | I/O |
| 15 | GND | Power supply GND input | Input |
| 16 | +5V | Power supply 5V input | Input |

CN2 definition:

| Pin | Symbol | Function | Remark |
|-----|----------------|-------------------------------|--------|
| 1-9 | NC | | - |
| 10 | GND | GND | Output |
| 11 | SYS_JTAG_SWO | Data pin for JTAG interface | I/O |
| 12 | NRST | Reset pin for JTAG interface | Input |
| 13 | SYS_JTAG_SWDIO | Data pin for JTAG interface | I/O |
| 14 | GND | GND for JTAG interface | Output |
| 15 | SYS_JTAG_SWCLK | CLK pin for JTAG interface | Input |
| 16 | VMCU | 3.3V power for JTAG interface | Output |

1.

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