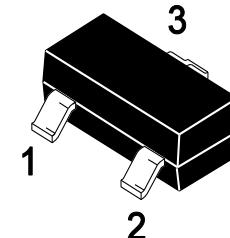




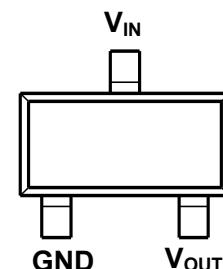
Description

The PJ75AL33SA is a set of three-terminal low power high voltage regulators implemented in CMOS technology. Allow input voltages as high as 36V and output voltages 3.3V. Because of the low power dissipation, PJ75AL33SA are widely used in a variety of equipment such as audio device, video device, communication device and so on.

SOT-23



Marking: 7533



Pin assignment

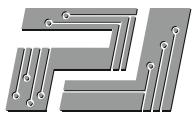
Absolute Maximum Ratings^(NOTE1)

Ratings at 25°C ambient temperature unless otherwise specified.

| Parameter | Limit | Unit |
|-----------------------------|------------|------|
| Supply voltage | -0.3 ~ +36 | V |
| Storage temperature range | -50 ~ +125 | °C |
| Operating temperature range | -40 ~ +85 | °C |

NOTE: 1. Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

| Parameter | Symbol | Value | Unit |
|--|-----------------|-------|------|
| Junction-to-Ambient Thermal Resistance | $R_{\theta JA}$ | 286 | °C/W |
| Power Consumption | P_D | 350 | mW |



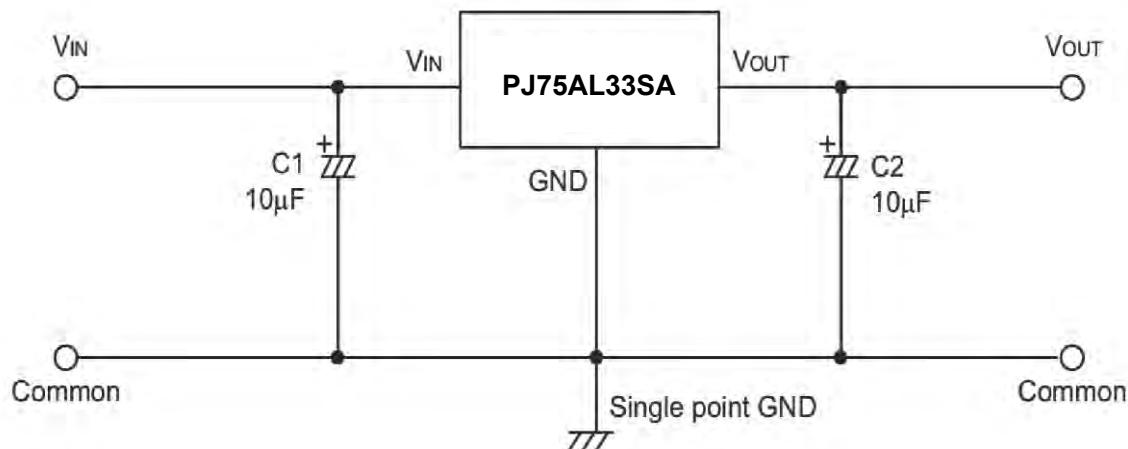
Electrical Characteristics

| Parameter | Symbol | Test conditions | Min. | Typ. | Max | Unit |
|-------------------------------|--|--|-------|------|-------|-----------------|
| Output voltage | V_{OUT} | $V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$ | 3.234 | 3.3 | 3.366 | V |
| Output current | I_{OUT} | $V_{IN}=V_{OUT}+2.0V$ | 70 | 100 | -- | mA |
| Load regulation | ΔV_{OUT} | $V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 50mA$ | -- | 25 | 60 | mV |
| Voltage drop ^{Note2} | V_{DIF} | $I_{OUT}=1mA$, $\Delta V_{OUT}=2\%$ | -- | 25 | 55 | mV |
| Quiescent Current | I_Q | No Load | -- | 2.5 | 3.0 | μA |
| Line regulation | $\Delta V_{OUT} / V_{OUT}$ $\times \Delta V_{IN}$ | $V_{OUT}+1.0V \leq V_{IN} \leq 30V$, $I_{OUT}=1mA$ | -- | -- | 0.2 | %/V |
| Input voltage | V_{IN} | | -- | -- | 36 | V |
| Temperature coefficient | $\Delta V_{OUT} / V_{OUT}$ $\times \Delta T_A$ | $V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$, $-40^\circ C \leq T_A \leq 85^\circ C$ | -- | 100 | -- | ppm/ $^\circ C$ |

NOTE: 2.The difference of input voltage and output voltage when input voltage falls down gradually till output voltage equals to 98% of rating V_{OUT} .

Application Circuit

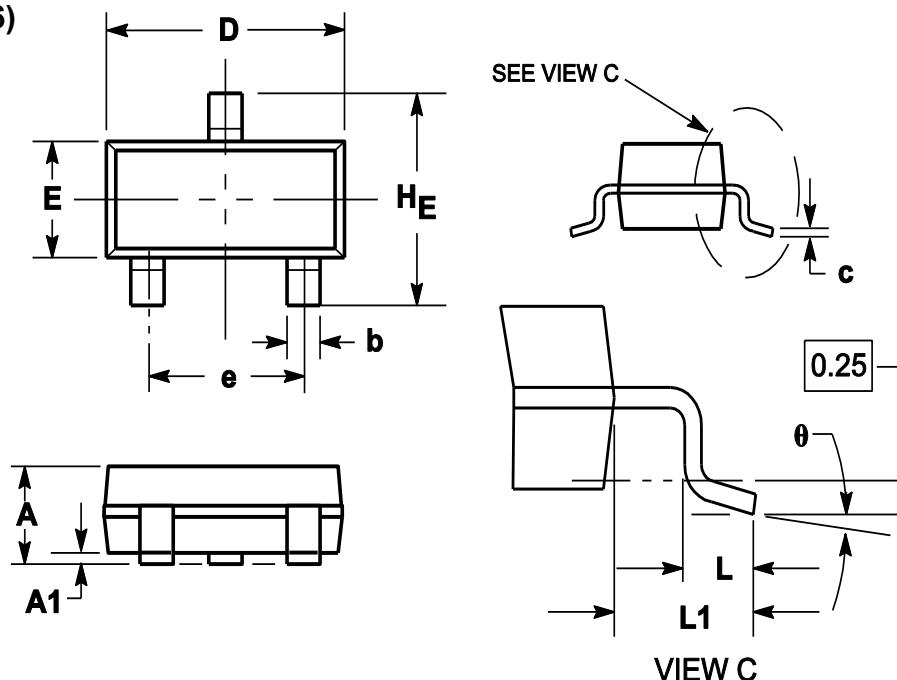
Basic circuits



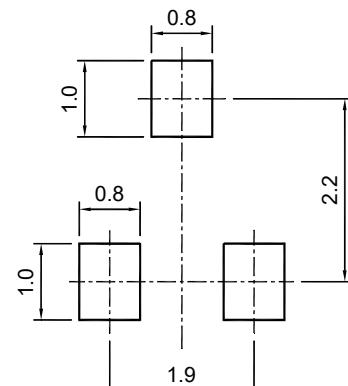


Package Outline

SOT-23 (TO-236)



| Symbol | Dimensions in millimeter | | |
|----------------|--------------------------|-------|-------|
| | Min. | Typ. | Max. |
| A | 0.900 | 1.025 | 1.150 |
| A1 | 0.000 | 0.050 | 0.100 |
| b | 0.300 | 0.400 | 0.500 |
| c | 0.080 | 0.115 | 0.150 |
| D | 2.800 | 2.900 | 3.000 |
| E | 1.200 | 1.300 | 1.400 |
| H _E | 2.250 | 2.400 | 2.550 |
| e | 1.800 | 1.900 | 2.000 |
| L1 | 0.550REF | | |
| L | 0.300 | | 0.500 |
| θ | 0° | | 8° |



SOT-23 (TO-236)

Recommended soldering pad

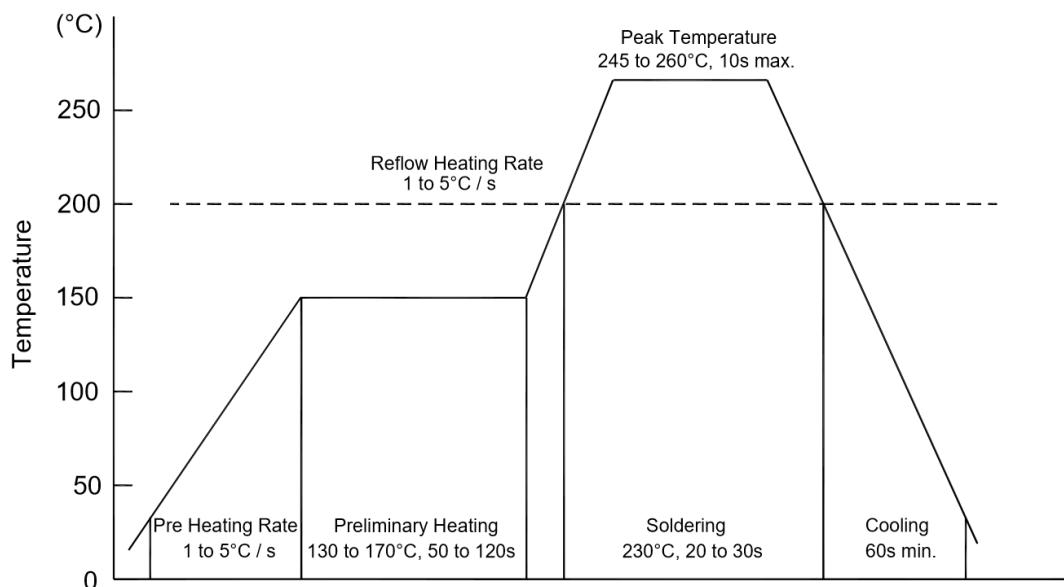
Ordering Information

| Device | Package | Shipping |
|------------|---------|-----------------------|
| PJ75AL33SA | SOT-23 | 3000/Reel&Tape(7inch) |



Conditions of Soldering and Storage

◆ Recommended condition of reflow soldering



Recommended peak temperature is over 245 °C. If peak temperature is below 245 °C, you may adjust the following parameters:

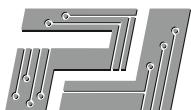
- Time length of peak temperature (longer)
- Time length of soldering (longer)
- Thickness of solder paste (thicker)

◆ Conditions of hand soldering

- Temperature: 370 °C
- Time: 3s max.
- Times: one time

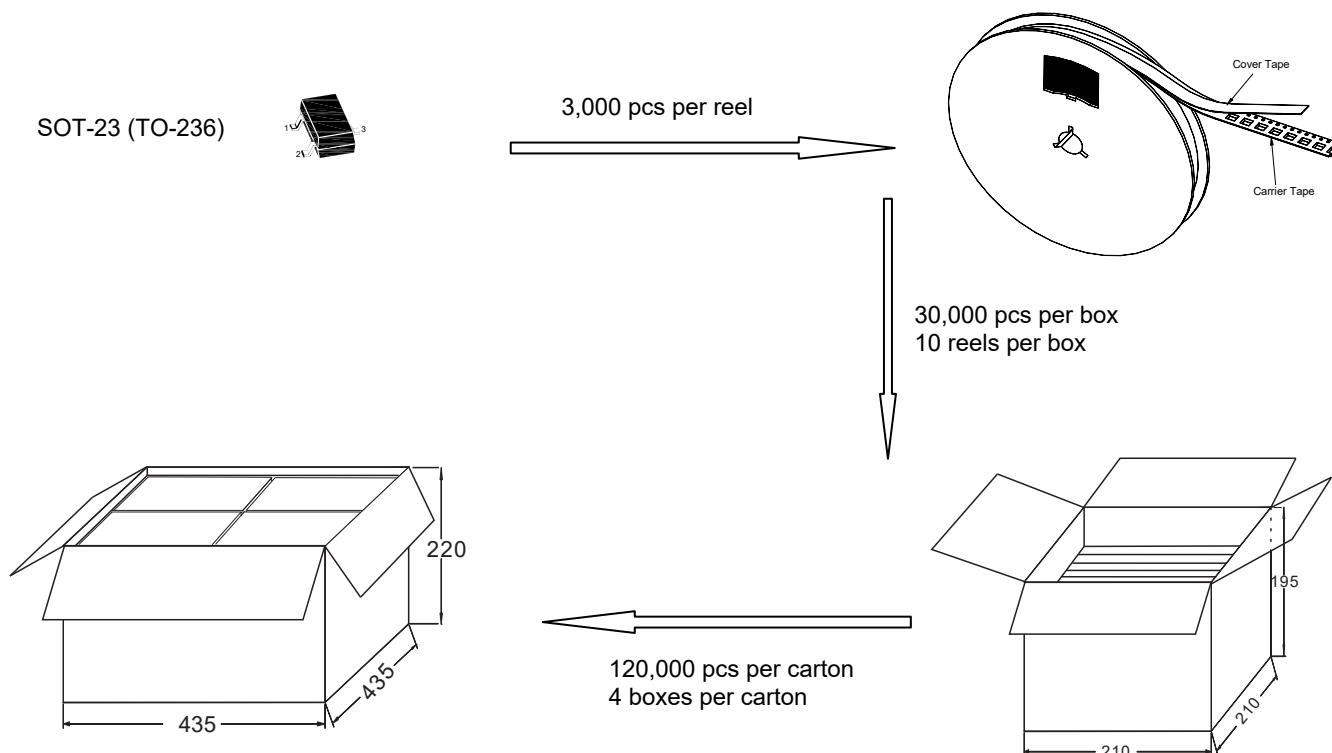
◆ Storage conditions

- **Temperature**
5 to 40 °C
- **Humidity**
30 to 80% RH
- **Recommended period**
One year after manufacturing

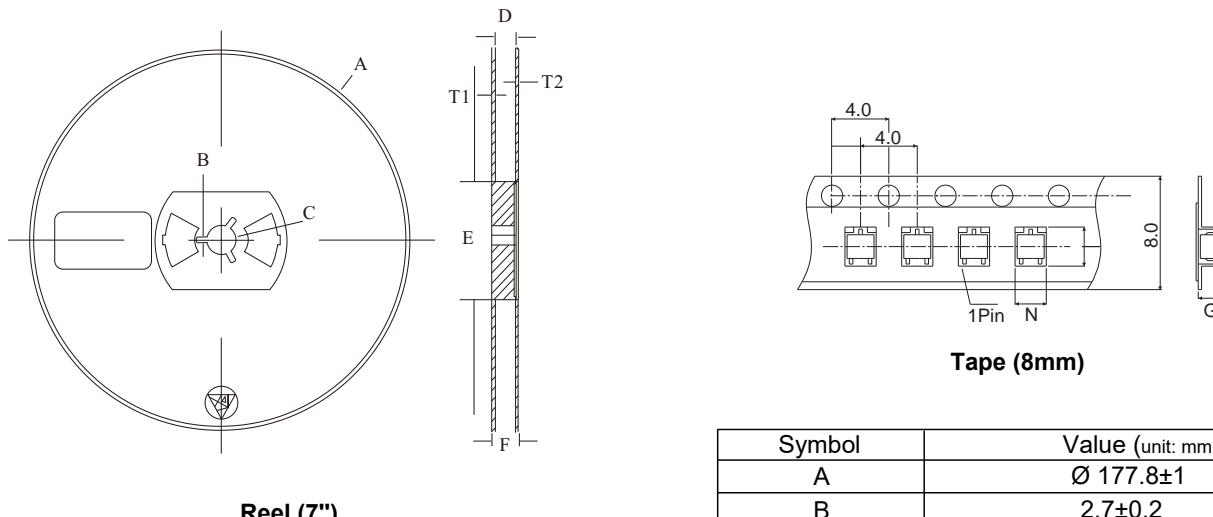


Package Specifications

◆ The method of packaging



◆ Embossed tape and reel data



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