

PJQ44607AP-AU

60V P-Channel Enhancement Mode MOSFET

Voltage

-60 V

Current

-25 A

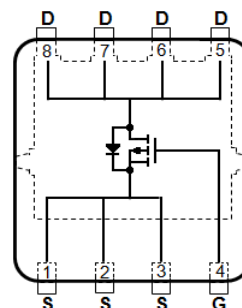
Features

- $R_{DS(ON)}$, $V_{GS}@-10V$, $I_D@-10A < 41m\Omega$
- $R_{DS(ON)}$, $V_{GS}@-4.5V$, $I_D@-6A < 56m\Omega$
- 100% UIS tested
- Reliable and Rugged
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN3333-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.03 grams

DFN3333-8L



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	-60	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^(Note 3)	$T_C=25^{\circ}C$	I_D	-25	A
	$T_C=100^{\circ}C$		-18	
Pulsed Drain Current ^(Note 1)	$T_C=25^{\circ}C$	I_{DM}	-55	
Power Dissipation	$T_C=25^{\circ}C$	P_D	54	W
	$T_C=100^{\circ}C$		27	
Continuous Drain Current ^(Note 4)	$T_A=25^{\circ}C$	I_D	-5.5	A
	$T_A=70^{\circ}C$		-4.6	
Power Dissipation	$T_A=25^{\circ}C$	P_D	2.5	W
	$T_A=70^{\circ}C$		1.8	
Single Pulse Avalanche Current ^(Note 5)		I_{AS}	11.2	A
Single Pulse Avalanche Energy ^(Note 5)		E_{AS}	61	mJ
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~175	$^{\circ}C$
Thermal Resistance ^(Note 4)	Junction to Case	$R_{\theta JC}$	2.8	$^{\circ}C/W$
	Junction to Ambient	$R_{\theta JA}$	60	

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Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

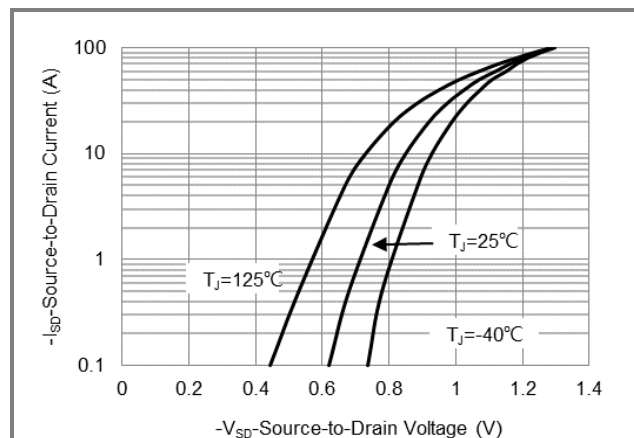
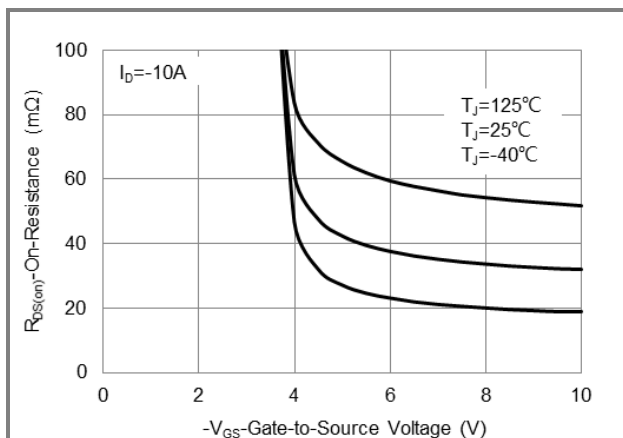
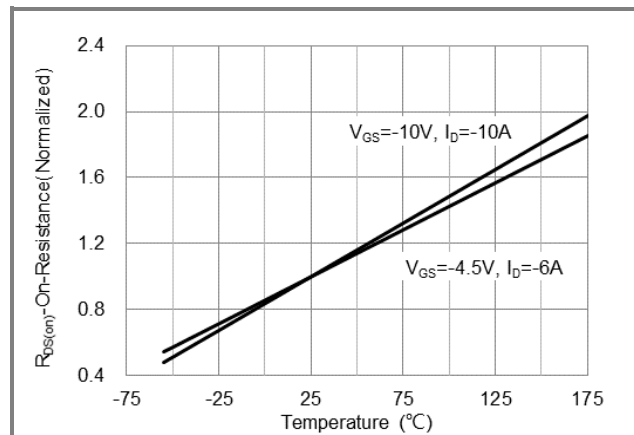
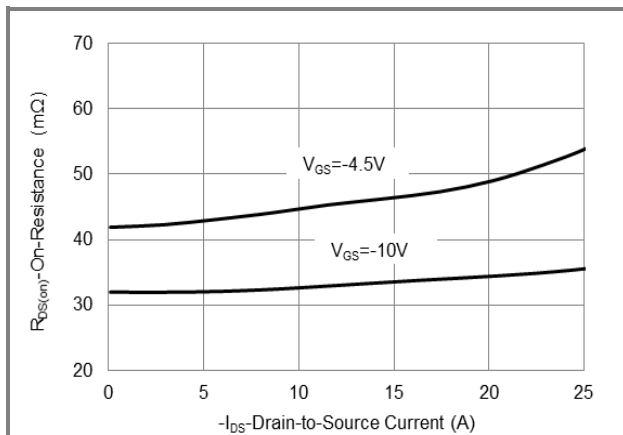
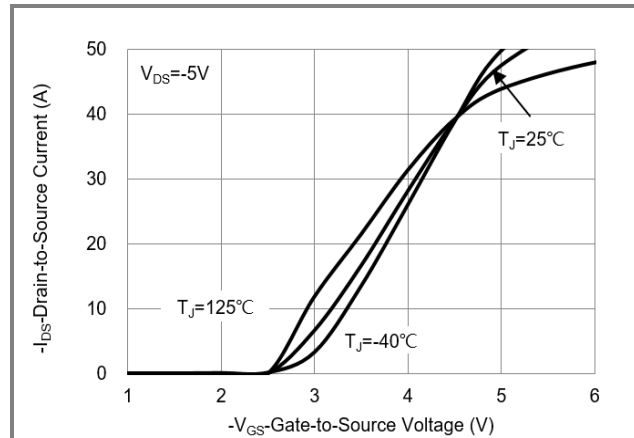
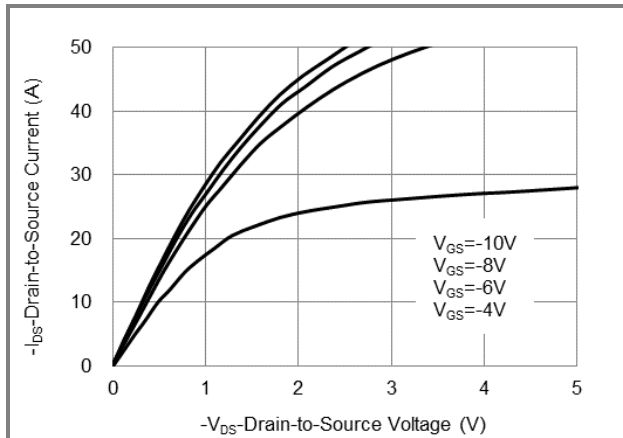
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-60	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-1	-1.7	-2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-10A	-	33.1	41	mΩ
		V _{GS} =-4.5V, I _D =-6A	-	43.1	56	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Dynamic ^(Note 6)						
Total Gate Charge	Q _g	V _{DS} =-30V, I _D =-10A, V _{GS} =-10V	-	35	46	nC
Gate-Source Charge	Q _{gs}		-	8	-	
Gate-Drain Charge	Q _{gd}		-	5	-	
Input Capacitance	C _{iss}	V _{DS} =-30V, V _{GS} =0V, f=1MHz	-	1708	2300	pF
Output Capacitance	C _{oss}		-	104	160	
Reverse Transfer Capacitance	C _{rss}		-	74	130	
Gate resistance	R _g	f=1MHz	-	11.3	-	Ω
Turn-On Delay Time	td _(on)	V _{DS} =-30V, I _D =-10A, V _{GS} =-10V, R _G =3Ω (Note 2)	-	5.2	-	ns
Turn-On Rise Time	tr		-	3.6	-	
Turn-Off Delay Time	td _(off)		-	58	-	
Turn-Off Fall Time	tf		-	29	-	
Drain-Source Diode						
Diode Forward Current	I _S	T _C =25 ^o C	-	-	-25	A
Pulsed Diode Forward Current	I _{SM}		-	-	-55	
Diode Forward Voltage	V _{SD}	I _S =-10A, V _{GS} =0V	-	-0.85	-1.3	V
Reverse Recovery Time	T _{rr}	V _{DD} =-30V,V _{GS} =0V	-	15	-	ns
Reverse Recovery Charge	Q _{rr}	I _S =-10A,dI _S /dt=100A/us	-	9	-	nC

NOTES :

- Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.
- Essentially independent of operating temperature typical characteristics.
- The maximum current rating is package limited.
- $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- E_{AS} is calculated based on the condition of $L=1mH$, $I_{AS}=-11A$, $V_{DD}=-30V$, $V_{GS}=-10V$. 100% test at $L=0.5mH$, $I_{AS}=-11.2A$ in production.
- Guaranteed by design, not subject to production testing.

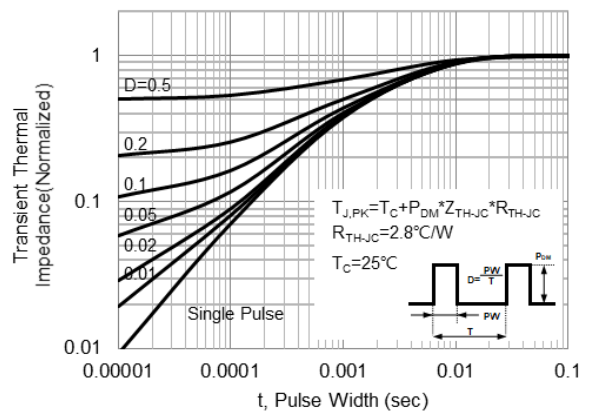
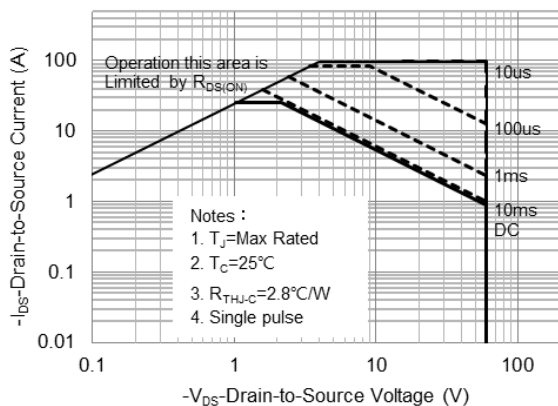
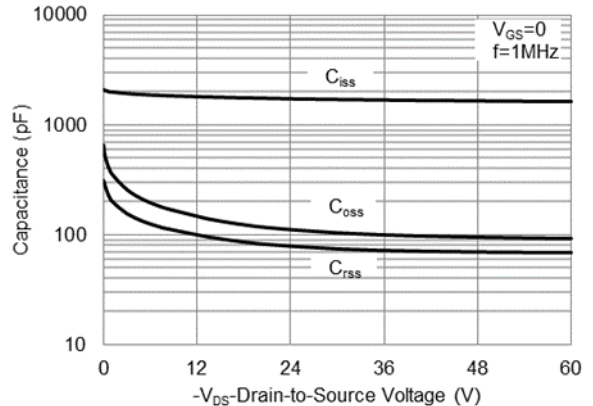
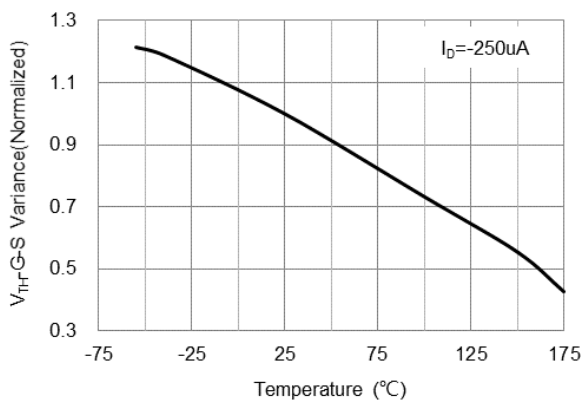
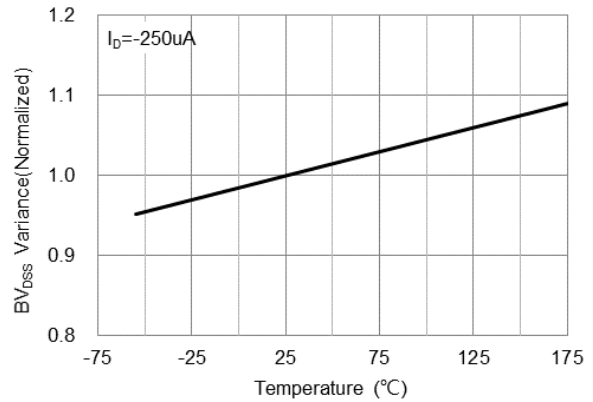
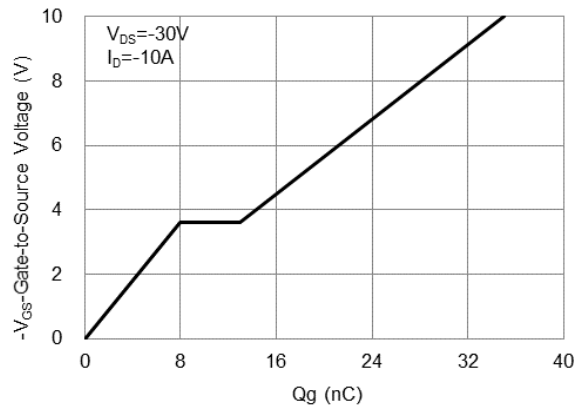
PJQ44607AP-AU

TYPICAL CHARACTERISTIC CURVES



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TYPICAL CHARACTERISTIC CURVES

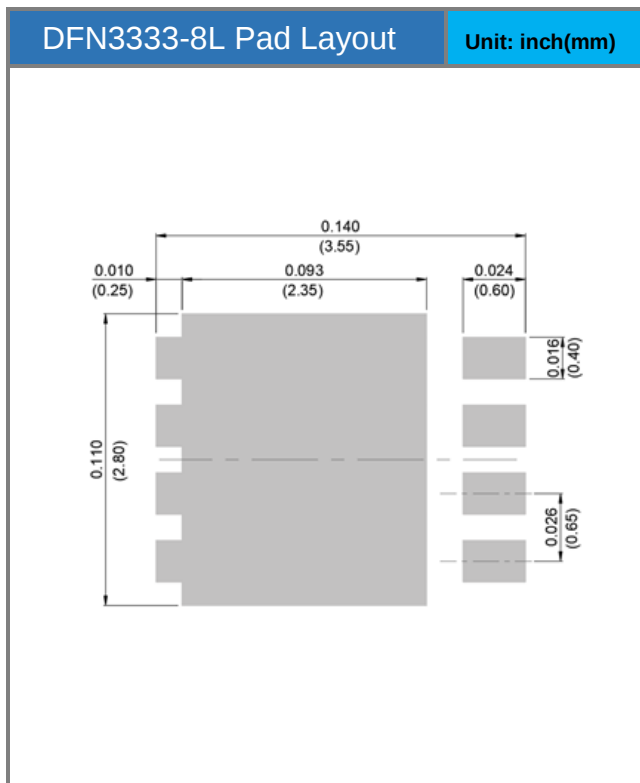
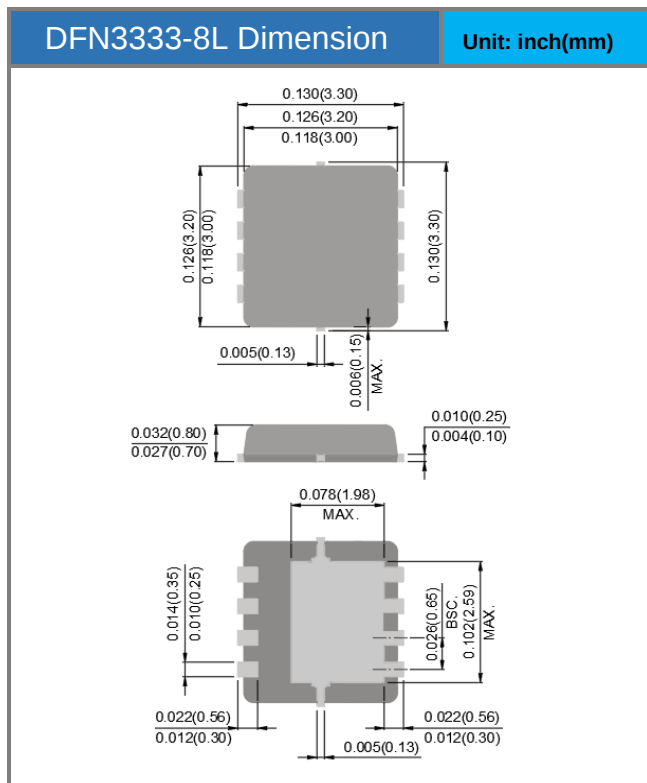


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Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJQ44607AP-AU	DFN3333-8L	5K pcs / 13" reel	44607A

Packaging Information & Mounting Pad Layout



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