NSM6056MT1G

NPN Transistor with Zener Diode

Features

 These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

- Driving Circuit
- Switching Applications

MAXIMUM RATINGS - NPN TRANSISTOR

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V_{CEO}	40	V
Collector - Base Voltage	V _{CBO}	60	V
Emitter - Base Voltage	V _{EBO}	6.0	V
Collector Current - Continuous	I _C	600	mA
Collector Current - Peak	I _{CM}	900	mA

MAXIMUM RATINGS - ZENER DIODE

Rating	Symbol	Value	Unit
Forward Voltage @ I _F = 10 mA	V _F	0.9	V

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) @ T _A = 25°C	P _D	380	mW
Thermal Resistance from Junction–to–Ambient	$R_{\theta JA}$	328	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

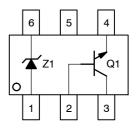
1. FR-4 Minimum Pad.



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NPN Transistor with Zener Diode





SC-74 CASE 318F

MARKING DIAGRAM



M60 = Device Code
M = Date Code*

• = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
NSM6056MT1G	SC-74 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

NSM6056MT1G

NPN TRANSISTOR – ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)

Char	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Collector - Emitter Breakdown Voltage	- Emitter Breakdown Voltage (Note 3) $(I_C = 1.0 \text{ mAdc}, I_B = 0)$		40	-	Vdc
Collector - Base Breakdown Voltage	$(I_{C} = 0.1 \text{ mAdc}, I_{E} = 0)$	V _{(BR)CBO}	60	-	Vdc
Emitter – Base Breakdown Voltage	$(I_E = 0.1 \text{ mAdc}, I_C = 0)$	V _{(BR)EBO}	6.0	-	Vdc
Base Cutoff Current	(V _{CE} = 35 Vdc, V _{EB} = 0.4 Vdc)	I _{BEV}	-	0.1	μAdc
Collector Cutoff Current	(V _{CE} = 35 Vdc, V _{EB} = 0.4 Vdc)	I _{CEX}	-	0.1	μAdc
ON CHARACTERISTICS (Note 3)			•	•	•
DC Current Gain	$ \begin{aligned} &(I_C = 0.1 \text{ mAdc, V}_{CE} = 1.0 \text{ Vdc}) \\ &(I_C = 1.0 \text{ mAdc, V}_{CE} = 1.0 \text{ Vdc}) \\ &(I_C = 10 \text{ mAdc, V}_{CE} = 1.0 \text{ Vdc}) \\ &(I_C = 150 \text{ mAdc, V}_{CE} = 1.0 \text{ Vdc}) \\ &(I_C = 500 \text{ mAdc, V}_{CE} = 2.0 \text{ Vdc}) \end{aligned} $	h _{FE}	20 40 80 100 40	- - - 300 -	-
Collector - Emitter Saturation Voltage	$(I_{C} = 150 \text{ mAdc}, I_{B} = 15 \text{ mAdc})$ $(I_{C} = 500 \text{ mAdc}, I_{B} = 50 \text{ mAdc})$	V _{CE(sat)}	- -	0.4 0.75	Vdc
Base – Emitter Saturation Voltage	$(I_{C} = 150 \text{ mAdc}, I_{B} = 15 \text{ mAdc})$ $(I_{C} = 500 \text{ mAdc}, I_{B} = 50 \text{ mAdc})$	V _{BE(sat)}	0.75 -	0.95 1.2	Vdc
SMALL-SIGNAL CHARACTERISTIC	s				
Current - Gain - Bandwidth Product	(I_C = 20 mAdc, V_{CE} = 10 Vdc, f = 100 MHz)	f _T	250	-	MHz
Collector-Base Capacitance	$(V_{CB} = 5.0 \text{ Vdc}, I_{E} = 0, f = 1.0 \text{ MHz})$	C _{cb}	-	6.5	pF
Emitter-Base Capacitance	$(V_{EB} = 0.5 \text{ Vdc}, I_{C} = 0, f = 1.0 \text{ MHz})$	C _{eb}	-	30	pF
Input Impedance	$(I_C = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{ie}	1.0	15	kΩ
Voltage Feedback Ratio	$(I_C = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{re}	0.1	8.0	X 10 ⁻⁴
Small – Signal Current Gain (I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)		h _{fe}	40	500	-
Output Admittance	h _{oe}	1.0	30	μmhos	
SWITCHING CHARACTERISTICS		_			
Delay Time (V _{CC} = 30 Vdc, V _{EB} = 2.0 Vdc,		t _d	-	15	
Rise Time	I _C = 150 mAdc, I _{B1} = 15 mAdc)	t _r	-	20	ns
Storage Time	(V _{CC} = 30 Vdc, I _C = 150 mAdc,	t _s	-	225	
Fall Time	$I_{B1} = I_{B2} = 15 \text{ mAdc}$	t _f	-	30	ns

^{2.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

ZENER DIODE – ELECTRICAL CHARACTERISTICS (V_F = 0.9 Max @ I_F = 10 mA for all types)

	Test	Zener Vo	ltage VZ	Z _{ZK} I _Z = 0.5	Z _{ZT} I _Z = IZT @ 10%	Ma IR @		d _{VZ} /dt @ l _{ZT1}		C pF Max @
Device	Current Izt mA	Min	Max	mA Ω Max	Mod Ω Max	μΑ	٧	Min	Max	V _R = 0 f = 1 MHz
NSM6056MT1G	5.0	5.49	5.73	200	40	1.0	2.0	-2.0	2.5	200

NSM6056MT1G

TYPICAL ELECTRICAL CHARACTERISTICS - NPN TRANSISTOR

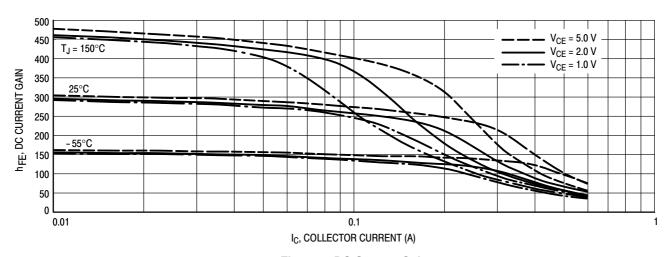


Figure 1. DC Current Gain

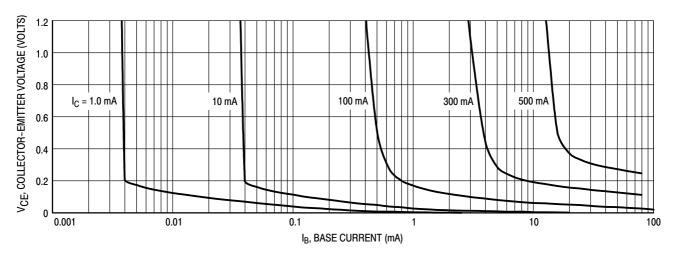


Figure 2. Collector Saturation Region

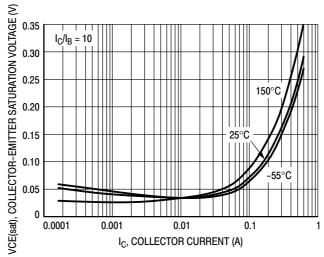


Figure 3. Collector–Emitter Saturation Voltage vs. Collector Current

TYPICAL ELECTRICAL CHARACTERISTICS - NPN TRANSISTOR

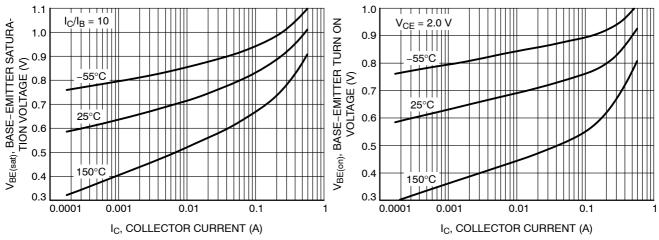


Figure 4. Base-Emitter Saturation Voltage vs.
Collector Current

Figure 5. Base-Emitter Turn On Voltage vs. Collector Current

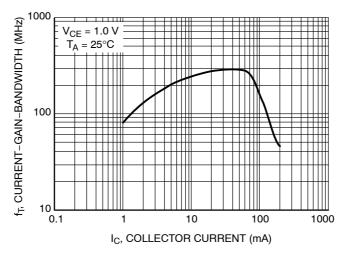


Figure 6. Current-Gain-Bandwidth Product

TYPICAL ELECTRICAL CHARACTERISTICS - ZENER DIODE

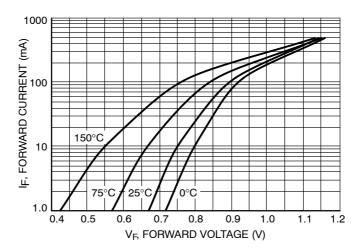


Figure 7. Typical Forward Voltage





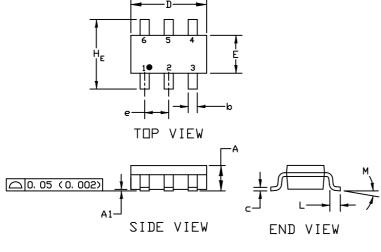
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DATE 07 OCT 2021

NOTES

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994
- 2. CONTROLLING DIMENSION: INCHES
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.

	MI	LLIMETER	52		INCHES	
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.
Α	0. 90	1. 00	1. 10	0. 035	0. 039	0. 043
A1	0. 01	0. 06	0.10	0. 001	0. 002	0. 004
b	0. 25	0. 37	0. 50	0. 010	0. 015	0. 020
c	0.10	0. 18	0. 26	0. 004	0. 007	0. 010
D	2. 90	3. 00	3. 10	0. 114	0. 118	0. 122
Ε	1. 30	1. 50	1. 70	0. 051	0. 059	0. 067
e	0. 85	0. 95	1. 05	0. 034	0. 037	0. 041
HE	2. 50	2. 75	3. 00	0. 099	0. 108	0. 118
L	0. 20	0. 40	0. 60	0. 008	0. 016	0. 024
М	0*		10*	0*		10*



GENERIC MARKING DIAGRAM*



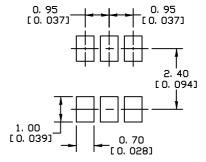
XXX = Specific Device Code

M = Date Code

(Note: Microdot may be in either location)

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

SOLDERING FOOTPRINT

STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:	STYLE 6:
PIN 1. CATHODE	PIN 1. NO CONNECTION	PIN 1. EMITTER 1	PIN 1. COLLECTOR 2	PIN 1. CHANNEL 1	PIN 1. CATHODE
2. ANODE	2. COLLECTOR	2. BASE 1	EMITTER 1/EMITTER 2	2. ANODE	ANODE
CATHODE	EMITTER	COLLECTOR 2	COLLECTOR 1	CHANNEL 2	CATHODE
CATHODE	4. NO CONNECTION	4. EMITTER 2	4. EMITTER 3	CHANNEL 3	CATHODE
5. ANODE	COLLECTOR	5. BASE 2	BASE 1/BASE 2/COLLECTOR 3	CATHODE	CATHODE
CATHODE	6. BASE	COLLECTOR 1	6. BASE 3	6. CHANNEL 4	CATHODE
STYLE 7: PIN 1. SOURCE 1 2. GATE 1 3. DRAIN 2 4. SOURCE 2 5. GATE 2 6. DRAIN 1	STYLE 8: PIN 1. EMITTER 1 2. BASE 2 3. COLLECTOR 2 4. EMITTER 2 5. BASE 1 6. COLLECTOR 1	STYLE 9: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	STYLE 10: PIN 1. ANODE/CATHODE 2. BASE 3. EMITTER 4. COLLECTOR 5. ANODE 6. CATHODE	STYLE 11: PIN 1. EMITTER 2. BASE 3. ANODE/CATHOD 4. ANODE 5. CATHODE 6. COLLECTOR	E

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