# **IGBT** with Monolithic Free **Wheeling Diode**

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on-state voltage and minimal switching loss. The IGBT is well suited for resonant or soft switching applications.

#### **Features**

- Extremely Efficient Trench with Fieldstop Technology
- Low Switching Loss Reduces System Power Dissipation
- Optimized for Low Case Temperature in IH Cooker Application
- Reliable and Cost Effective Single Die Solution
- These are Pb-Free Devices

#### **Typical Applications**

- Inductive Heating
- Consumer Appliances
- Soft Switching

#### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-emitter voltage	$V_{CES}$	1200	V
Collector current @ Tc = 25°C @ Tc = 100°C	I <sub>C</sub>	30 15	Α
Pulsed collector current, T <sub>pulse</sub> limited by T <sub>Jmax</sub>	I <sub>CM</sub>	60	Α
Diode forward current @ Tc = 25°C @ Tc = 100°C	l <sub>F</sub>	30 15	А
Diode pulsed current, $T_{\text{pulse}}$ limited by $T_{\text{Jmax}}$	I <sub>FM</sub>	60	Α
Gate-emitter voltage Transient Gate-emitter voltage (T <sub>pulse</sub> = 5 μs, D < 0.10)	$V_{\sf GE}$	±20 ±25	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P <sub>D</sub>	333 166	W
Operating junction temperature range	TJ	-40 to +175	°C
Storage temperature range	T <sub>stg</sub>	-55 to +175	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T <sub>SLD</sub>	260	°C

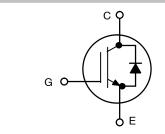
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

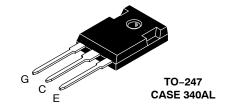


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15 A, 1200 V  $V_{CEsat} = 2.10 V$  $E_{off} = 0.34 \text{ mJ}$ 





#### **MARKING DIAGRAM**



= Assembly Location

= Year WW = Work Week = Pb-Free Package

#### **ORDERING INFORMATION**

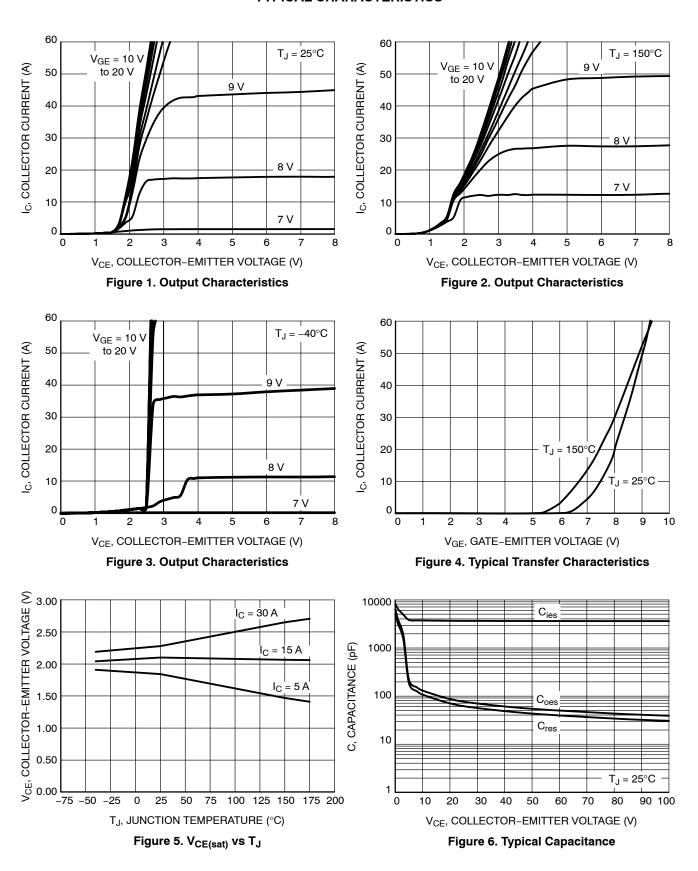
Device	Package	Shipping
NGTB15N120IHRWG	TO-247 (Pb-Free)	30 Units / Rail

#### THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case	$R_{ heta JC}$	0.45	°C/W
Thermal resistance junction-to-ambient	$R_{ hetaJA}$	40	°C/W

#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC				•		
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE} = 0 \text{ V, I}_{C} = 500  \mu\text{A}$	V <sub>(BR)CES</sub>	1200	_	_	V
Collector-emitter saturation voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 15 A V <sub>GE</sub> = 15 V, I <sub>C</sub> = 15 A, T <sub>J</sub> = 175°C	V <sub>CEsat</sub>	- -	2.10 2.30	2.50 -	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 250 \mu A$	V <sub>GE(th)</sub>	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V	I <sub>CES</sub>	-	_	0.1	mA
Gate leakage current, collector-emitter short-circuited	V <sub>GE</sub> = 20 V, V <sub>CE</sub> = 0 V	I <sub>GES</sub>	_	_	100	nA
DYNAMIC CHARACTERISTIC				•		
Input capacitance		C <sub>ies</sub>	-	3690	_	pF
Output capacitance	V <sub>CE</sub> = 20 V, V <sub>GE</sub> = 0 V, f = 10 kHz	C <sub>oes</sub>	-	85	_	
Reverse transfer capacitance	1	C <sub>res</sub>	-	69	_	
Gate charge total		$Q_g$	-	160	_	nC
Gate to emitter charge	V <sub>CE</sub> = 600 V, I <sub>C</sub> = 15 A, V <sub>GE</sub> = 15 V	Q <sub>ge</sub>	-	27	_	
Gate to collector charge		Q <sub>gc</sub>	-	70	_	
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-off delay time	T <sub>J</sub> = 25°C	t <sub>d(off)</sub>	-	170	_	ns
Fall time	$V_{CC} = 600 \text{ V, } I_{C} = 15 \text{ A}$ $R_{a} = 10 \Omega$	t <sub>f</sub>	-	177	_	
Turn-off switching loss	V <sub>GE</sub> = 0 V/ 15V	E <sub>off</sub>	-	0.34	_	mJ
Turn-off delay time	T <sub>J</sub> = 150°C	t <sub>d(off)</sub>	-	190	-	ns
Fall time	$V_{CC} = 600 \text{ V}, I_{C} = 15 \text{ A}$ $R_{a} = 10 \text{ Q}$	t <sub>f</sub>	-	255	-	
Turn-off switching loss	$R_g = 10 \Omega$ $V_{GE} = 0 \text{ V} / 15 \text{V}$	E <sub>off</sub>	-	0.74	_	mJ
DIODE CHARACTERISTIC						
Forward voltage	$V_{GE} = 0 \text{ V}, I_F = 15 \text{ A}, T_J = 25^{\circ}\text{C}$ $V_{GE} = 0 \text{ V}, I_F = 15 \text{ A}, T_J = 175^{\circ}\text{C}$	V <sub>F</sub>	_ _	1.75 2.50	2.0 -	V



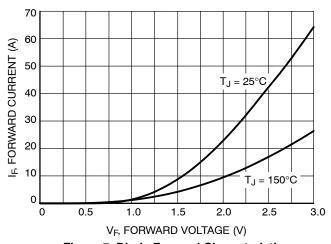


Figure 7. Diode Forward Characteristics

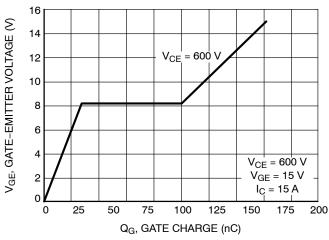


Figure 8. Typical Gate Charge

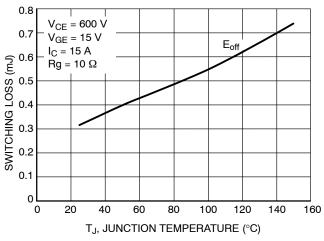


Figure 9. Switching Loss vs. Temperature

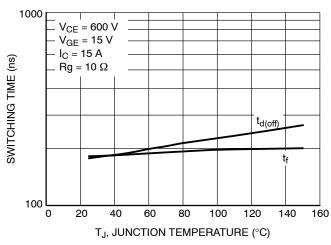


Figure 10. Switching Time vs. Temperature

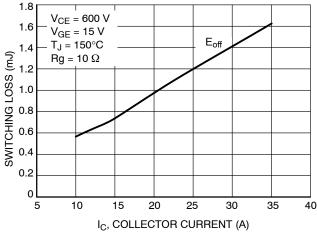


Figure 11. Switching Loss vs. I<sub>C</sub>

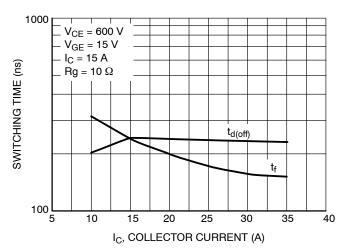


Figure 12. Switching Time vs. I<sub>C</sub>

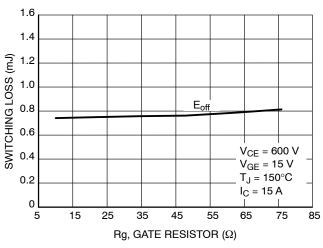


Figure 13. Switching Loss vs. Rg

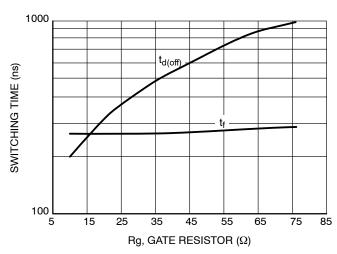


Figure 14. Switching Time vs. Rg

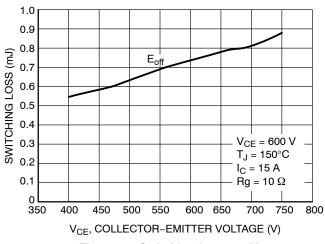


Figure 15. Switching Loss vs. V<sub>CE</sub>

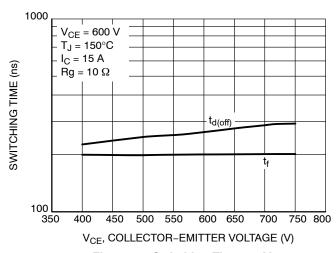


Figure 16. Switching Time vs. V<sub>CE</sub>

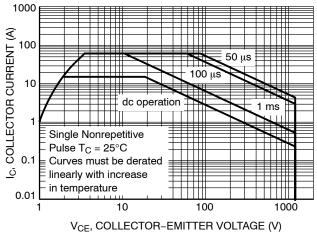


Figure 17. Safe Operating Area

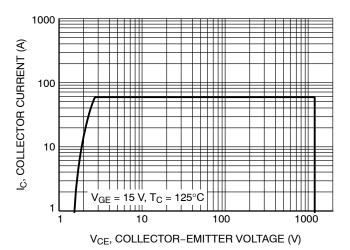


Figure 18. Reverse Bias Safe Operating Area

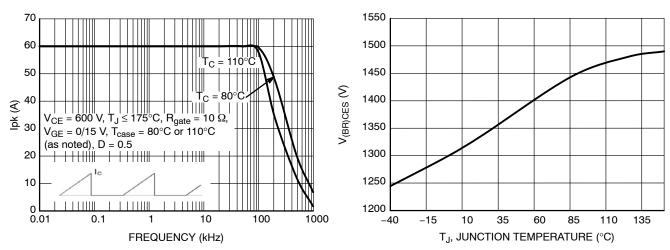


Figure 19. Collector Current vs. Switching Frequency

Figure 20. Typical  $V_{(BR)CES}$  vs. Temperature

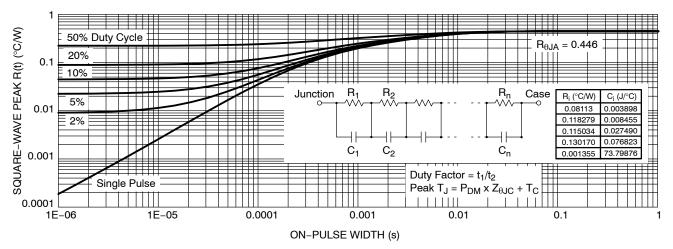


Figure 21. IGBT Transient Thermal Impedance

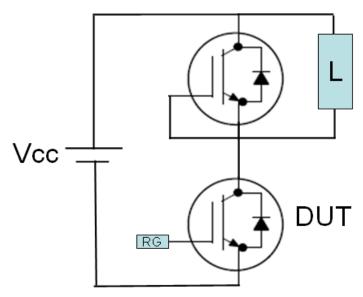


Figure 22. Test Circuit for Switching Characteristics

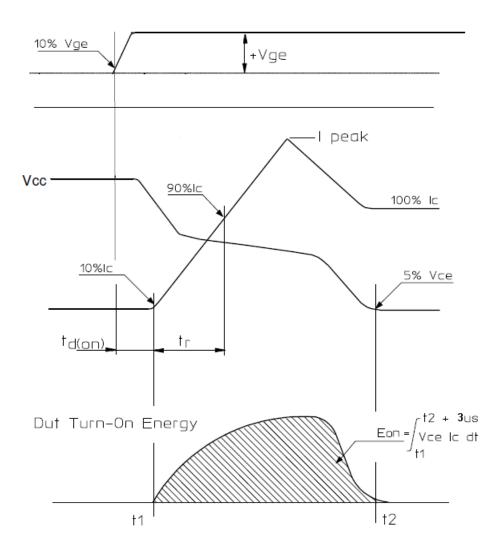


Figure 23. Definition of Turn On Waveform

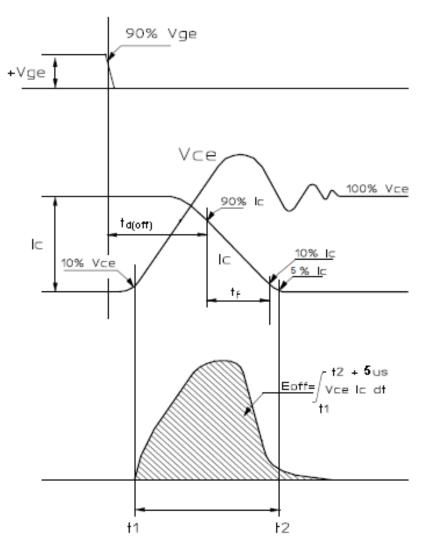
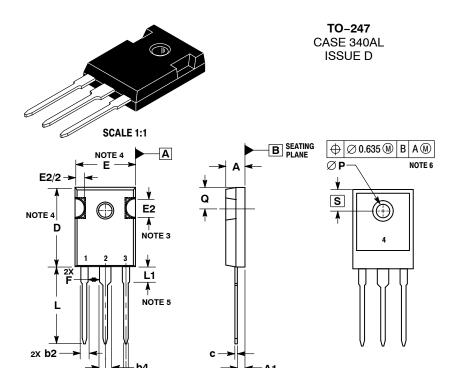


Figure 24. Definition of Turn Off Waveform

зх b

e -

**⊕** 0.25 **M** B A **M** 



NOTE 7

**DATE 17 MAR 2017** 

- NOTES:

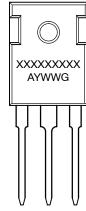
  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. SLOT REQUIRED, NOTCH MAY BE ROUNDED.

  - DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH.
    MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY
  - LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY
- ©P SHALL HAVE A MAXIMUM DRAFT ANGLE OF 1.5° TO THE TOP OF THE PART WITH A MAXIMUM DIAMETER OF 3.91.

  DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.70	5.30	
A1	2.20	2.60	
b	1.07	1.33	
b2	1.65	2.35	
b4	2.60	3.40	
С	0.45	0.68	
D	20.80	21.34	
E	15.50	16.25	
E2	4.32	5.49	
е	5.45 BSC		
F	2.655		
L	19.80	20.80	
L1	3.81	4.32	
P	3.55	3.65	
Q	5.40	6.20	
S	6.15 BSC		

#### **GENERIC MARKING DIAGRAM\***



XXXXX = Specific Device Code Α = Assembly Location

Υ = Year WW = Work Week = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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