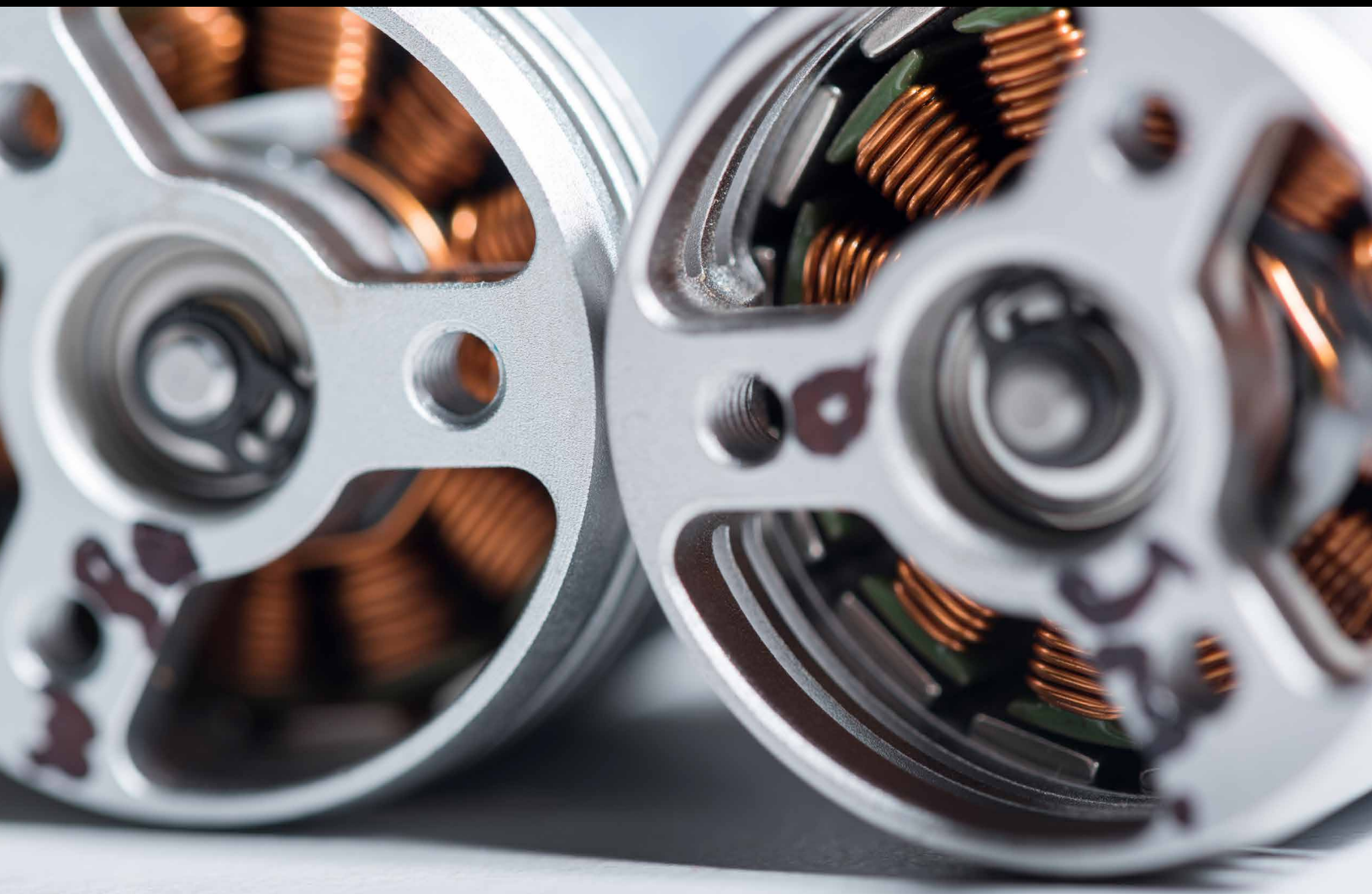




Motor control solutions based on **S32K3 MCUs**





The S32K3 family of 32-bit AEC-Q100 qualified MCUs combines a scalable family of Arm® Cortex-M7-based microcontrollers built on long-lasting features with a comprehensive suite of production-grade tools. S32K3 MCUs are included in NXP's Product Longevity Program, guaranteeing a minimum of 15 years of assured supply.

S32K3 value proposition for motor control

Scalable MCU platform

- Hardware- and software-compatible MCU family
- 120 – 320 MHz Arm Cortex-M7 core
- Flash memory: from 512 KB up to 12 MB
- MAPBGA, HDQFP packages, from 48 to 437 pin count
- CAN FD, FlexIO, QSPI, Ethernet and serial audio interfaces
- AEC-Q100 qualified:
 - Grade 1 (-40° C to +125° C)
 - Grade 2 (-40° C to +105° C)
- Functional Safety compliant: ISO 26262 up to ASIL D

Hardware Security Engine (HSE): AES-128/192/256, RSA and ECC encryption, ISO 21434 compliant

Motor control coverage

- Engineered tools for 3-phase PMSM and 3-phase BLDC motor control targeting body and chassis

- Dedicated peripherals set for rapid motor control loop implementation: enhanced Modular IO Subsystem (eMIOS), Logic Control Unit (LCU), TRGMUX, Body Cross-triggering Unit (BCTU), Analog to Digital Converter (ADC), and Analog Comparator (CMP)

Comprehensive motor control ecosystem

Diverse hardware solutions supporting motor control applications

S32K3 software ecosystem with production-ready algorithm library:

Automotive Math and Motor Control Library (AMMCLib) set

FreeMASTER and Motor Control Application Tuning (MCAT) tool

Model-Based Design Toolbox (MBDT)

Dedicated technical support and online community

S32K3 product overview

S32K3 provides a scalable platform with high hardware and software compatibility to address various motor control techniques and applications.

S32K310	S32K311	S32K312	S32K314	S32K322	S32K324	S32K341	S32K342	S32K344
1x Cortex-M7 @ 120 MHz			1x Cortex-M7 @ 160 MHz	2x Cortex-M7 @ 160 MHz		1x LS Cortex-M7 @ 160MHz		
ASIL B						ASIL D		
512 KB Flash	1 MB Flash	2 MB Flash	4 MB Flash	2 MB Flash	4 MB Flash	1 MB Flash	2 MB Flash	4 MB Flash
112 KB SRAM (incl. 96 KB TCM)	128 KB SRAM (incl. 96 KB TCM)	192 KB SRAM (incl. 96 KB TCM)	512 KB SRAM (incl. 96 KB TCM)	256 KB SRAM (incl. 192 KB TCM)	512 KB SRAM (incl. 192 KB TCM)	256 KB SRAM (incl. 192 KB TCM)		512 KB SRAM (incl. 192 KB TCM)
up to 83 I/Os	up to 83 I/Os	up to 145 I/Os	up to 218 I/Os	up to 142 I/Os	up to 218 I/Os	up to 142 I/Os	up to 143 I/Os	up to 218 I/Os
12-ch. eDMA			32-ch. eDMA					
3x FlexCAN w/CAN FD		6x FlexCAN w/CAN FD		4x FlexCAN w/CAN FD	6x FlexCAN w/CAN FD	4x FlexCAN w/CAN FD	4x FlexCAN w/CAN FD	6x FlexCAN w/CAN FD
			1x 100 Mbps Ethernet (TSN)					
2x I ² C								
4x SPI**			6x SPI**	4x SPI**	6x SPI**	4x SPI**		6x SPI**
2x 24-ch. 12-bit ADC			3x 24-ch. 12-bit ADC	2x 24-ch. 12-bit ADC	3x 24-ch. 12-bit ADC	2x 24-ch. 12-bit ADC		3x 24-ch. 12-bit ADC
			2x SAI (I ² S)					
			Quad SPI (4-bit data)					
LQFP-48								
		HDQFP-172		HDQFP-172				
HDQFP-100				HDQFP-100		HDQFP-100	HDQFP-100	
			MAPBGA-257		MAPBGA-257			MAPBGA-257

S32K328	S32K338	S32K348	S32K358	S32K388	S32K389
2x Cortex-M7 @ 240 MHz	3x Cortex-M7 @ 240 MHz	1x LS Cortex-M7 @ 240 MHz	1x LS Cortex-M7 + 1X Cortex-M7 @ 240 MHz	1x LS +3 or 2x LS +1 Arm Cortex-M7 @ 320 MHz	1x LS +3 or 2x LS +1 Arm Cortex-M7 @ 320 MHz
ASIL B		ASIL D			
8 MB Flash					12 MB Flash
1152K SRAM				1152 KB SRAM (incl. 384 TCM)	2304 KB SRAM (incl. 384 TCM)
up to 325 I/Os				202 I/Os	321 I/Os
32-ch. eDMA					
8 x CAN (FD)					12 x CAN (FD)
1x 1 Gbps Ethernet (TSN)				Ethernet (TSN) 2x 1 Gbit/s 2x 1 Gbps Ethernet (TSN)	
2 x I ² C, 16 x UART(LIN)					
6x SPI**					
3x 24-ch. 12-bit ADC					
2x SAI (I ² S)					
Quad SPI (8-bit data width)				Quad SPI (4-bit data)	
uSDHC (SDIO)					
HDQFP-172					
MAPBGA-289v					
					MAPBGA-437

Common HW: HSE B, LPUART, FlexIO, eMIOS Timers, ACMP, LCU, BCTU, TRGMUX

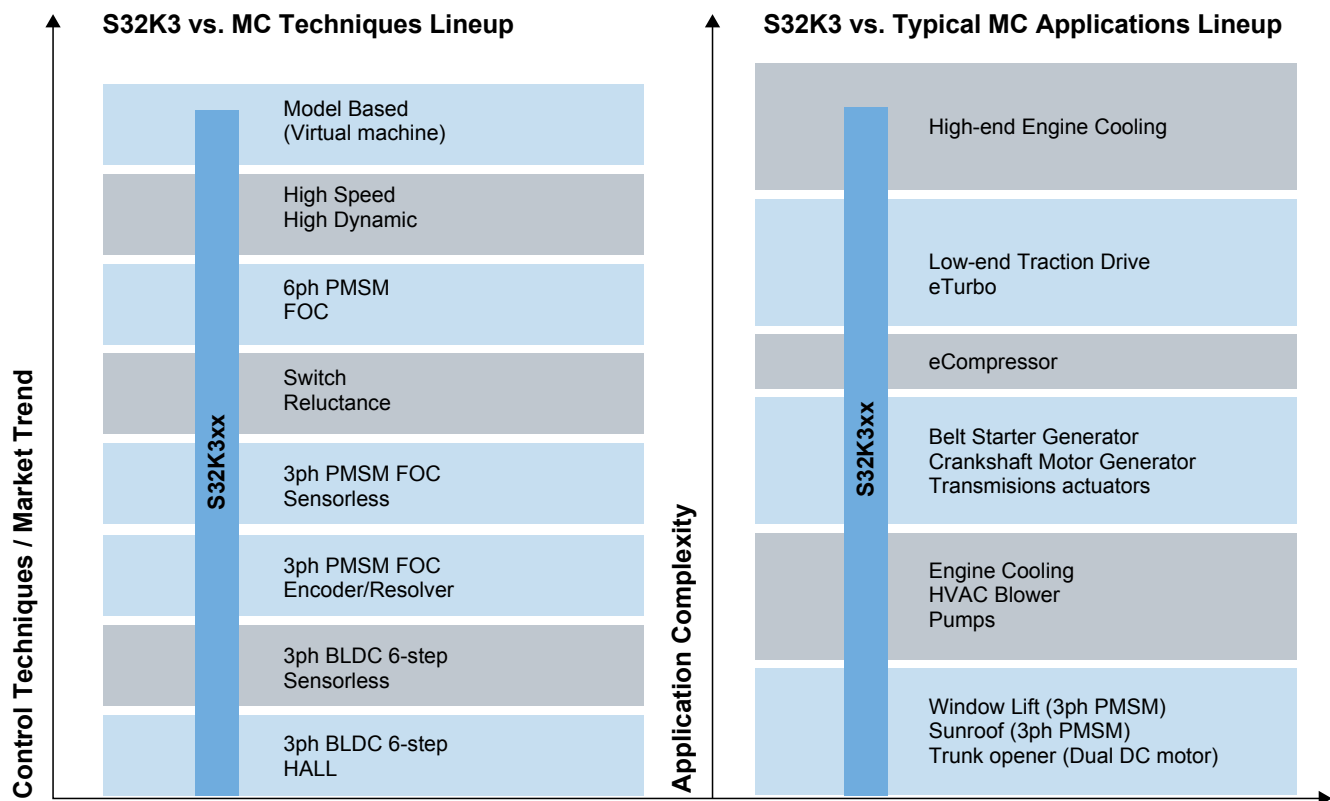
Common SW: Real Time Driver, Security FM, Safety Framework SW & Core Self Test Lib, Application Specific SW

**Ethernet 10BaseT1S supported by SPI + external MAC&PHY

S32K3 product overview (cont.)

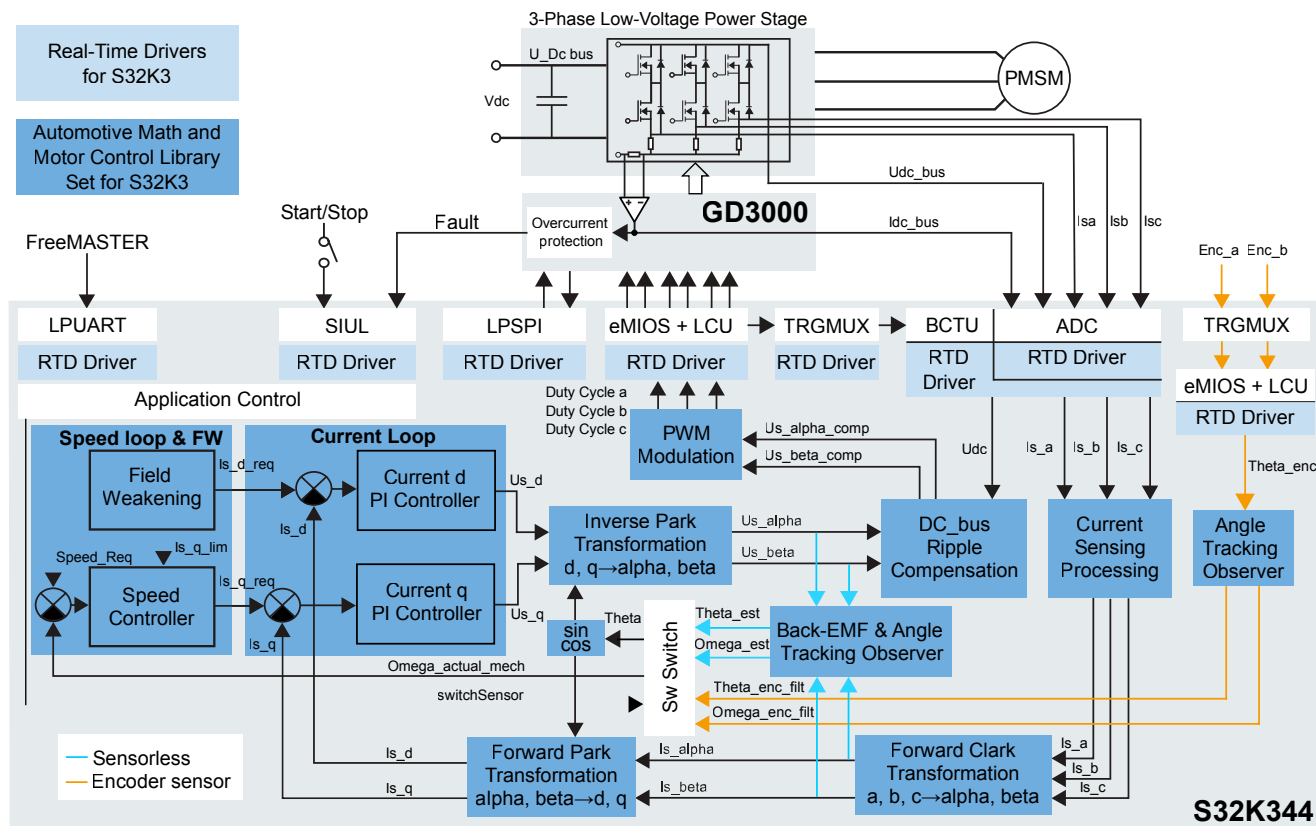
S32K364	S32K366	S32K374	S32K376	S32K394	S32K396
1 x LockStep + 1 Arm® Cortex®-m7 @320 MHz		1 x LockStep + 1 Arm® Cortex®-m7 @320 MHz			
2 x motor control coprocessor (2x 16-ch.)		2 x motor control coprocessor (2x 32-ch.)			
ASIL D					
4 MB Flash	6 MB Flash	4 MB Flash	6 MB Flash	4 MB Flash	6 MB Flash
704 KB SRAM (incl. 192 KB TCM)		800 KB SRAM (incl. 288 KB TCM)			
127/ 209+8LVDS I/Os					
64-ch. eDMA with 32-ch. LockStep					
6 x CAN (FD)					
100 Mbit/s Ethernet (AVB/TSN)					
ZipWire					
6 x SPI, 4x UART(LIN), 2 x I2C					
2x 12-ch. eFlexPWM with NanoEdge (8-ch. each high-resolution PWM)					
2 x sigma-delta ADC with programmable DSP		4 x sigma-delta ADC with programmable DSP			
4 x 12-bit SAR-ADC (48 inputs)		7 x 12-bit SAR-ADC (69 inputs)			
2x Sine Wave Generator, 2 x Analog Comparator					
Quad SPI (8-bit data width, SDR and DDR mode)					
LQFP-EP-176					
MAPBGA-289					

S32K3 Motor Control Lineup

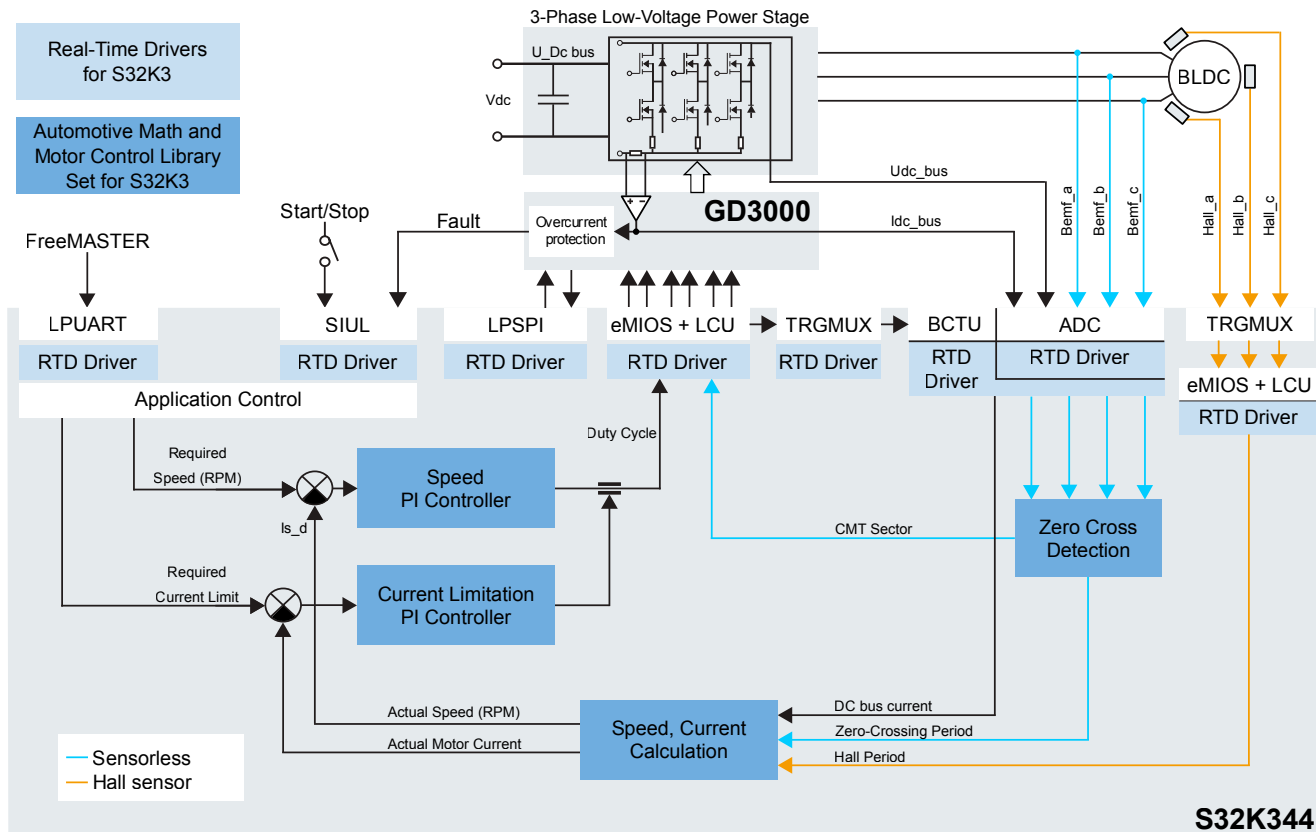


S32K3 Motor Control Block Diagrams

Field Ocontrol (FOC) for PMSM motor



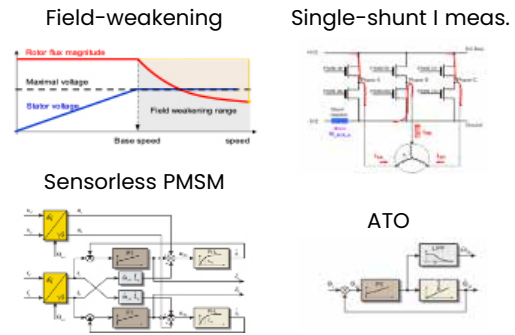
Six-step commutation control for BLDC motor



S32K3 Motor control software and ecosystem

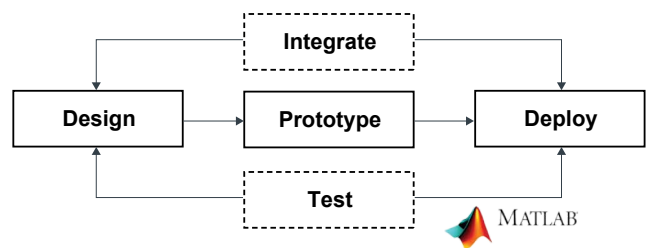
Automotive Math and Motor Control Library (AMMCLib) set

- Precompiled software library including NXP-patented control math algorithms
- Automotive production-ready software (SPICE Level 3, CMMI and ISO 9001/TS 16949)
- Delivered as bit-accurate models for MATLAB®/Simulink® and C code
- Single API across NXP MCUs, simple migration across platforms



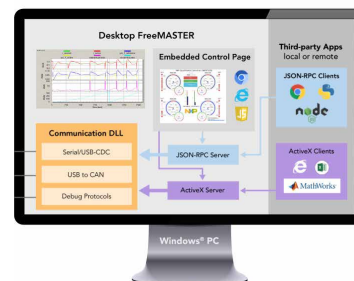
Model-Based Design Toolbox (MBDT)

- Model-based design environment in MATLAB® and Simulink® for motor control software on S32K MCUs
- Automatic code generation for S32K3xx peripherals and applications prototyping
- Extensive online community and tutorials available
- Model-based design approach helps to save R&D time and test efforts



FreeMASTER (Lite)

- Real-time data visualization tool for debugging and tuning embedded algorithm during development
- Graphs, tabular grids and web views embedded directly in the desktop application
- FreeMASTER Lite supports JSON RPC protocol and is able to run on Windows® or Linux® host PC, enabling custom UI on web browsers



Motor Control Application Tuning (MCAT)

- HTML-based graphical user interface tool, plug-in to FreeMASTER and fully compliant with AMMCLib set API
- Real-time tuning and updating of control parameters



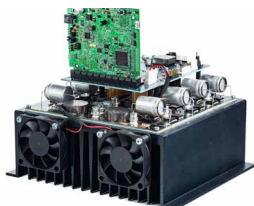
S32K3 additional software

- S32 Design Studio IDE: Eclipse, GCC and debugger
- Security firmware: NXP provided
- Core Self-Test Library for functional safety applications
- Production-grade ASIL compliant Real Time Drivers (RTD) support for both AUTOSAR and non-AUTOSAR applications
- Third-party ecosystem support to reduce time-to-market



S32K3 motor control hardware tools

Part number	MCSPTR2AK396
Picture	
Supported products	
MCU	S32K39-37-36: S32K39/37/36 Microcontrollers for Electrification Applications
Analog	MC33937: 3-Phase Field Effect Transistor Pre-Driver FS26: Safety System Basis Chip with Low Power, for ASIL D Systems
Hardware features	
Motor	3-phase PMSM motor with resolver, 3000 RPM, 0.32 Nm, 95 W, 5.2 A
Voltage	30 V per phase
Current and Voltage Sensing	DC Bus voltage and current, phase voltages, BEMF voltages, over voltage/current protections
Position Sensing	Resolver, encoder, HALL or sensorless techniques
Communication	100/10 Mbit Automotive Ethernet (MATenet connector), CAN (FD) On-board PEmicro debugger (micro-USB connector) with virtual COM port for serial communication 20-Pin Arm Standard JTAG connector
Motor control software application	Field-oriented control (FOC) with field weakening for sinusoidal motor type (PMSM)- Available as ANSI C examples and model-based design for non-Autosar applications

Part number	MC_XTM4CK344 (MCSXTM4CK344 / MCDXTM4CK344)
Picture	
Supported products	
MCU	S32K3: S32K3 Microcontrollers for Automotive General Purpose
Analog	TJA1052IT: Galvanically-Isolated High-Speed CAN Transceiver TJA1153: Secure HS-CAN Transceiver with Sleep Mode TJA1021: ISO17987 LIN 2.1/SAE J2602 Transceiver TJA1101: TJA1101B, IEEE 100BASE-T1 Compliant Automotive Ethernet PHY Transceiver FS26: Safety System Basis Chip with Low Power, for ASIL D Systems
Hardware features	
Motor	Control of 3/6-phase AC motors types: PMSM / BLDC / ACIM / (optional DC)
Current and Voltage Sensing	DC Bus voltage and current, phase voltages, BEMF voltages, over voltage/current protections
Position Sensing	Resolver, encoder, HALL or sensorless techniques
Communication	Onboard isolated and nonisolated CAN Onboard LIN Onboard Ethernet (socket connector) Onboard S32K3 debug interface (including serial communication) JTAG debug interface
Sensors	Battery Voltage DC Bus voltage Phase voltages and currents BEMF voltages Over current/voltage/temperature protections
Motor control software application	Field-oriented control (FOC) with field weakening for sinusoidal motor type (PMSM). Available as ANSI C examples for non-Autosar applications

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[LKS32MC037QM6Q8-K](#) [LKS32MC074DF8Q8-K](#) [LKS32MC034DF6Q8-k](#) [LKS32MC051C6T8-K](#) [LKS32MC081C8T8-K](#)
[LKS32MC083C8T8-K](#) [LKS32MC081C8T8-GK](#) [LKS32MC057M6S8-K](#) [LKS32MC037M6S8-K](#) [LKS32MC071C8T8-K](#) [LKS32MC057M6S8-GK](#) [LKS32MC054DF6Q8-K](#) [LKS32AT085C8Q9-K](#) [LKS32MC082K8Q8-K](#) [LKS32MC087M6S8-K](#)