RICOH

RP550K001A

1A PWM/VFM Dual Step-down DC/DC Converter with Synchronous Rectifier

No.EA-285-210909

OUTLINE

The RP550K001A is a CMOS-based 1A*1 dual step-down DC/DC converter with synchronous rectifier. Internally, a single converter consists of oscillators, reference voltage units, error amplifiers, switching control circuits, soft-start circuit, latch type protection circuit, an under voltage lockout (UVLO) circuit, a thermal shutdown circuit and switching transistors.

Replacing diodes with built-in switching transistors improves the efficiency of rectification. Therefore, by simply using two inductors, resistors and capacitors as the external components, a low ripple high efficiency synchronous rectifier step-down DC/DC converter can be easily configured.

Latch type protection circuit latches the built-in driver to the OFF state during high load or if the output is short-circuited for a specified time (protection delay time). The latch protection circuit can be released by once setting the converter into the standby mode with the CE pin and then setting it back to the active mode, or, by turning the power off and back on. Setting the supply voltage lower than the UVLO detector threshold can also release the latch protection circuit. Thermal shutdown circuit detects overheating of the converter and stops the converter operation to protect it from damage if the junction temperature exceeds the specified temperature.

By inputting a signal to the MODE pin, the RP550K001A can choose PWM/VFM auto switching control or forced PWM control. In low output current, PWM/VFM auto switching control automatically switches from PWM mode to VFM mode in order to achieve high efficiency. Likewise, in low output current, forced PWM control switches at fixed frequency in order to reduce noise.

When the both converters are in PWM control, the converters operate with 180º turn-on phase shift of the switching transistors.

The RP550K001A is available in DFN(PLP)2730-12 package which achieves high-density mounting on boards.

*1 This is an approximate value, because output current depends on conditions and external components.

FEATURES

- Supply Current ······ Typ. 45μA (VFM mode with no load/ 1 channel)
- Standby Current ······ Max. 10μA
- Input Voltage Range ······ 2.3V to 5.5V (V_{SET} ≥ 0.8V)
- Output Voltage Range ······ Adjustable from 0.6V (Recommended range is up to 3.3V)
- Feedback Voltage Accuracy ······ ±9mV (V_{FB}=0.6V)
- Output Voltage Temperature Coefficient ······ ±100ppm/°C
- Oscillator Frequency ······ Typ. 2.25MHz
- Oscillator Maximum Duty ······ Min. 100%
- Built-in Driver ON Resistance ······ Typ. Pch. 0.25Ω, Nch. 0.21Ω (V_{IN}=3.6V)
- UVLO Detector Threshold ······ Typ. 2.0V
- Soft Start Time ······ Typ. 0.2ms
- Lx Current Limit Circuit ······ Typ. 1700mA/ channel
- Latch Type Protection Circuit ······ Typ. 1.5ms
- Package ······ DFN(PLP)2730-12

APPLICATION

- Power source for battery-powered equipment
- Power source for hand-held communication equipment, cameras, and VCRs
- Power source for Wireless LAN terminals

BLOCK DIAGRAM

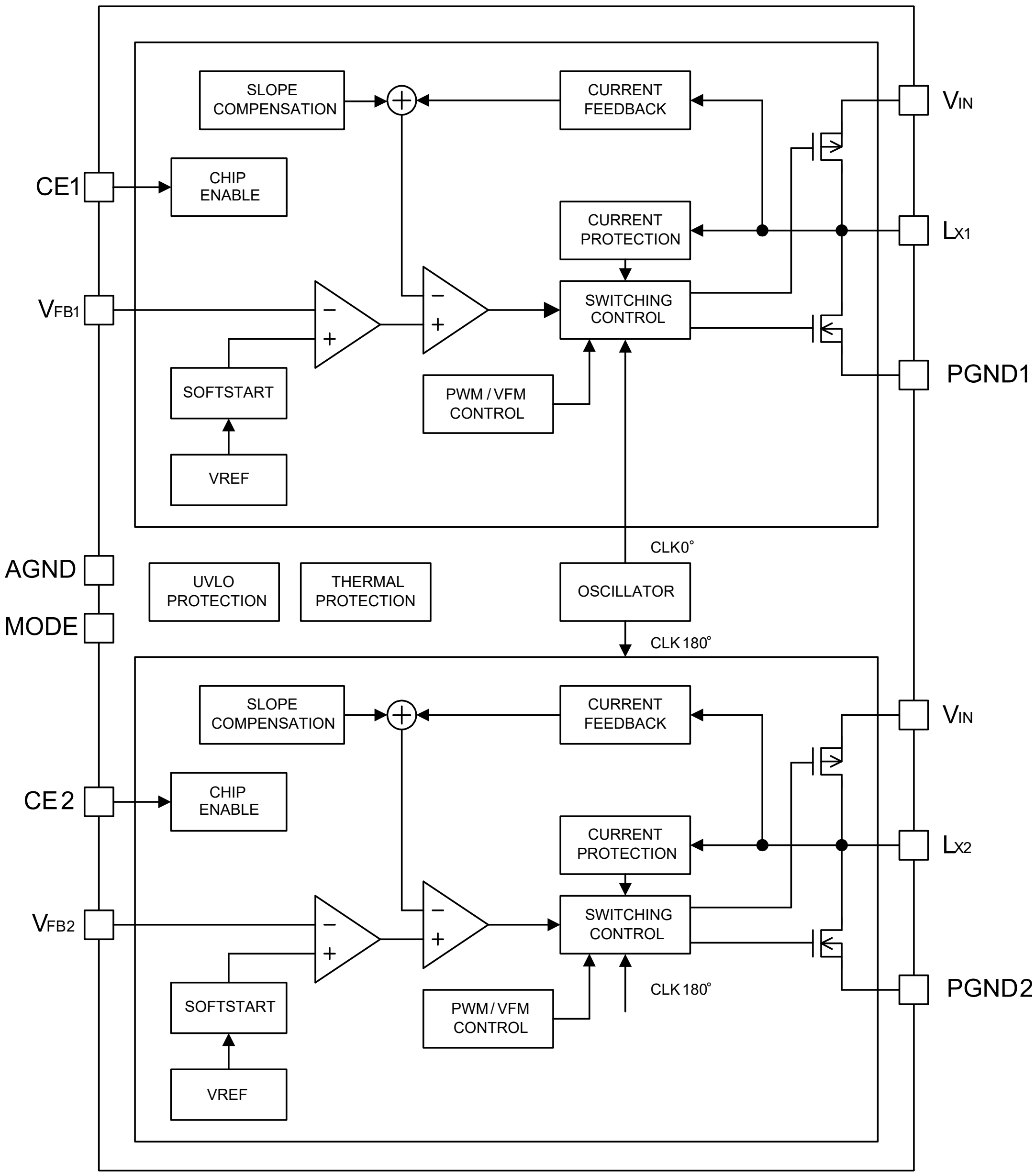


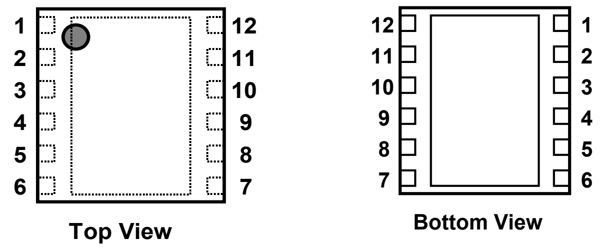
Figure 1. RP550K001A

SELECTION GUIDE

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
RP550K001A-TR	DFN(PLP)2730-12	5,000pcs	Yes	Yes
Output voltage is adjustable with external divider resistors. Recommended output voltage range is from 0.6V to 3.3V.				

PIN CONFIGURATIONS

● DFN(PLP)-2730-12



PIN DESCRIPTIONS

RP550K001A: DFN(PLP)2730-12

Pin No.	Symbol	Description
1	V_{FB2}	Channel 2 Feedback Pin
2	MODE	Mode Control Pin ("H" forced PWM control, "L" PWM/VFM auto switching control)
3	V_{IN}	Input Pin*2
4	V_{IN}	Input Pin*2
5	AGND	Analog Ground Pin*3
6	V_{FB1}	Channel 1 Feedback Pin
7	CE1	Channel 1 Chip Enable Pin ("H" active)
8	L_{X1}	Channel 1 Lx Switching Pin
9	PGND1	Channel 1 Power Ground Pin*3
10	PGND2	Channel 2 Power Ground Pin*3
11	L_{X2}	Channel 2 Lx Switching Pin
12	CE2	Channel 2 Chip Enable Pin ("H" active)

The exposed tab on the bottom of the package enhances thermal performance and is electrically connected to GND (substrate level). It is recommended that the exposed tab be connected to the ground plane on the board or otherwise be left open.
*2 No.3 pin and No.4 pin must be wired to the V_{IN} plane when mounting on boards.
*3 No.5 pin, No.9 pin and No.10 pin be must wired to the GND plane when mounting on boards.

ABSOLUTE MAXIMUM RATINGS

(AGND=PGND1=PGND2=0V)

Symbol	Item		Rating	Unit
V_{IN}	V_{IN} Input Pin Voltage		-0.3 to 6.5	V
V_{LX1}, V_{LX2}	L_{X1}, L_{X2} Pin Voltage		-0.3 to V_{IN} + 0.3	V
V_{CE1}, V_{CE2}	CE1, CE2 Pin Voltage		-0.3 to 6.5	V
V_{MODE}	MODE Pin Voltage		-0.3 to 6.5	V
V_{FB1}, V_{FB2}	V_{FB1}, V_{FB2} Pin Voltage		-0.3 to 6.5	V
I_{LX1}, I_{LX2}	L_{X1}, L_{X2} Pin Output Current		1.7	A
P_D	Power Dissipation*4	Standard Land Pattern*4	1000	mW
		High Wattage Land Pattern*4	1950	mW
Ta	Operating Temperature Range		-40 to +85	°C
Tstg	Storage Temperature Range		-55 to +125	°C

*4 For more information about Power Dissipation, Standard Land Pattern and High Wattage Land Pattern, please refer to *PACKAGE INFORMATION*.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

ELECTRICAL CHARACTERISTICS

Test Circuit is “OPEN LOOP” and Test Condition is AGND=PGND1=PGND2=0V, unless otherwise noted.

RP550K001A (Ta=25°C)

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V_{IN}	Operating Input Voltage	0.8V ≤ V_{SET}*6	2.3		5.5	V
		0.6V ≤ V_{SET} < 0.8V	2.3		4.5	
V_{FB}	Feedback Voltage	V_{IN}=V_{CE1}=V_{CE2}=3.6V	0.591	0.600	0.609	V
ΔV_{FB} /ΔTa	Output Voltage Temperature Coefficient	-40°C ≤ Ta ≤ 85°C		±100		ppm /°C
fosc	Oscillator Frequency	V_{IN}=V_{CE1}=V_{CE2}=3.6V	2.00	2.25	2.50	MHz
I_{DD1}	Supply Current 1*5	V_{IN}=V_{CE1}=V_{CE2}=5.5V, V_{FB1}=V_{FB2}=0.45V, V_{MODE}=0V		800	1100	μA
I_{DD2}	Supply Current 2*5	V_{IN}=V_{CE1}=V_{CE2}=5.5V, V_{FB1}=V_{FB2}=0.75V, V_{MODE}=0V		45	60	μA
I_{DD3}	Supply Current 3*5	V_{IN}=V_{CE1}=V_{CE2}=5.5V, V_{FB1}=V_{FB2}=0.75V, V_{MODE}=5.5V		800	1100	μA
Istandby	Standby Current *5	V_{IN}=5.5V, V_{CE1}=V_{CE2}=0V		0	10	μA
I_{CEH}	CE “H” Input Current*5	V_{IN}=5.5V, V_{CE1}=V_{CE2}=5.5V	-1	0	1	μA
I_{CEL}	CE “L” Input Current*5	V_{IN}=5.5V, V_{CE1}=V_{CE2}=0V	-1	0	1	μA
I_{MODEH}	MODE “H” Input Current	V_{IN}=V_{MODE}=5.5V	-1	0	1	μA
I_{MODEL}	MODE “L” Input Current	V_{IN}=5.5V, V_{MODE}=0V	-1	0	1	μA
I_{FBH}	V_{FB} “H” Input Current *5	V_{IN}=V_{FB1}=V_{FB2}=5.5V, V_{CE1}=V_{CE2}=0V	-1	0	1	μA
I_{FBL}	V_{FB} “L” Input Current*5	V_{IN}=5.5V, V_{CE1}=V_{CE2}=V_{FB1}=V_{FB2}=0V	-1	0	1	μA
$I_{LXLEAKH}$	L_X Leakage Current “H”*5	V_{IN}=V_{LX1}=V_{LX2}=5.5V, V_{CE1}=V_{CE2}=0V	-1	0	5	μA
$I_{LXLEAKL}$	L_X Leakage Current “L”*5	V_{IN}=5.5V, V_{CE1}=V_{CE2}=V_{LX1}=V_{LX2}=0V	-5	0	1	μA
V_{CEH}	CE “H” Input Voltage	V_{IN}=5.5V	1.0			V
V_{CEL}	CE “L” Input Voltage	V_{IN}=2.3V			0.4	V
V_{MODEH}	MODE “H” Input Voltage	V_{IN}=5.5V	1.0			V
V_{MODEL}	MODE “L” Input Voltage	V_{IN}=2.3V			0.4	V
R_{ONP}	Pch Transistor ON Resistance	V_{IN}=3.6V, I_{LX1}=I_{LX2}=-100mA		0.25		Ω
R_{ONN}	Nch Transistor ON Resistance	V_{IN}=3.6V, I_{LX1}=I_{LX2}=-100mA		0.21		Ω
Maxduty	Oscillator Maximum Duty Cycle		100			%
tstart	Soft-start Time	V_{IN}=V_{CE1}=V_{CE2}=3.6V		200	300	μs
I_{LXLIM}	L_X Limit Current	V_{IN}=V_{CE1}=V_{CE2}=3.6V	1400	1700		mA
tprot	Protection Delay Time	V_{IN}=V_{CE1}=V_{CE2}=3.6V	0.5	1.5	5	ms
V_{UVLO1}	UVLO Detector Threshold	V_{IN}=V_{CE1}=V_{CE2}	1.9	2.0	2.1	V
V_{UVLO2}	UVLO Released Voltage	V_{IN}=V_{CE1}=V_{CE2}	2.0	2.1	2.2	V

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
T_{TSD}	Thermal Shutdown Temperature	Junction Temperature		140		°C
T_{TSR}	Thermal Shutdown Released Temperature	Junction Temperature		100		°C

All test items listed under *Electrical Characteristics* (P.5, P.6) are done under the pulse load condition (Tj≈Ta=25ºC) except Output Voltage Temperature Coefficient and Oscillator Maximum Duty Cycle.

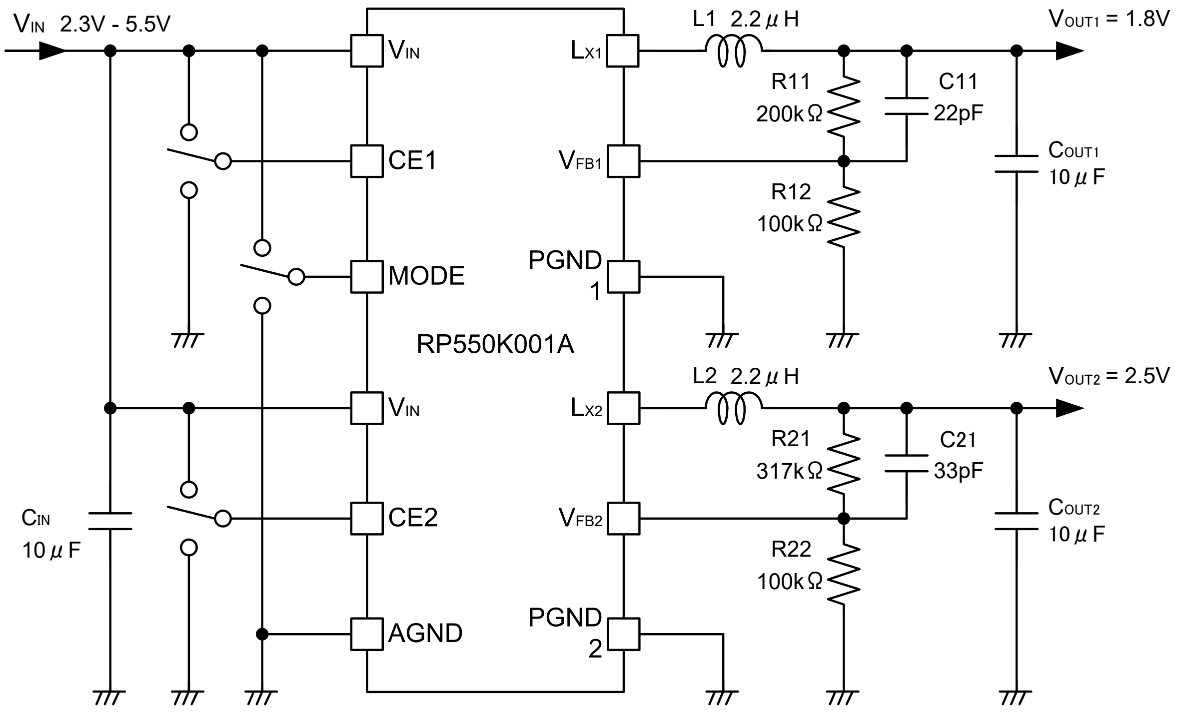
*5 For Standby Current, the sum of Channel 1 and Channel 2 is indicated.
As for the following currents, either Channel 1 value or Channel 2 value is indicated.

- Supply Current 1 to Supply Current 3
- CE "H" Input Current
- CE "L" Input Current
- V_{FB} "H" Input Current
- V_{FB} "L" Input Current
- L_X Leakage Current "H"
- L_X Leakage Current "L"

*6 V_{SET}= Set Output Voltage

TYPICAL APPLICATION

Figure 2. RP550K001A



Note: MODE="H" forced PWM control
MODE="L"PWM/VFM auto switching control

Table 1. Recommended Components: 0.8V ≤ V_{SET} ≤ 3.3V

Symbol	Value	Components	Part Number
C_{IN}	10μF	Ceramic Capacitor	C1608JB0J106M(TDK)
C_{OUT}	10μF	Ceramic Capacitor	C1608JB0J106M(TDK)
L	2.2μH	Inductor	MIPSA2520D2R2(FDK)

Table 2. Recommended Components: 0.6V ≤ V_{SET} < 0.8V

Symbol	Value	Components	Part Number
C_{IN}	10μF	Ceramic Capacitor	C1608JB0J106M(TDK)
C_{OUT}	10μF x 2	Ceramic Capacitor	C1608JB0J106M(TDK)
L	1.5μH	Inductor	MIPSA2520D1R5(FDK)

TECHNICAL NOTES

When using the RP550K001A, please consider the following points.

- AGND, PGND1 and PGND2 must be wired to the GND plane when mounting on boards.
- The V_{IN} pins must be wired to the V_{IN} plane when mounting on boards
- Ensure the V_{IN} and GND lines are sufficiently robust. A large switching current flows through the GND line, the V_{DD} line, the V_{OUT} line, an inductor, and L_X. If their impedance is too high, noise pickup or unstable operation may result. Set external components as close as possible to the IC and minimize the wiring between the components and the IC, especially between a capacitor and the V_{IN} pin. The wiring between V_{FB} and load and between L and V_{OUT} should be separated.
- Choose a low ESR ceramic capacitor. The ceramic capacitance of a capacitor (C_{IN}) connected between V_{IN} and GND should be more than or equal to 10μF. The ceramic capacitance of a capacitor (C_{OUT}) connected between V_{OUT} and GND should be 10μF to 20μF. Please be aware of the characteristics of bias dependence and temperature fluctuation of ceramic capacitor.
- The phase compensation of this IC is designed according to the above C_{OUT} values and L values. For stable operation, a ceramic capacitance value and an inductance value have to be selected within these values. Choose an inductor that has small DC resistance, has enough allowable current and is hard to cause magnetic saturation. If the inductance value of an inductor is extremely small, the peak current of L_X may increase along with the load current. As a result, over current protection circuit may start to operate when the peak current of L_X reaches to “L_X limit current”.
- Over current protection circuit and latch type protection circuit may be affected by self-heating or power dissipation environment.
- The output voltages (V_{OUT1}, V_{OUT2}) are adjustable by changing the values of R11, R12, R21, and R22 as follows.

  $V_{OUT1} = 0.6 \times (R11 + R12) / R12$ (Recommended range: $0.6V \leq V_{OUT1} \leq 3.3V$)
  $V_{OUT2} = 0.6 \times (R21 + R22) / R22$ (Recommended range: $0.6V \leq V_{OUT2} \leq 3.3V$)

- If R11, R12, R21, and R22 are too large, the impedances of V_{FB1} and V_{FB2} also become large, as a result, the IC could be easily affected by noise. For this reason, R12 and R22 should be 100kΩ or less. If the operation becomes unstable dues to the high impedances, the impedances should be decreased.
- C11 and C21 can be calculated by the following equations. Please use the value close to the calculation result.

  $C11 = 2.2 \times 10^{-6} / R12$ [F] ($0.6V \leq V_{OUT1} \leq 3.3V$)
  $C21 = 2.2 \times 10^{-6} / R22$ [F] ($0.6V \leq V_{OUT2} \leq 3.3V$)

- The recommended resistance values for R11, R12, R21, R22, C11, and C21 are as follows.

Table 3. Recommended Resistor and Capacitor Values

Output Voltage	Resistor [kΩ]		Capacitor [pF]
V_{OUT1}, V_{OUT2} [V]	R11, R21	R12, R22	C11, C21
0.6	0	100	-
0.7	16.7	100	22
0.8	33.3	100	22
1.2	100	100	22
1.8	200	100	22
2.5	317	100	22
3.3	450	100	22

★ The performance of power source circuits using this IC largely depends on the peripheral circuits. When selecting the peripheral components, please consider the conditions of use. Do not allow each component, PCB pattern and the IC to exceed their respected rated values (voltage, current, and power) when designing the peripheral circuits.

OPERATION OF STEP-DOWN DC/DC CONVERTER AND OUTPUT CURRENT

The step-down DC/DC converter charges energy in the inductor when L_X Tr. turns "ON", and discharges the energy from the inductor when L_X Tr. turns "OFF" and controls with less energy loss, so that a lower output voltage (V_{OUT}) than the input voltage (V_{IN}) can be obtained. The operation of the step-down DC/DC converter is explained in the following figures.

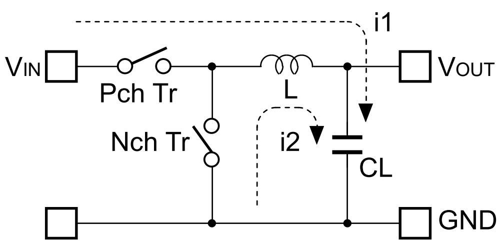


Figure 3. Basic Circuit

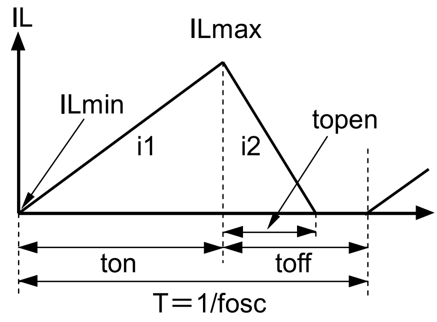


Figure 4. Inductor Current (IL) flowing through Inductor

Step 1: Pch Tr. is "ON" and current IL=i1 flows, and energy is charged into CL. At this moment, in proportion to the time while Pch Tr. is "ON" (t_{ON}), IL=i1 increases from $IL=IL_{MIN}=0$, and reaches IL_{MAX}.

Step 2: While Pch Tr. is "OFF" and synchronous rectifier Nch Tr. is "ON", L tries to maintain $IL= IL_{MAX}$, so IL=i2 flows into L.

Step 3: IL=i2 decreases gradually and reaches $IL=IL_{MIN}=0$ after the time while Pch Tr. is "OFF" and $IL=IL_{MIN}=0$ (t_{OPEN}). Then, synchronous rectifier Nch Tr. turns "OFF". Provided that in the continuous mode, next cycle starts before $IL=IL_{MIN}=0$ because the time while Pch Tr. is "OFF" (t_{OFF}) is not enough. In this case, IL value increases from this IL_{MIN} (>0).

In the case of PWM mode, V_{OUT} is maintained by controlling t_{ON}. During PWM mode, the oscillator frequency (f_{OSC}) is being maintained constant.

As shown in Figure 4., while the step-down operation is constant, the minimum inductor current (IL_{MIN}) and the maximum inductor current (IL_{MAX}) when Pch Tr. is "ON" would be same as the maximum and the minimum inductor currents when Pch Tr. is "OFF".

The current differential between IL_{MAX} and IL_{MIN} is described as ΔI.

$\Delta I = IL_{MAX} - IL_{MIN} = V_{OUT} \times t_{OPEN} / L = (V_{IN} - V_{OUT}) \times t_{ON} / L$ ··· Equation 1

However,

$$T = 1 / f_{OSC} = t_{ON} + t_{OFF}$$

$$\text{duty}(\%)= t_{ON} / T \times 100 = t_{ON} \times f_{OSC} \times 100$$

$$t_{OPEN} \leq t_{OFF}$$

In Equation 1, "$V_{OUT} \times t_{OPEN} / L$" show the amount of current change at " OFF ". Also, "$(V_{IN} - V_{OUT}) \times t_{ON} / L$" shows the amount of current change at " ON ".

Discontinuous Mode and Continuous Mode

As illustrated in Figure 5., when the output current (I_{OUT}) is relatively small, t_{OPEN} < t_{OFF}. In this case, the energy charged into the inductor during t_{ON} will be completely discharged during t_{OFF}, as a result, IL_{MIN}=0. This is called discontinuous mode.

When I_{OUT} is gradually increased, eventually t_{OPEN}=t_{OFF} and when I_{OUT} is increased further, eventually IL_{MIN}>0. This is called continuous mode.

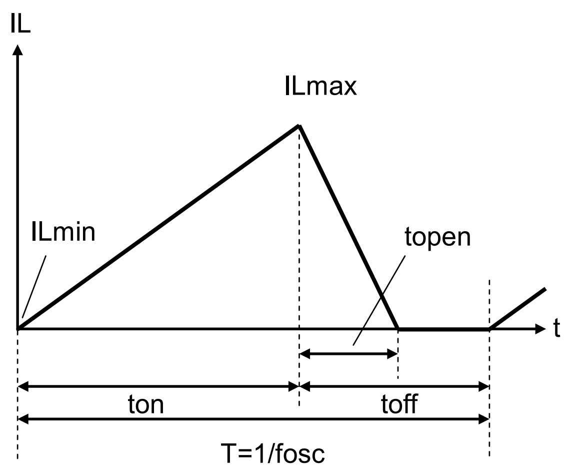


Figure 5. Discontinuous Mode

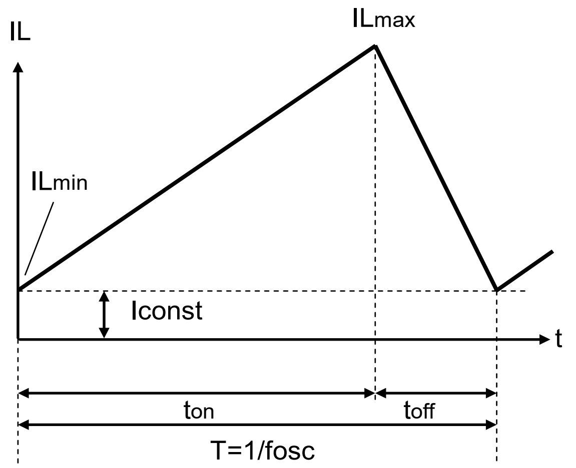


Figure 6. Continuous Mode

In the continuous mode, the solution of Equation 1 is tonc

$$tonc = T \times V_{OUT} / V_{IN} \cdots\cdots \text{Equation 2}$$

When t_{ON}<tonc, it is discontinuous mode, and when t_{ON}=tonc, it is continuous mode.

Forced PWM Mode and VFM Mode

By setting the MODE pin to "H", the IC switches the frequency at the fixed rate to reduce noise even when the output load is light. Therefore, when I_{OUT} is $\Delta IL/2$ or less, IL_{MIN} becomes less than 0. That is, the accumulated electricity in CL is discharged through the IC side while IL is increasing from IL_{MIN} to 0 during ton time, and also while IL is decreasing from 0 to IL_{MIN} during t_{OFF} time.

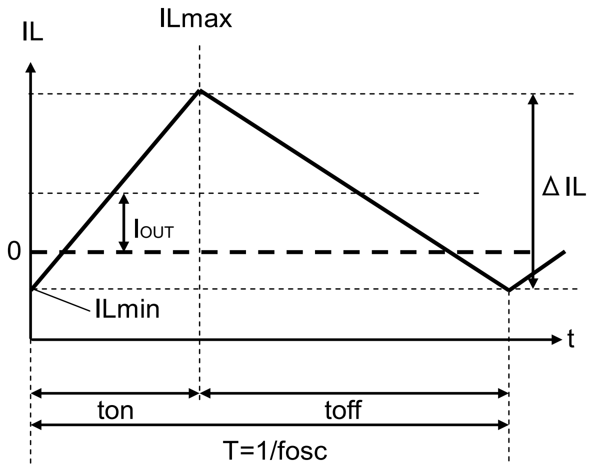


Figure 7. Forced PWM Mode

VFM Mode

By setting the MODE pin to "L", in low output current, the IC automatically switches into VFM mode in order to achieve high efficiency. In VFM mode, ton is forced to end when the inductor current reaches the pre-set IL_{MAX}. With the RP550K001A, IL_{MAX} in the VFM mode is typically set to 280mA. When t_{ON} reaches 1.5 times of $T=1/f_{OSC}$, t_{ON} will be forced to end even if the inductor current is not reached IL_{MAX}.

Output Current and Selection of External Components

The following equations explain the relationship between output current and peripheral components used in Figure 2. in *Typical Applications* (P.7).

Ripple Current P-P value is described as I_{RP}, ON resistance of Pch Tr. is described as R_{ONP}, ON resistance of Nch Tr. is described as R_{ONN}, and DC resistor of the inductor is described as R_L.

First, when Pch Tr. is "ON", the following equation is satisfied.

$$V_{IN} = V_{OUT} + (R_{ONP} + R_L) \times I_{OUT} + L \times I_{RP} / ton \cdots\cdots \text{Equation 3}$$

Second, when Pch Tr. is "OFF" (Nch Tr. is "ON"), the following equation is satisfied.

$$L \times I_{RP} / toff = R_{ONN} \times I_{OUT} + V_{OUT} + R_L \times I_{OUT} \cdots\cdots \text{Equation 4}$$

Put Equation 4 into Equation 3 to solve ON duty of Pch Tr. (D_{ON} = ton / (toff + ton)):

$$D_{ON} = (V_{OUT} + R_{ONN} \times I_{OUT} + R_L \times I_{OUT})/(V_{IN} + R_{ONN} \times I_{OUT} - R_{ONP} \times I_{OUT}) \cdots\cdots \text{Equation 5}$$

Ripple Current is described as follows:

$$I_{RP} = (V_{IN} - V_{OUT} - R_{ONP} \times I_{OUT} - R_L \times I_{OUT}) \times D_{ON} / fosc / L \cdots\cdots \text{Equation 6}$$

Peak current that flows through L and L_X Tr. is described as follows:

$$IL_{XMAX} = I_{OUT} + I_{RP} / 2 \cdots\cdots \text{Equation 7}$$

★ Please consider IL_{XMAX} when setting conditions of input and output, as well as selecting the external components.
★ The above calculation formulas are based on the ideal operation of the ICs in continuous mode.

TIMING CHART

(1) Soft Start Time

Starting-up with CE Pin

The IC starts to operate when the CE pin voltage (V_{CE}) exceed the threshold voltage. The threshold voltage is preset between CE “H” input voltage (V_{CEH}) and CE “L” input voltage (V_{CEL}).

After the start-up of the IC, soft-start circuit starts to operate. Then, after a certain period of time, the reference voltage (V_{REF}) in the IC gradually increases up to the specified value.

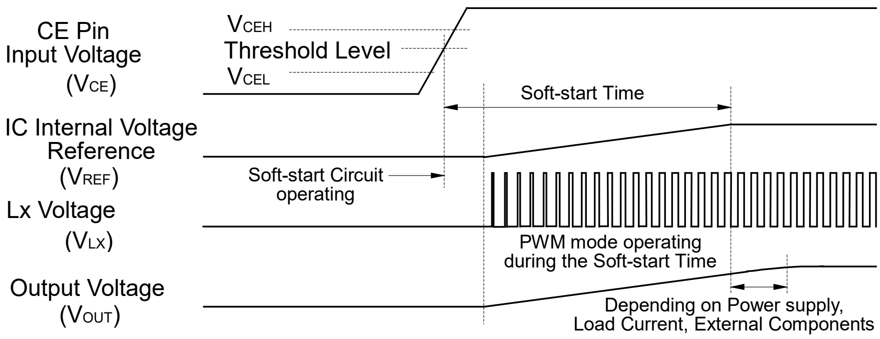


Figure 9. Timing Chart

Soft-start time starts when soft-start circuit activates, and ends when the reference voltage reaches the specified voltage.

★ Soft start time is not always equal to the turn-on speed of the step-down DC/DC converter. Please note that the turn-on speed could be affected by the power supply capacity, the output current, the inductance value and the C_{OUT} value.

Starting-up with Power Supply

After the power-on, the IC starts to operate when V_{IN} exceed the UVLO released voltage (V_{UVLO2}). Soft-start circuit starts to operate and then after a certain period of time, V_{REF} in the IC gradually increases up to the specified value. Soft-start time starts when soft-start circuit activates, and ends when V_{REF} reaches the specified voltage.

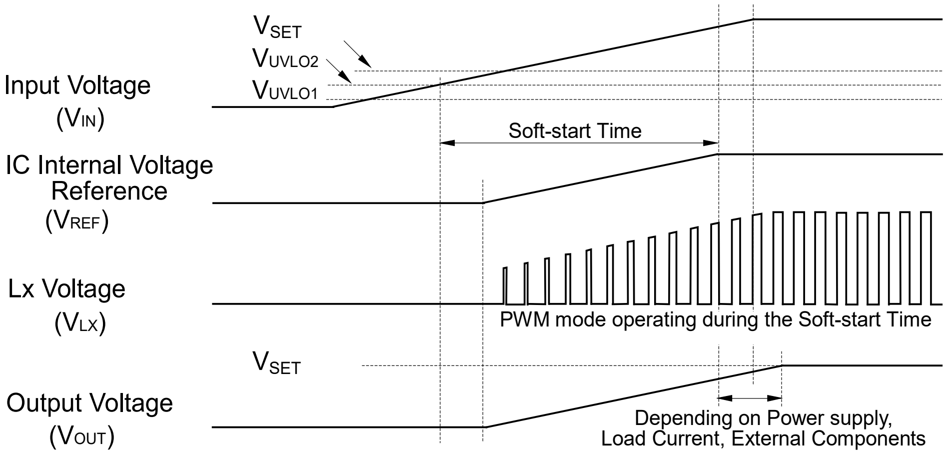


Figure 10. Timing Chart

Soft-start time starts when soft-start circuit activates, and ends when the reference voltage reaches the specified voltage.

★ Please note that the turn-on speed of V_{OUT} could be affected by the power supply capacity, the output current, the inductance value, the C_{OUT} value and the turn-on speed of V_{IN} determined by C_{IN}.

(2) Under Voltage Lockout (UVLO) Circuit

If V_{IN} becomes lower than the setting voltage (V_{SET}), the step-down DC/DC converter stops the switching operation and ON duty becomes 100%, and then V_{OUT} gradually drops according to V_{IN}.

If the V_{IN} drops more and becomes lower than the UVLO detector threshold (V_{UVLO1}), the UVLO circuit (UVLO) starts to operate, V_{REF} stops, and Pch and Nch built-in switch transistors turn "OFF". As a result, V_{OUT} drops according to the C_{OUT} capacitance value and the load.

To restart the operation, V_{IN} needs to be higher than V_{UVLO2}. The timing chart below shows the voltage shifts of V_{REF}, V_{LX} and V_{OUT} when V_{IN} value is varied.

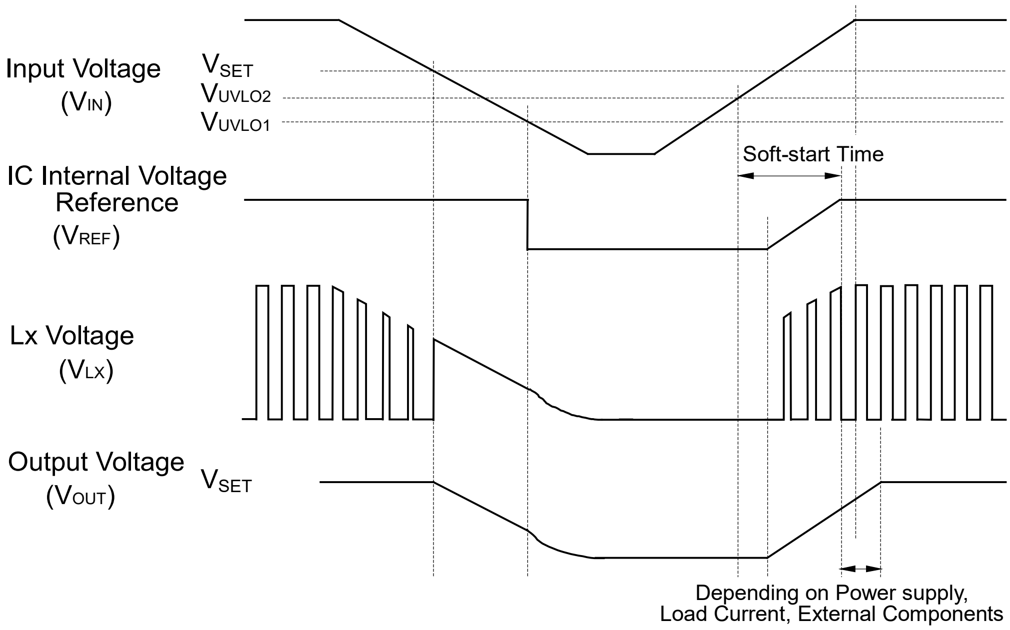


Figure 11. Timing Chart

★ Falling edge (operating) and rising edge (releasing) waveforms of V_{OUT} could be affected by the initial voltage of C_{OUT} and the output current of V_{OUT}.

(3) Over Current Protection Circuit, Latch Type Protection Circuit

Over current protection circuit supervises the inductor peak current (the peak current flowing through Pch Tr.) in each switching cycle, and if the current exceeds the Lx current limit (I_{LXLIM}), it turns off Pch Tr. I_{LXLIM} of the RP550K001A is set to Typ.1700mA.

Latch type protection circuit latches the built-in driver to the OFF state and stops the operation of the step-down DC/DC converter if the over current status continues or V_{OUT} continues being the half of the setting voltage for equal or longer than protection delay time (tprot).

Note: I_{LXLIM} and tprot could be easily affected by self-heating or ambient environment. If the V_{IN} drops dramatically or becomes unstable due to short-circuit, protection operation and tprot could be affected.

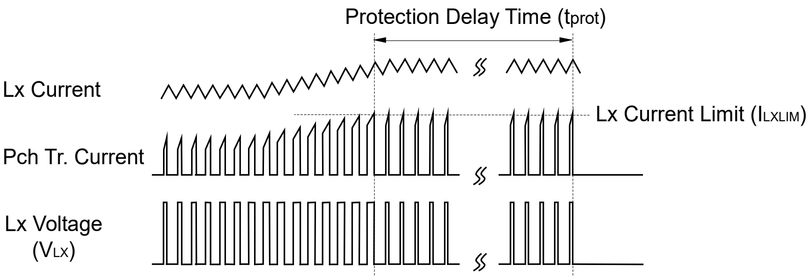


Figure 12. Protection Delay Time

To release the latch type protection circuit, restart the IC by inputting "L" signal to the CE pin, or restart the IC with power-on or make the supply voltage lower than V_{UVLO1}.

The timing chart below shows the voltage shifts of V_{LX} and V_{OUT} when the IC status is changed by the following orders: V_{IN} and V_{CE} rising → stable operation → high load → CE reset → stable operation → high load → V_{IN} falling → V_{IN} recovering (UVLO reset) → stable operation.

(1)(2) If the large current flows through the circuit or the IC goes into low V_{OUT} condition due to short-circuit or other reasons, the latch type protection circuit latches the built-in driver to “OFF” state after tprot. Then, V_{LX} becomes "L" and V_{OUT} turns “OFF”.

(3) The latch type protection circuit is released by CE reset, which puts the IC into "L" once with the CE pin and back into "H".

(4) The latch type protection circuit is released by UVLO reset, which makes V_{IN} lower than V_{UVLO1}.

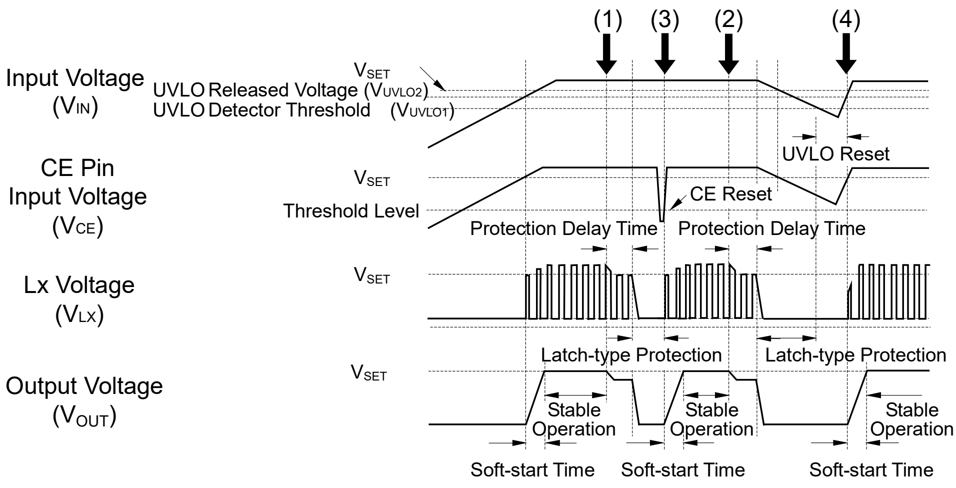
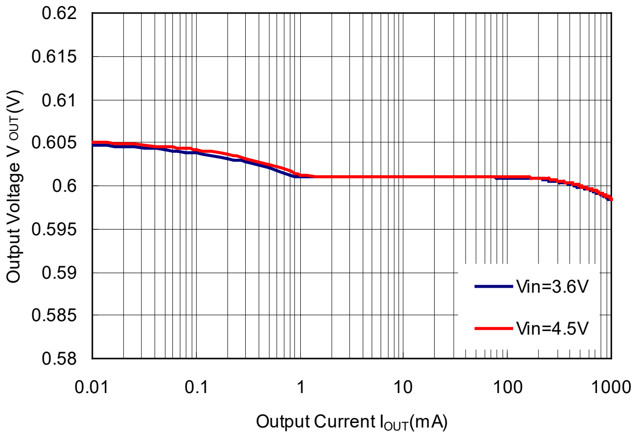


Figure 13. Timing Chart

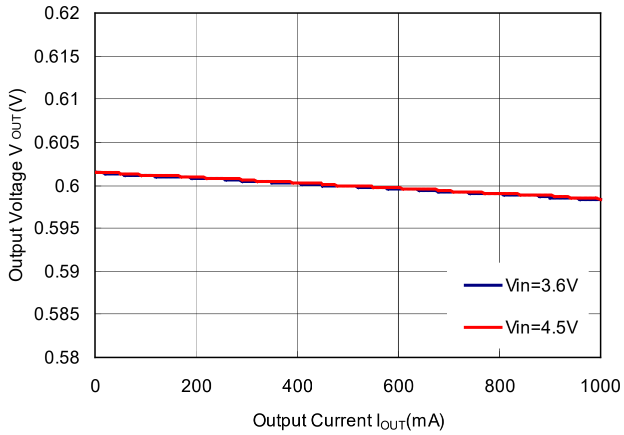
CHARACTERISTICS

1) Output Voltage vs. Output Current

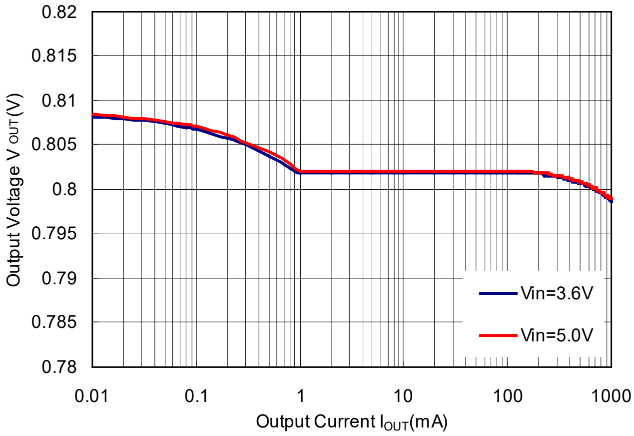
RP550K001A V_{OUT}=0.6V
MODE="L"PWM/VFM Auto Switching Control



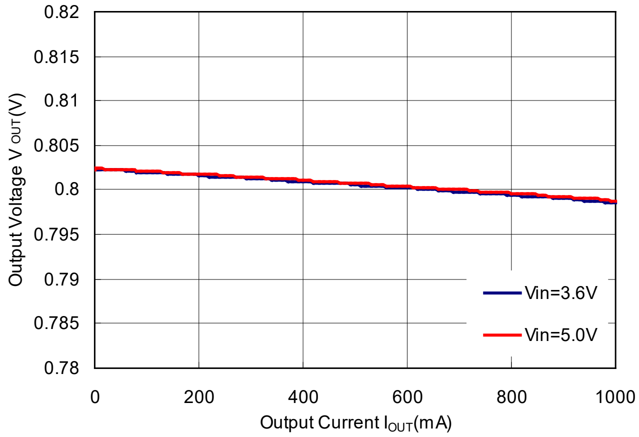
RP550K001A V_{OUT}=0.6V
MODE="H" Forced PWM Control



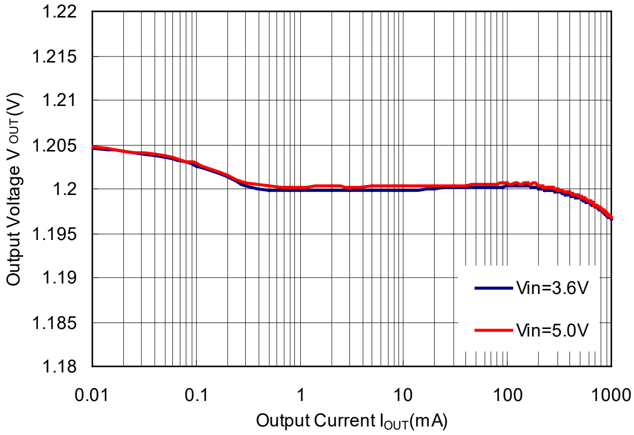
RP550K001A V_{OUT}=0.8V
MODE="L"PWM/VFM Auto Switching Control



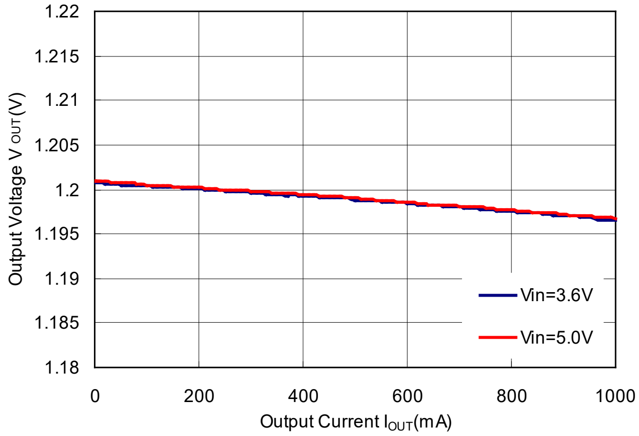
RP550K001A V_{OUT}=0.8V
MODE="H" Forced PWM Control



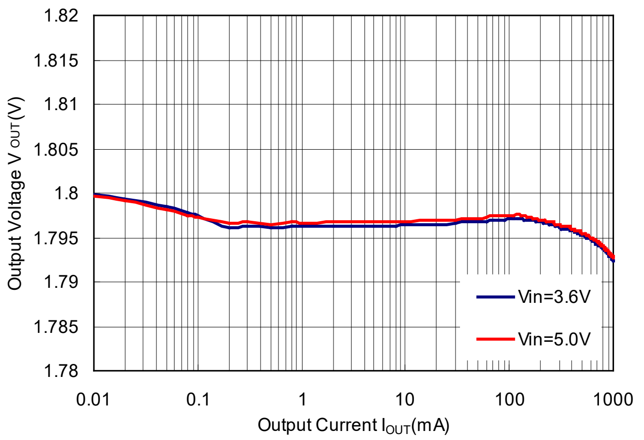
RP550K001A V_{OUT}=1.2V
MODE="L"PWM/VFM Auto Switching Control



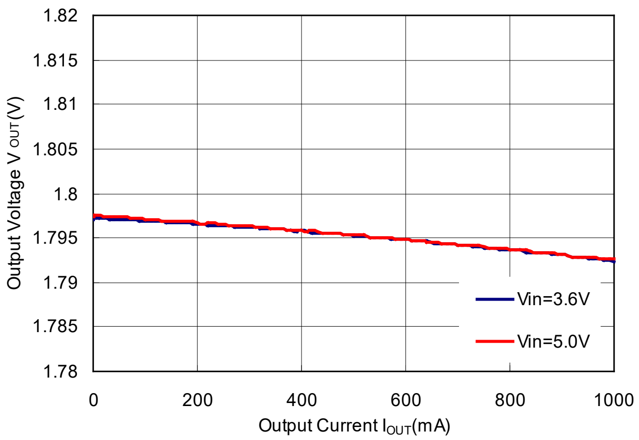
RP550K001A V_{OUT}=1.2V
MODE="H" Forced PWM Control



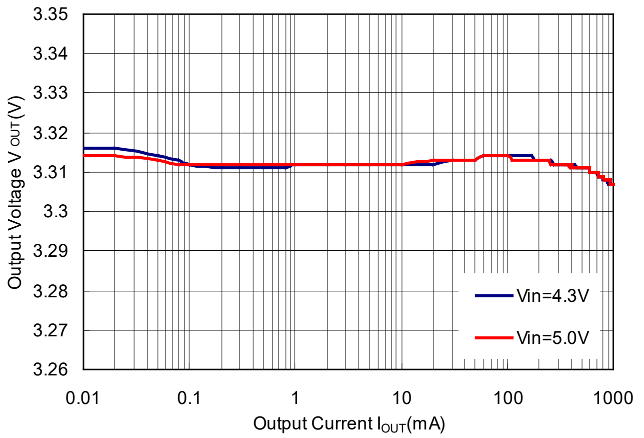
RP550K001A VOUT=1.8V
MODE="L"PWM/VFM Auto Switching Control



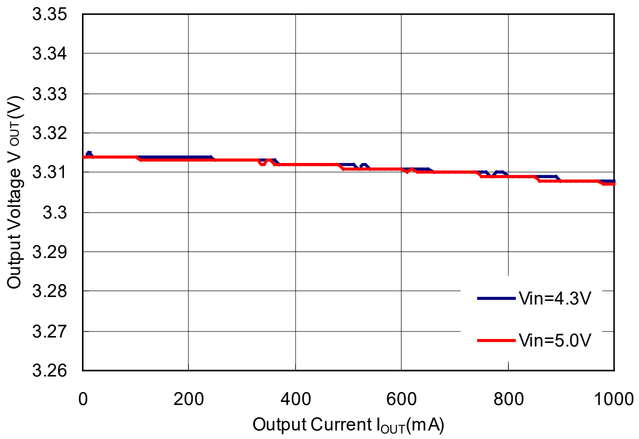
RP550K001A VOUT=1.8V
MODE="H" Forced PWM Control



RP550K001A VOUT=3.3V
MODE="L"PWM/VFM Auto Switching Control

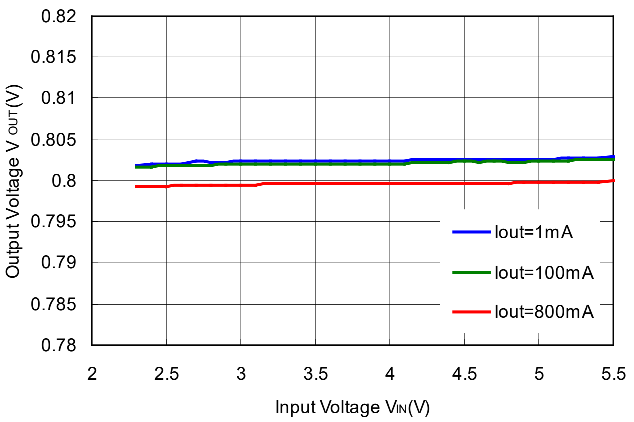


RP550K001A VOUT=3.3V
MODE="H" Forced PWM Control

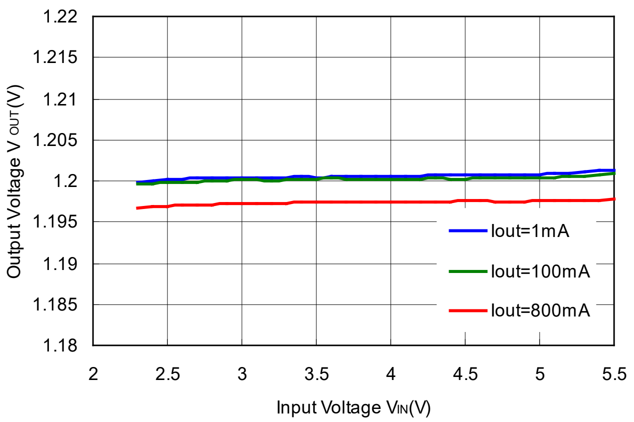


2) Output Voltage vs. Input Voltage

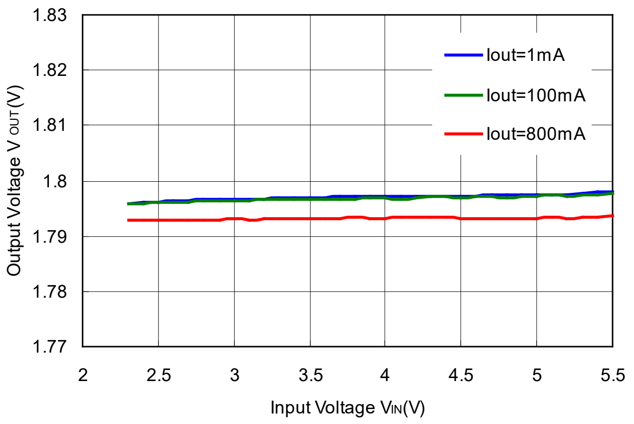
RP550K001A VOUT=0.8V
MODE="H" Forced PWM Control



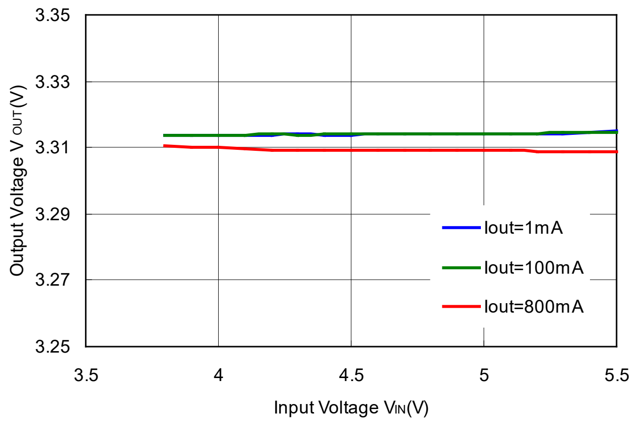
RP550K001A VOUT=1.2V
MODE="H" Forced PWM Control



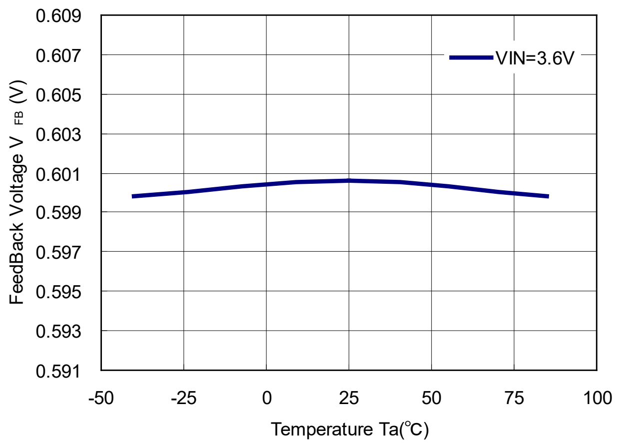
RP550K001A V_{OUT}=1.8V
MODE="H" Forced PWM Control



RP550K001A V_{OUT}=3.3V
MODE="H" Forced PWM Control

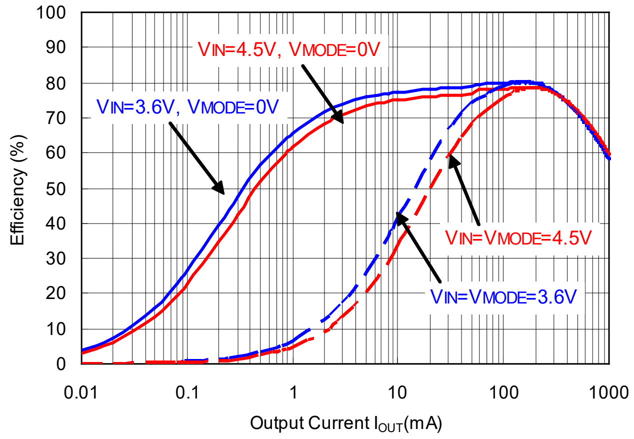


3) Feedback Voltage vs. Ambient Temperature

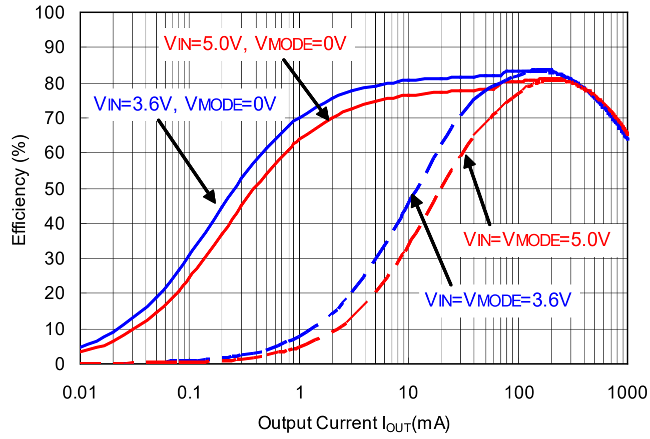


4) Efficiency vs. Output Current

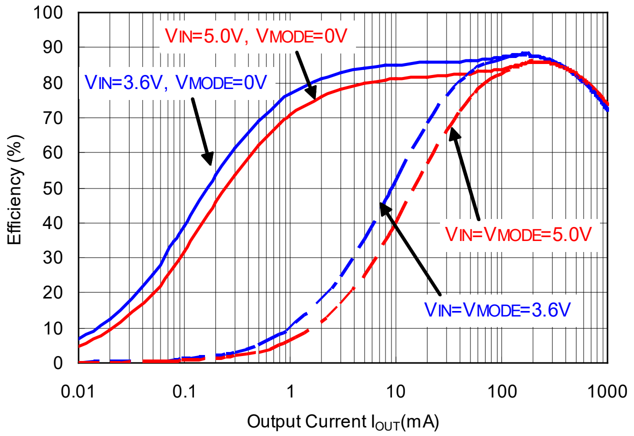
RP550K001A V_{OUT}=0.6V



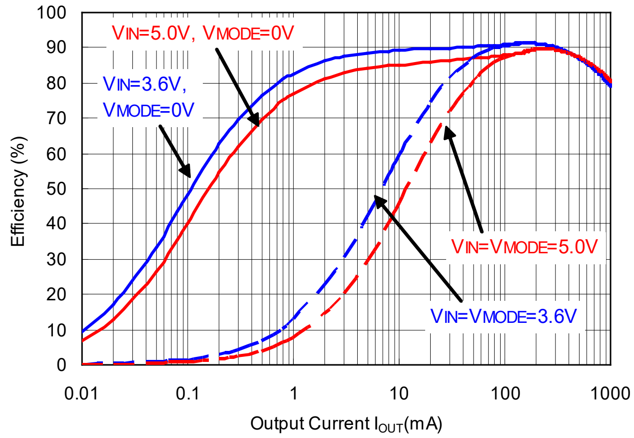
RP550K001A V_{OUT}=0.8V



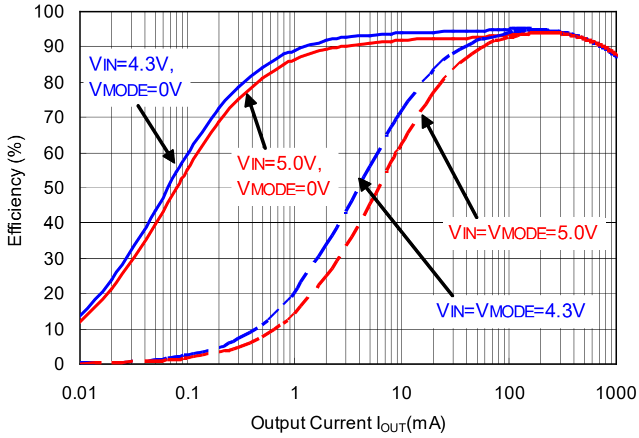
RP550K001A V_{OUT}=1.2V



RP550K001A V_{OUT}=1.8V



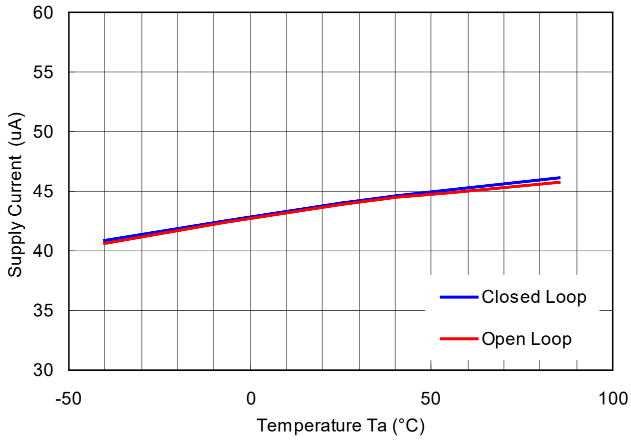
RP550K001A V_{OUT}=3.3V



5) Supply Current vs. Ambient Temperature

RP550K001A V_{OUT}=1.8V(V_{IN}=5.5V)

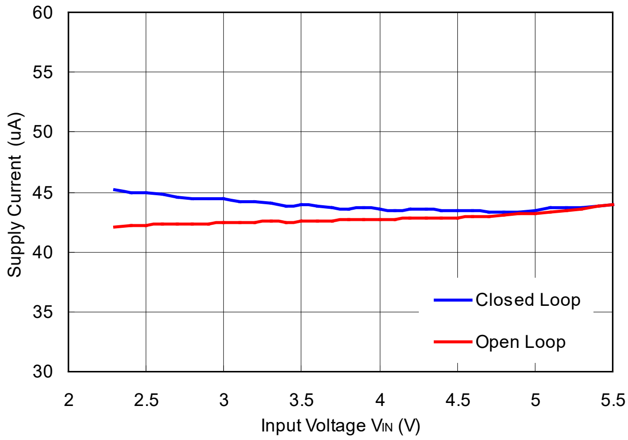
MODE="L"PWM/VFM Auto Switching Control



6) Supply Current vs. Input Voltage

RP550K001A V_{OUT}=1.8V

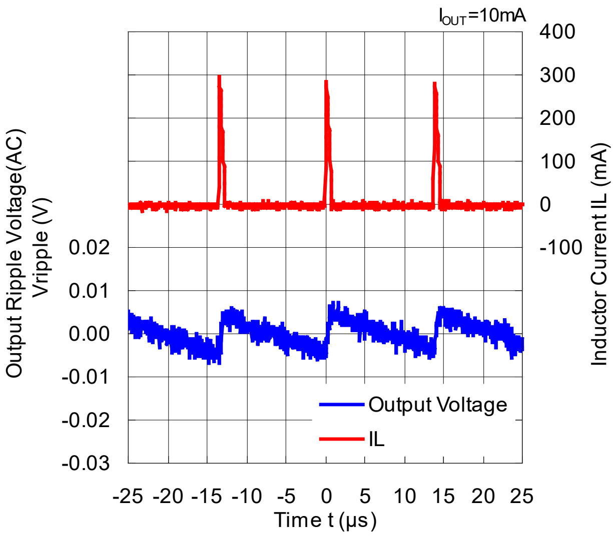
MODE="L"PWM/VFM Auto Switching Control



7) Output Voltage Waveform

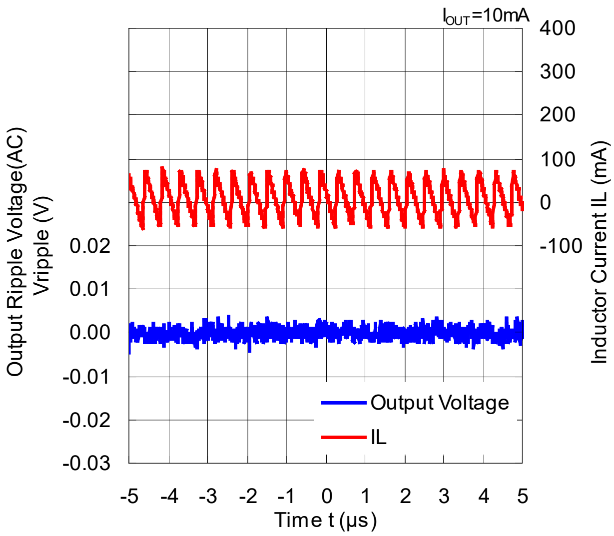
RP550K001A V_{OUT}=0.6V(V_{IN}=3.6V)

MODE="L"PWM/VFM Auto Switching Control

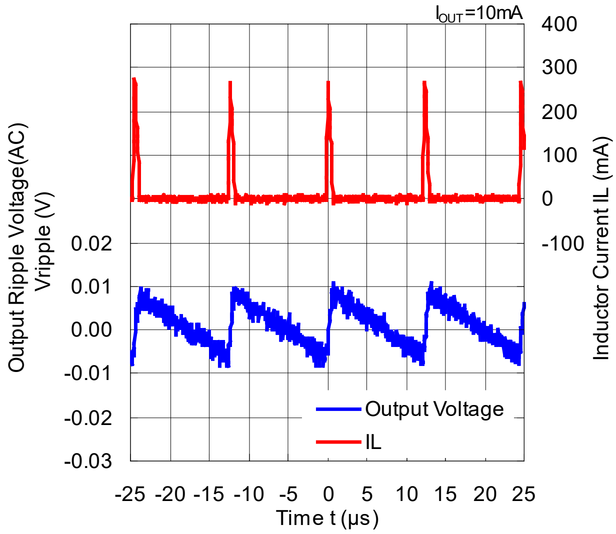


RP550K001A V_{OUT}=0.6V(V_{IN}=3.6V)

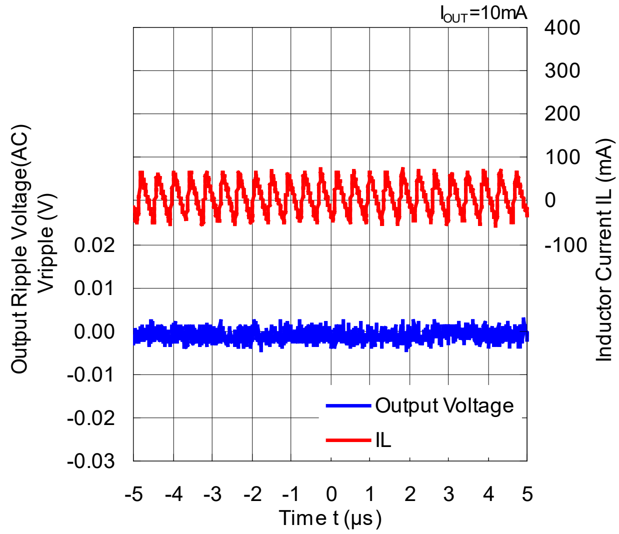
MODE="H" Forced PWM Control



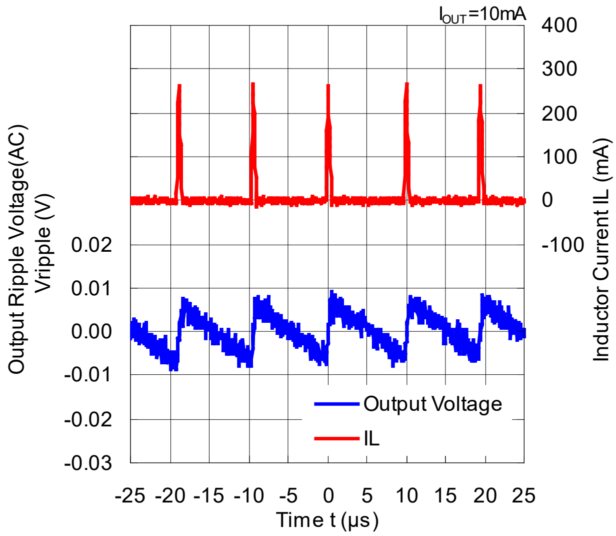
RP550K001A V_{OUT}=0.8V(V_{IN}=3.6V)
MODE="L"PWM/VFM Auto Switching Control



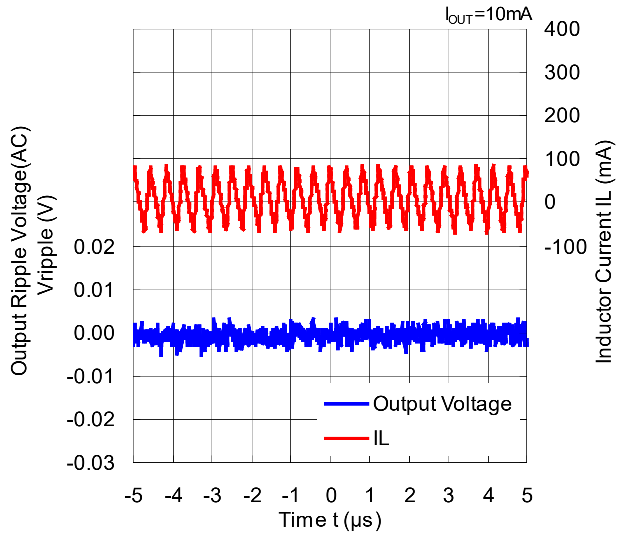
RP550K001A V_{OUT}=0.8V(V_{IN}=3.6V)
MODE="H" Forced PWM Control



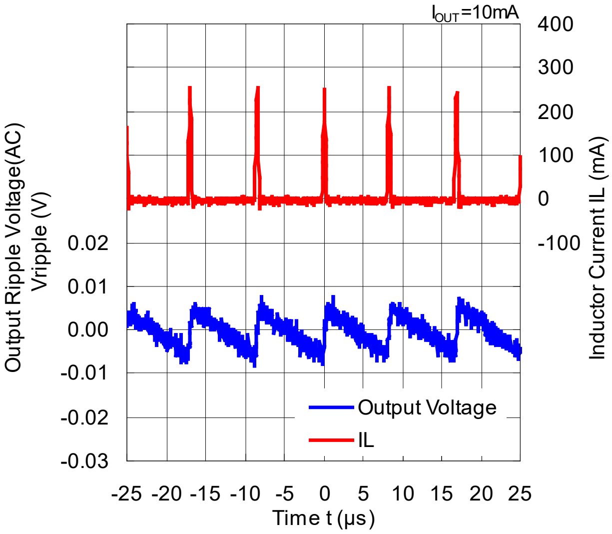
RP550K001A V_{OUT}=1.2V(V_{IN}=3.6V)
MODE="L"PWM/VFM Auto Switching Control



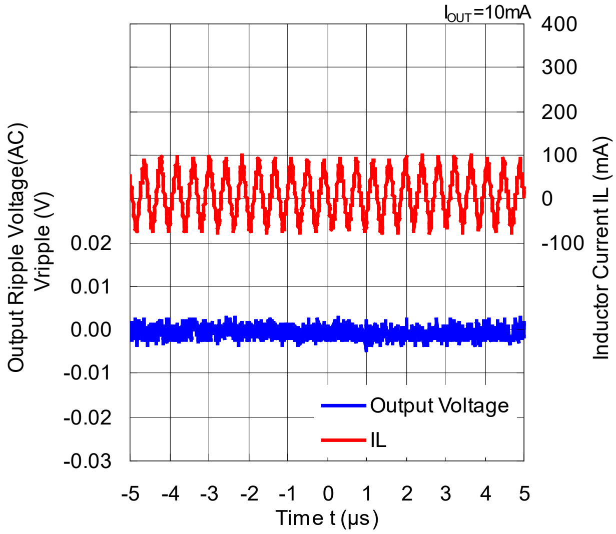
RP550K001A V_{OUT}=1.2V(V_{IN}=3.6V)
MODE="H" Forced PWM Control



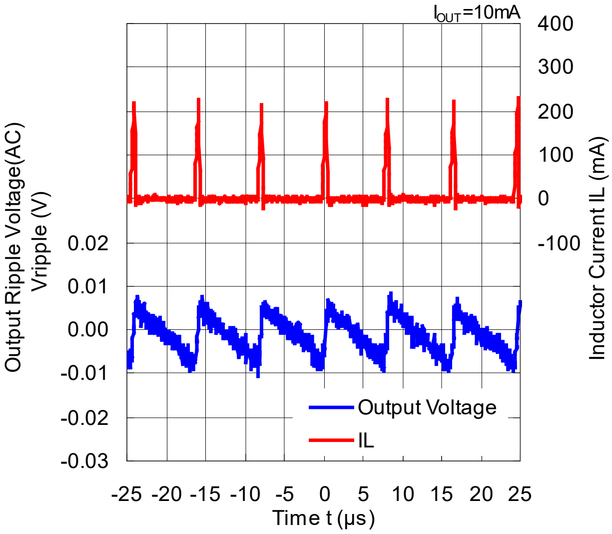
RP550K001A V_{OUT}=1.8V(V_{IN}=3.6V)
MODE="L"PWM/VFM Auto Switching Control



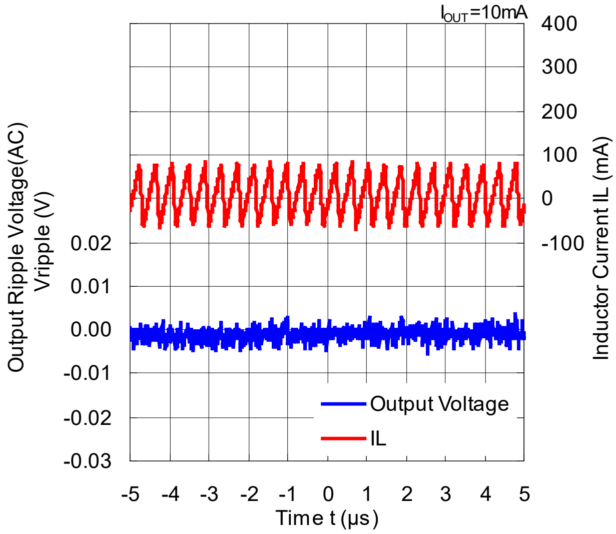
RP550K001A V_{OUT}=1.8V(V_{IN}=3.6V)
MODE="H" Forced PWM Control



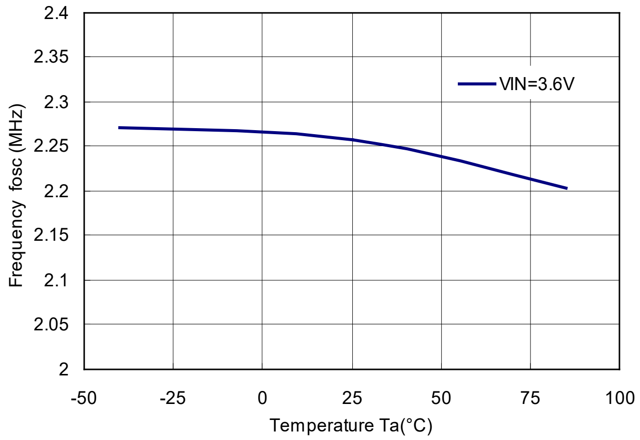
RP550K001A V_{OUT}=3.3V(V_{IN}=4.3V)
MODE="L"PWM/VFM Auto Switching Control



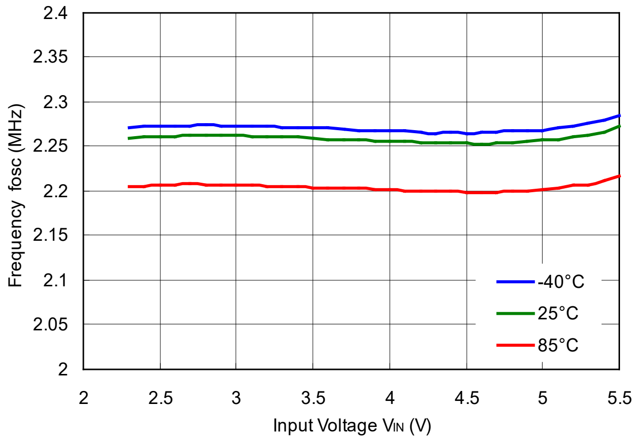
RP550K001A V_{OUT}=3.3V(V_{IN}=4.3V)
MODE="H" Forced PWM Control



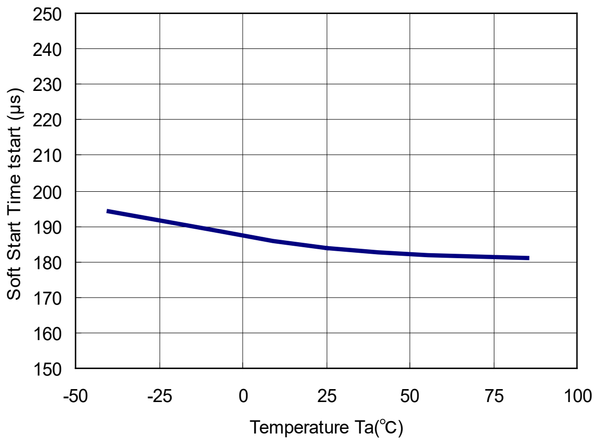
8) Oscillator Frequency vs. Ambient Temperature



9) Oscillator Frequency vs. Input Voltage

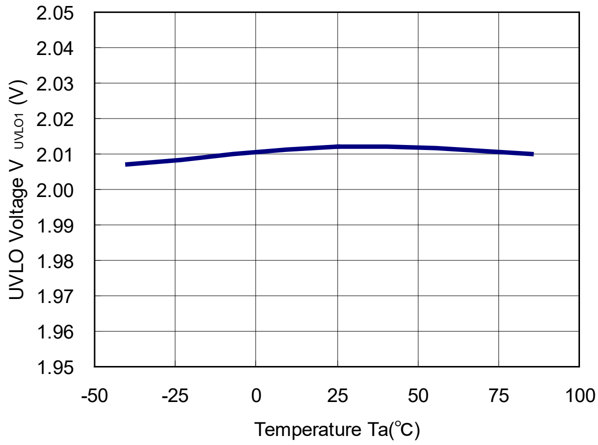


10) Soft-start Time vs. Ambient Temperature

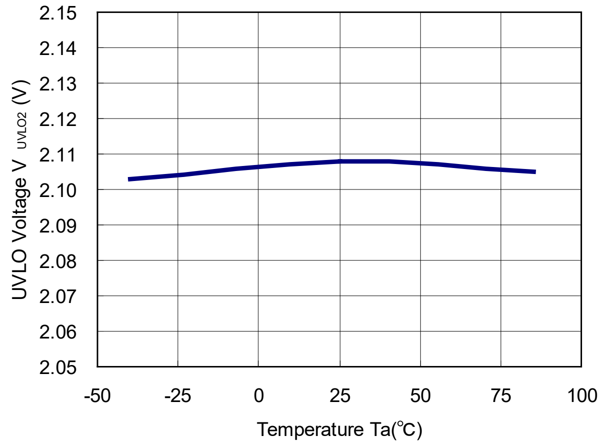


11) UVLO Detector/ Released Threshold vs. Ambient Temperature

UVLO Detector Threshold

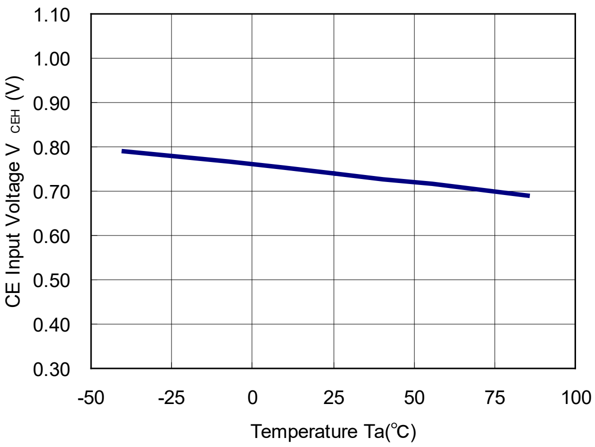


UVLO Released Threshold

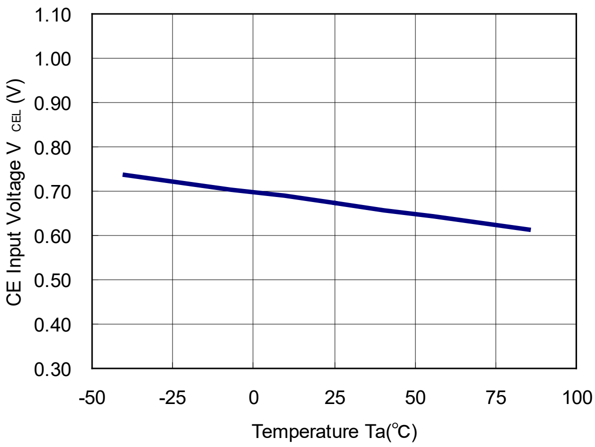


12) CE Input Voltage vs. Ambient Temperature

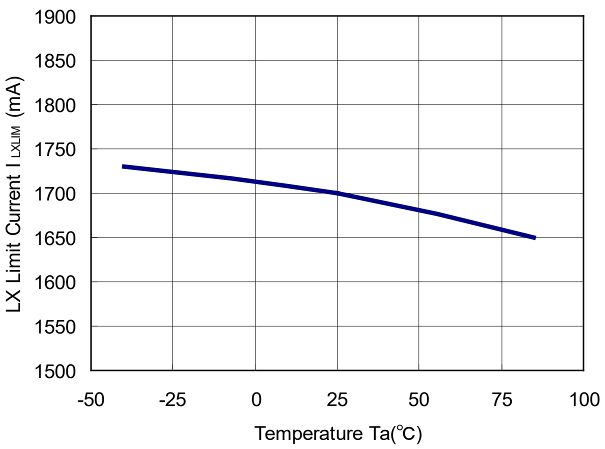
CE "H" Input Voltage (V_{IN}=5.5V)



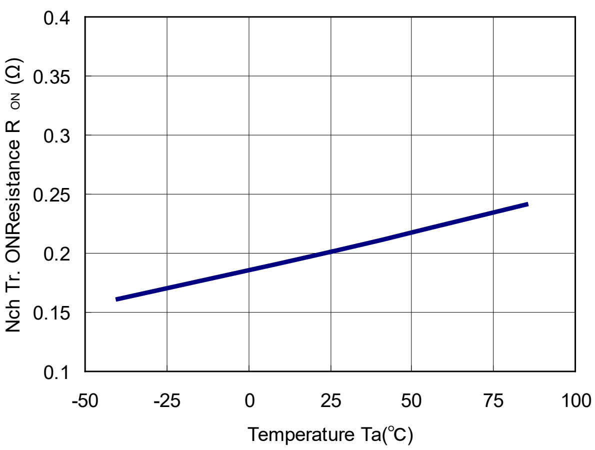
CE "L" Input Voltage (V_{IN}=2.3V)



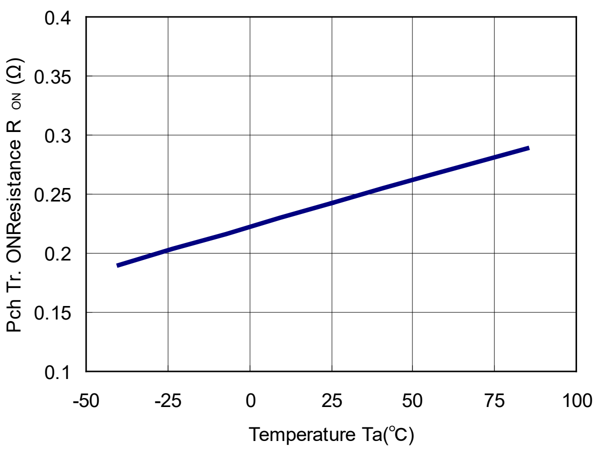
13) Lx Limit Current vs. Ambient Temperature



14) Nch Transistor ON Resistance vs. Ambient Temperature

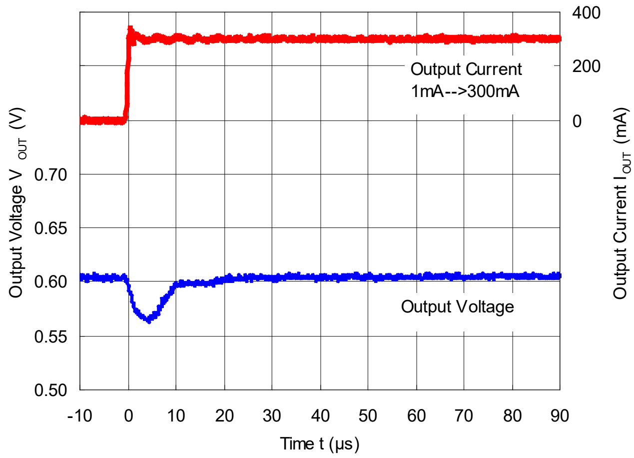


15) Pch Transistor ON Resistance vs. Ambient Temperature

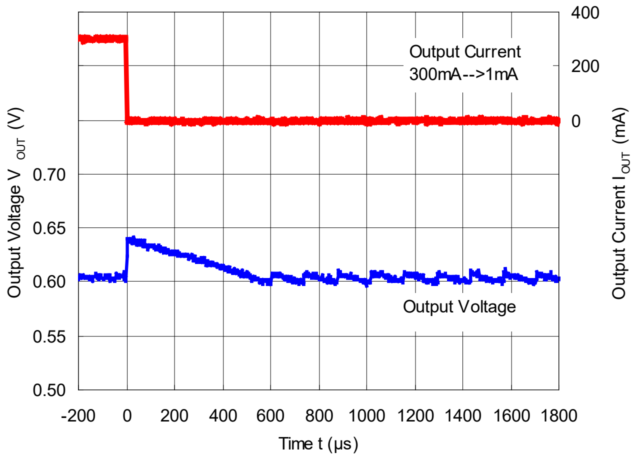


16) Load Transient Response

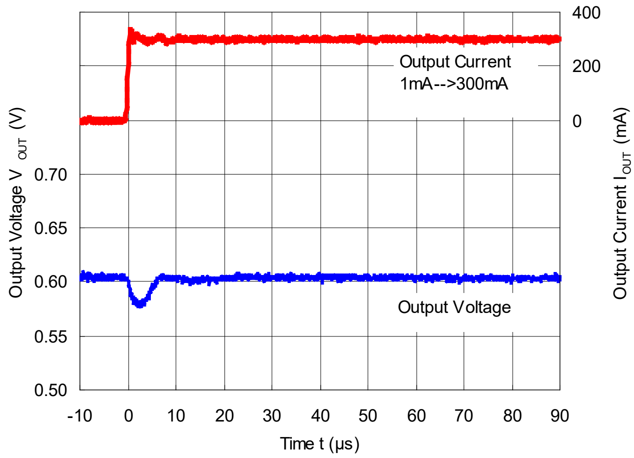
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.6V)
MODE="L"PWM/VFM Auto Switching Control



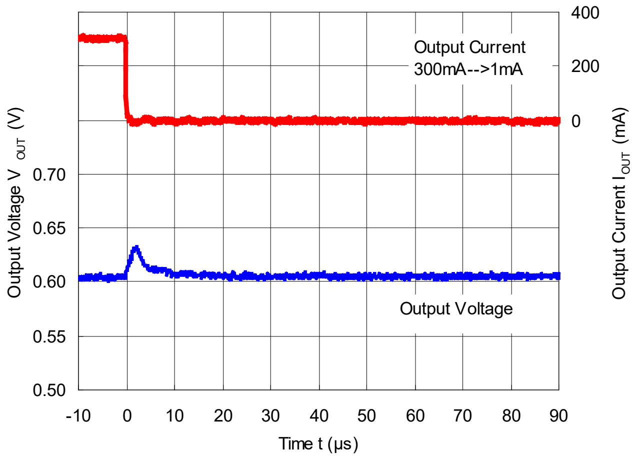
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.6V)
MODE="L"PWM/VFM Auto Switching Control



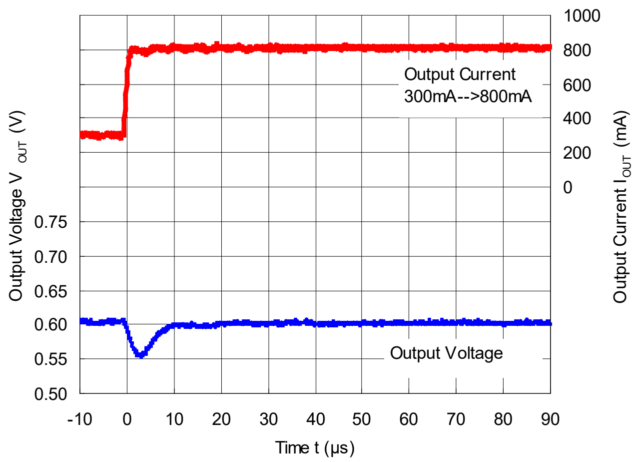
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.6V)
MODE="H" Forced PWM Control



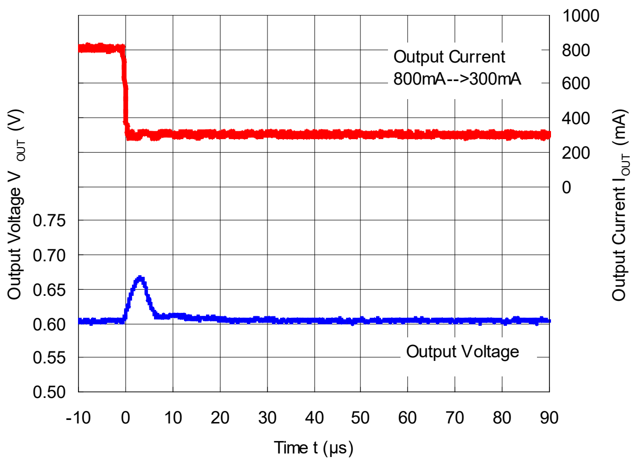
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.6V)
MODE="H" Forced PWM Control



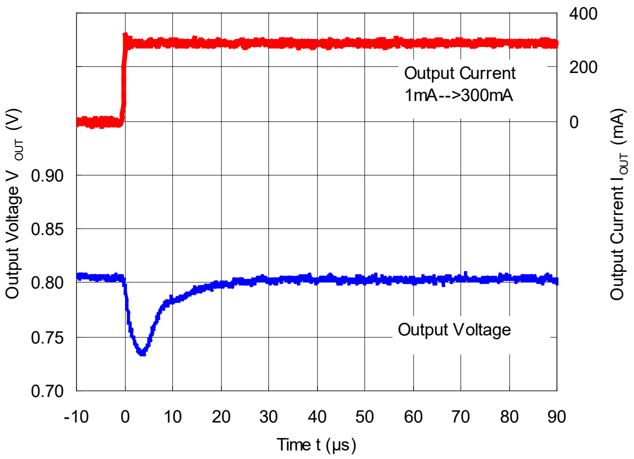
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.6V)



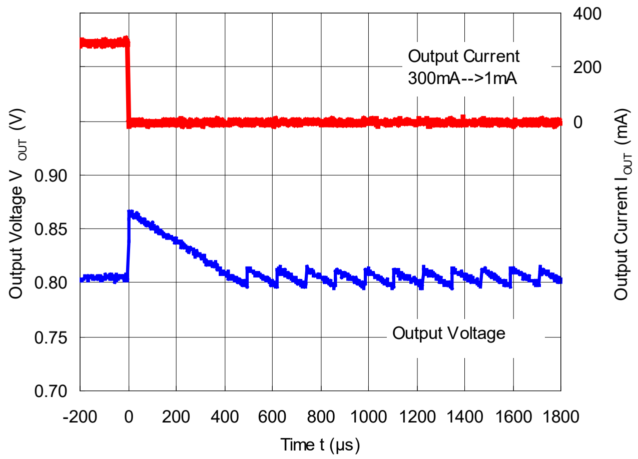
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.6V)



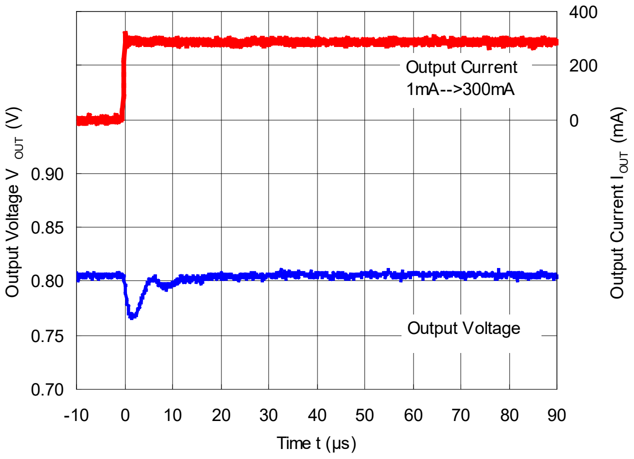
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.8V)
MODE="L"PWM/VFM Auto Switching Control



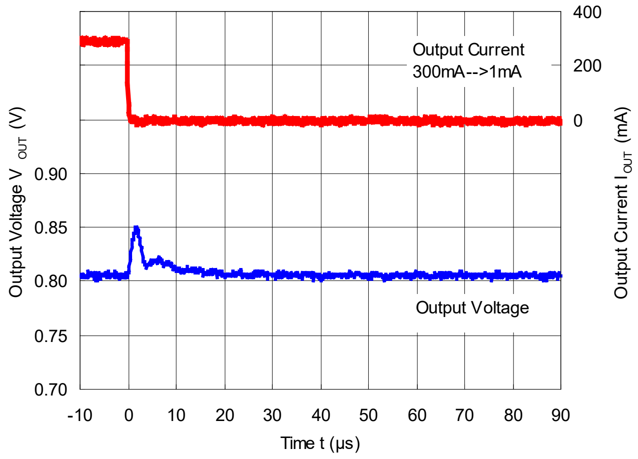
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.8V)
MODE="L"PWM/VFM Auto Switching Control



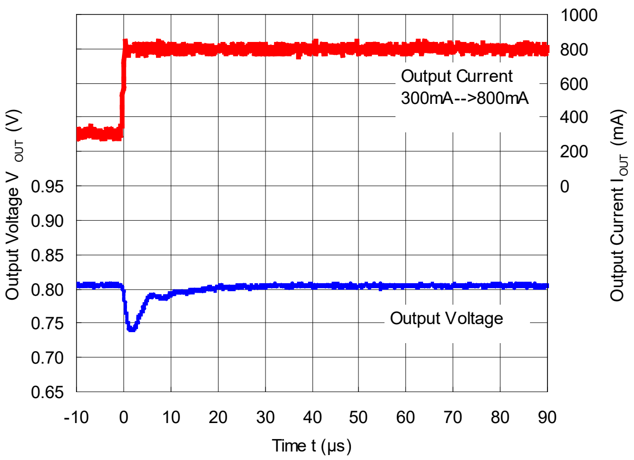
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.8V)
MODE="H" Forced PWM Control



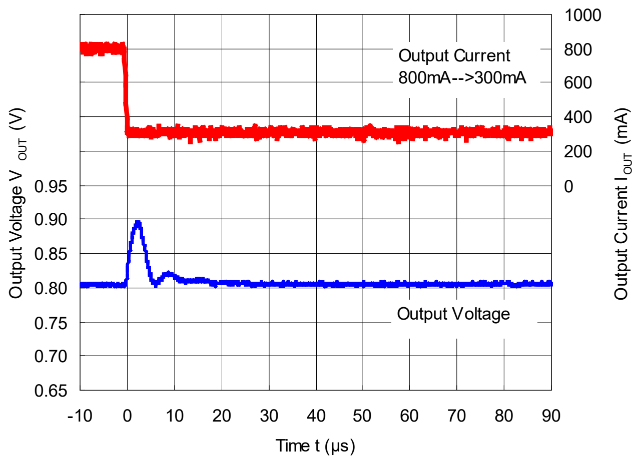
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.8V)
MODE="H" Forced PWM Control



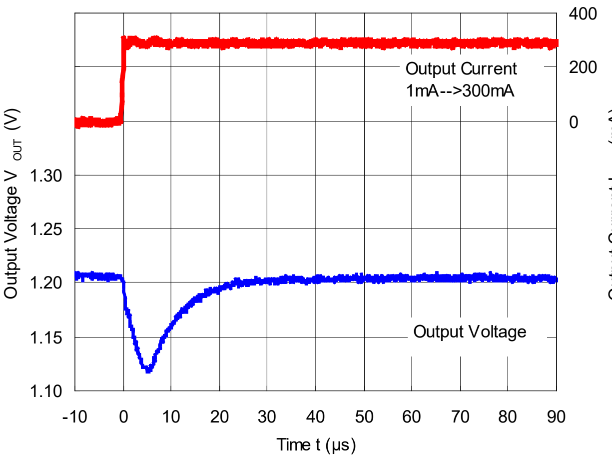
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.8V)



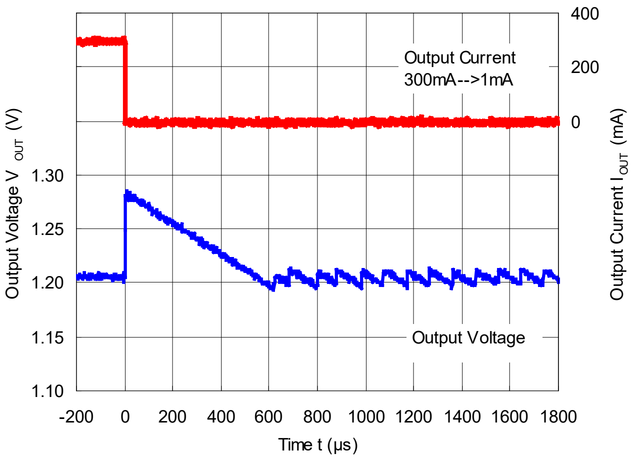
RP550K001A (V_{IN}=3.6V, V_{OUT}=0.8V)



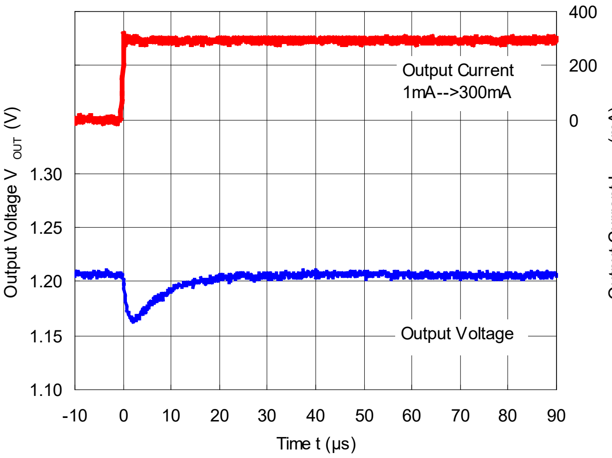
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.2V)
MODE="L"PWM/VFM Auto Switching Control



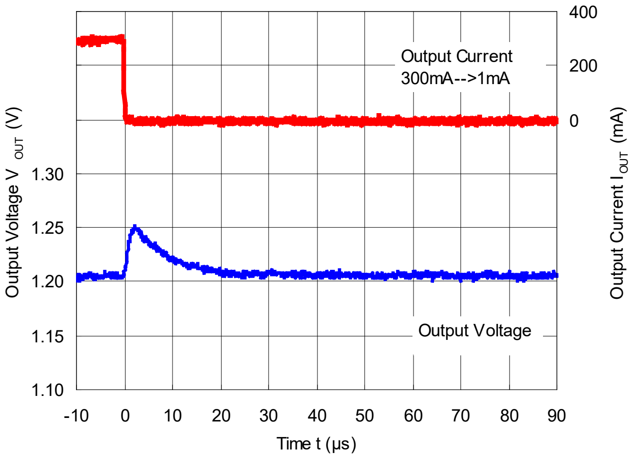
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.2V)
MODE="L"PWM/VFM Auto Switching Control



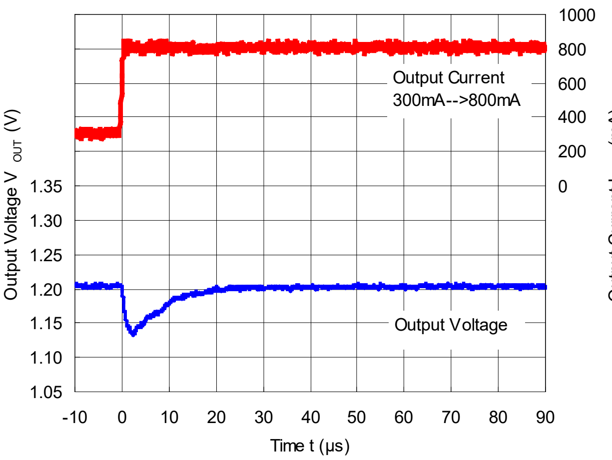
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.2V)
MODE="H" Forced PWM Control



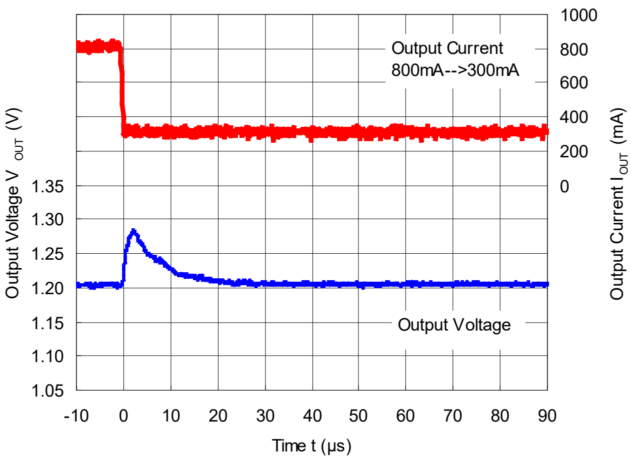
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.2V)
MODE="H" Forced PWM Control



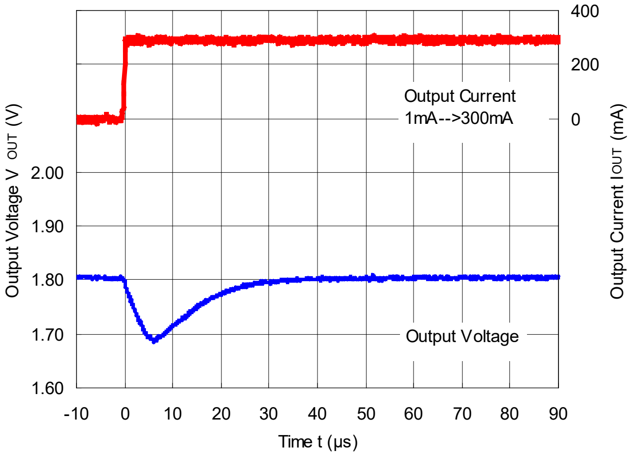
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.2V)



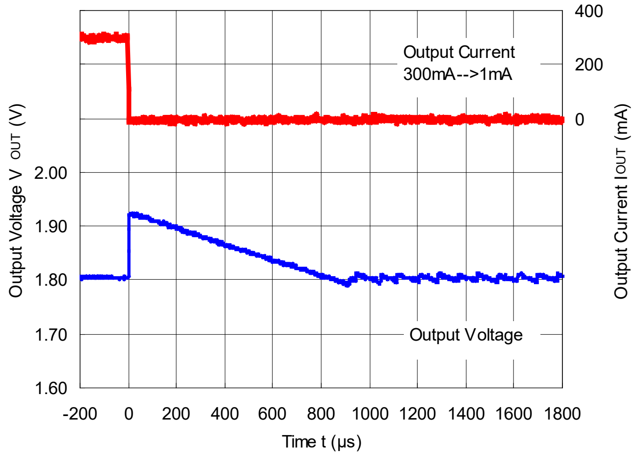
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.2V)



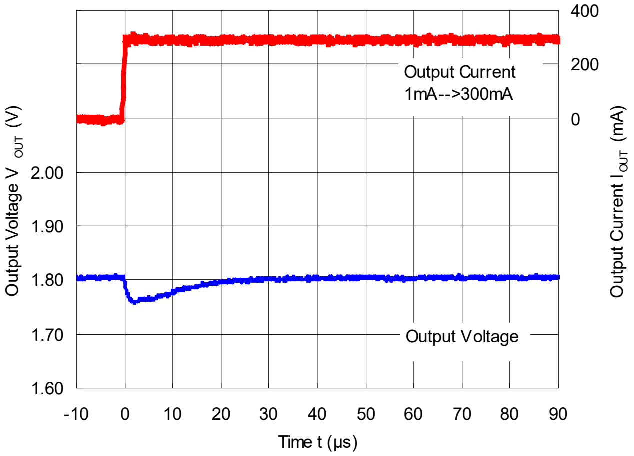
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.8V)
MODE="L"PWM/VFM Auto Switching Control



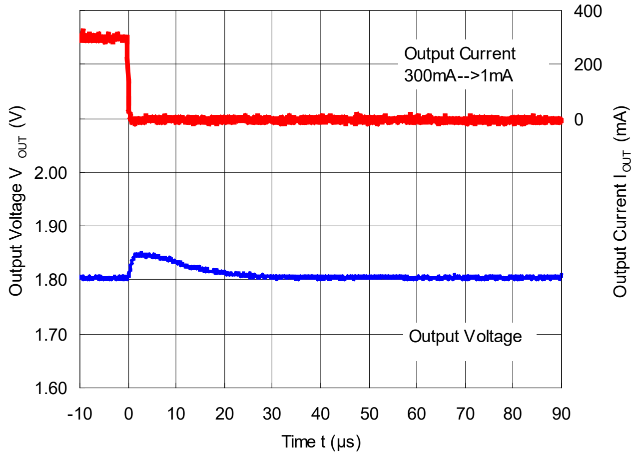
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.8V)
MODE="L"PWM/VFM Auto Switching Control



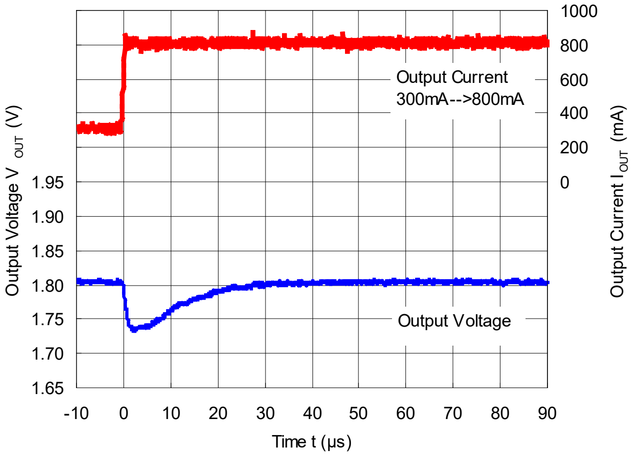
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.8V)
MODE="H" Forced PWM Control



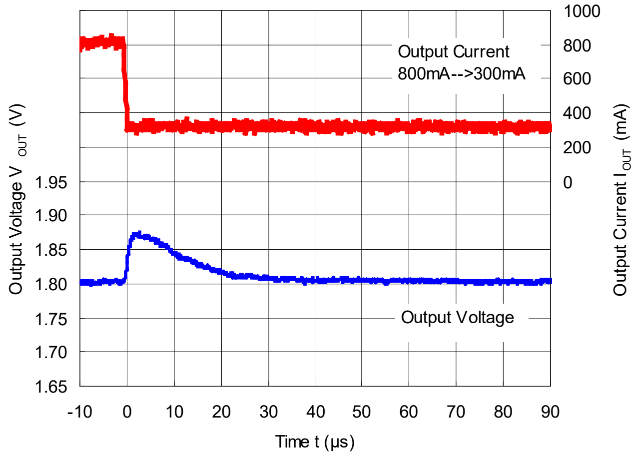
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.8V)
MODE="H" Forced PWM Control



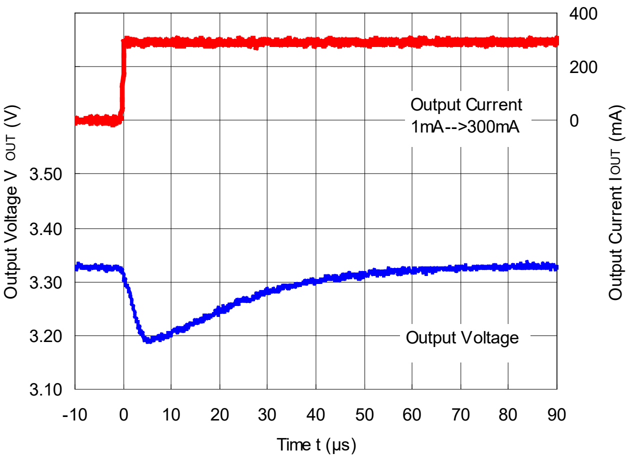
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.8V)



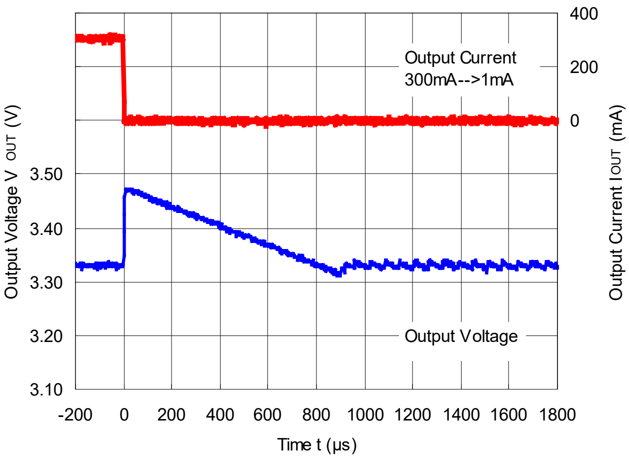
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.8V)



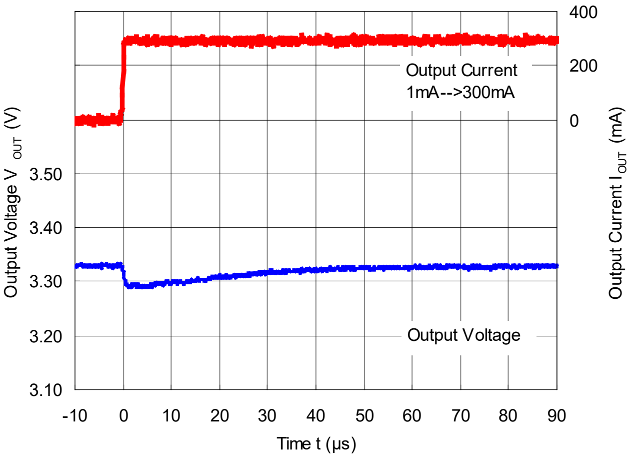
RP550K001A (V_{IN}=5.0V, V_{OUT}=3.3V)
MODE="L"PWM/VFM Auto Switching Control



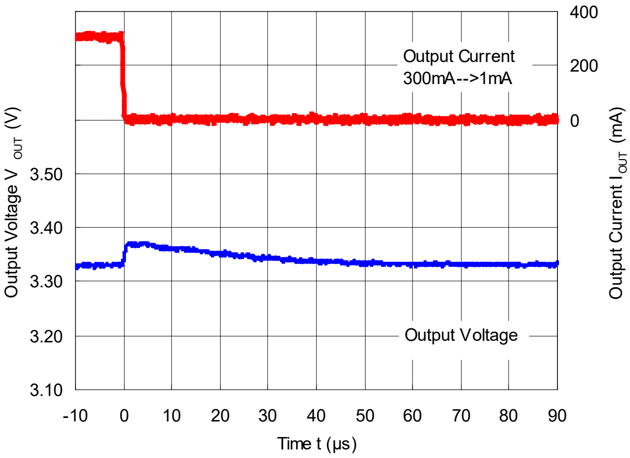
RP550K001A (V_{IN}=5.0V, V_{OUT}=3.3V)
MODE="L"PWM/VFM Auto Switching Control



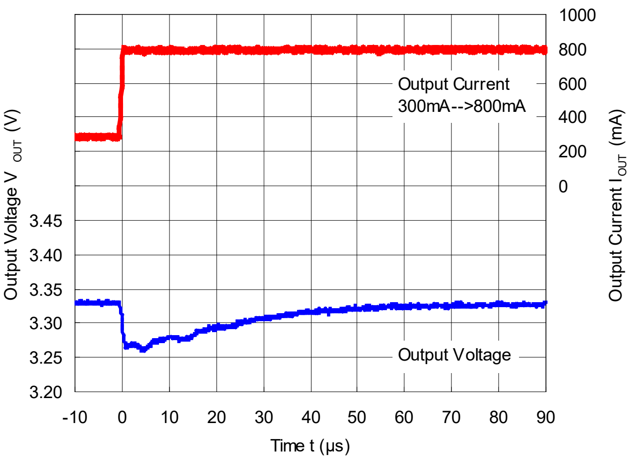
RP550K001A (V_{IN}=5.0V, V_{OUT}=3.3V)
MODE="H" Forced PWM Control



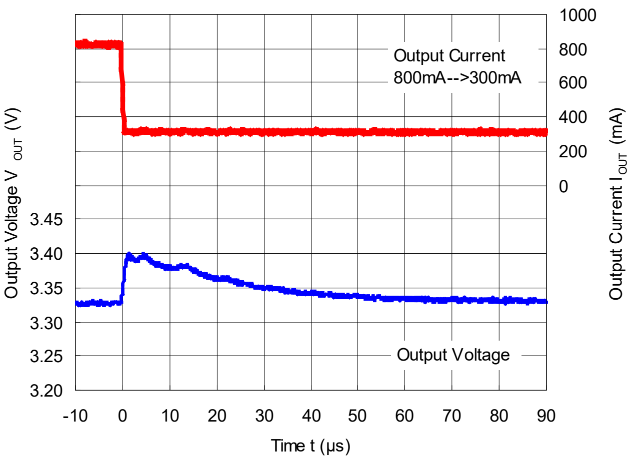
RP550K001A (V_{IN}=5.0V, V_{OUT}=3.3V)
MODE="H" Forced PWM Control



RP550K001A (V_{IN}=5.0V, V_{OUT}=3.3V)

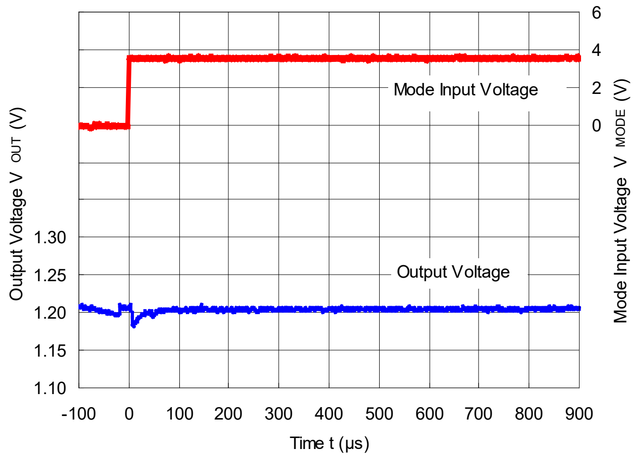


RP550K001A (V_{IN}=5.0V, V_{OUT}=3.3V)

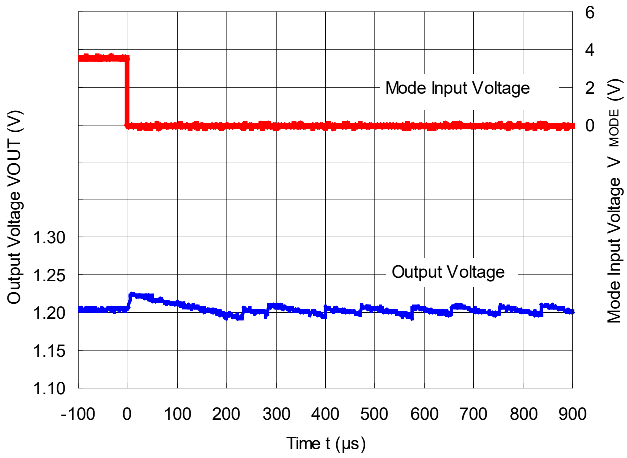


17) Mode Switching Waveform

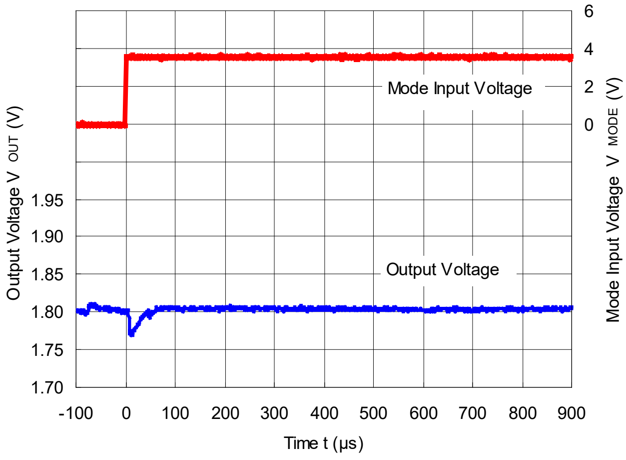
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.2V, I_{OUT}=1mA)
MODE="L" --> MODE="H"



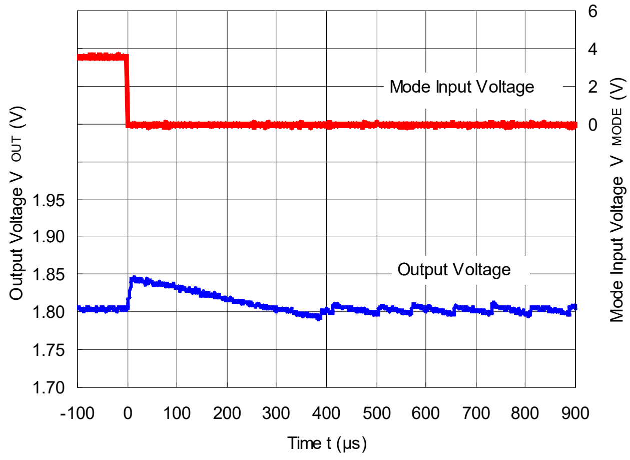
RP550K001A (V_{IN}=3.6V, V_{OUT}=1.2V, I_{OUT}=1mA)
MODE="H" --> MODE="L"



RP550K001A (V_{IN}=3.6V, V_{OUT}=1.8V, I_{OUT}=1mA)
MODE="L" --> MODE="H"



RP550K001A (V_{IN}=3.6V, V_{OUT}=1.8V, I_{OUT}=1mA)
MODE="H" --> MODE="L"



PACKAGE INFORMATION

Power Dissipation (DFN(PLP)2730-12)

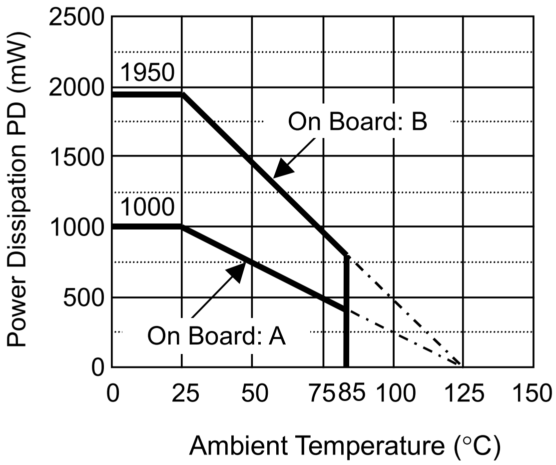
Power Dissipation (P_D) depends on conditions of mounting on board. This specification is based on the Measurement Conditions below.

Measurement Conditions

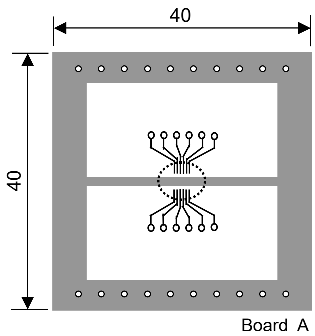
	Standard Land Pattern	High Wattage Land Pattern
Environment	Mounting on Board (Wind Velocity=0m/s)	Mounting on Board (Wind Velocity=0m/s)
Board Material	Glass Cloth Epoxy Plastic (Double-sided)	Glass Cloth Epoxy Plastic (Four-layers)
Board Dimensions	40mm x 40mm x 1.6mm	76.2mm x 114.3mm x 1.6mm
Copper Ratio	Top-layer: Approx. 50%, Back-layer: Approx. 50%	Top and Back-layers: Approx. 10%, 2nd, 3rd layers: Approx. 100%
Through-holes	ϕ 0.54mm x 32pcs	ϕ 0.85mm x 64pcs Notes: Pad (Heat spreader), top layer, 2nd layer and 3rd layer are connected by 0.3mm holes drilled in PCBs.

Measurement Conditions (Ta=25°C, Tjmax=125°C)

	Standard Land Pattern	High Wattage Land Pattern
Power Dissipation	1000mW	1950mW
Thermal Resistance	θja = (125-25°C)/1.0W = 100°C/W	θja = (125-25°C)/1.95W = 51.2°C/W
	θjc = 18°C/W	θjc = 5.9°C/W

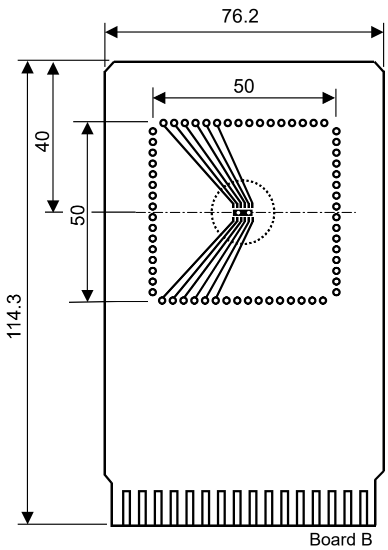


Power Dissipation



Measurement Board Pattern

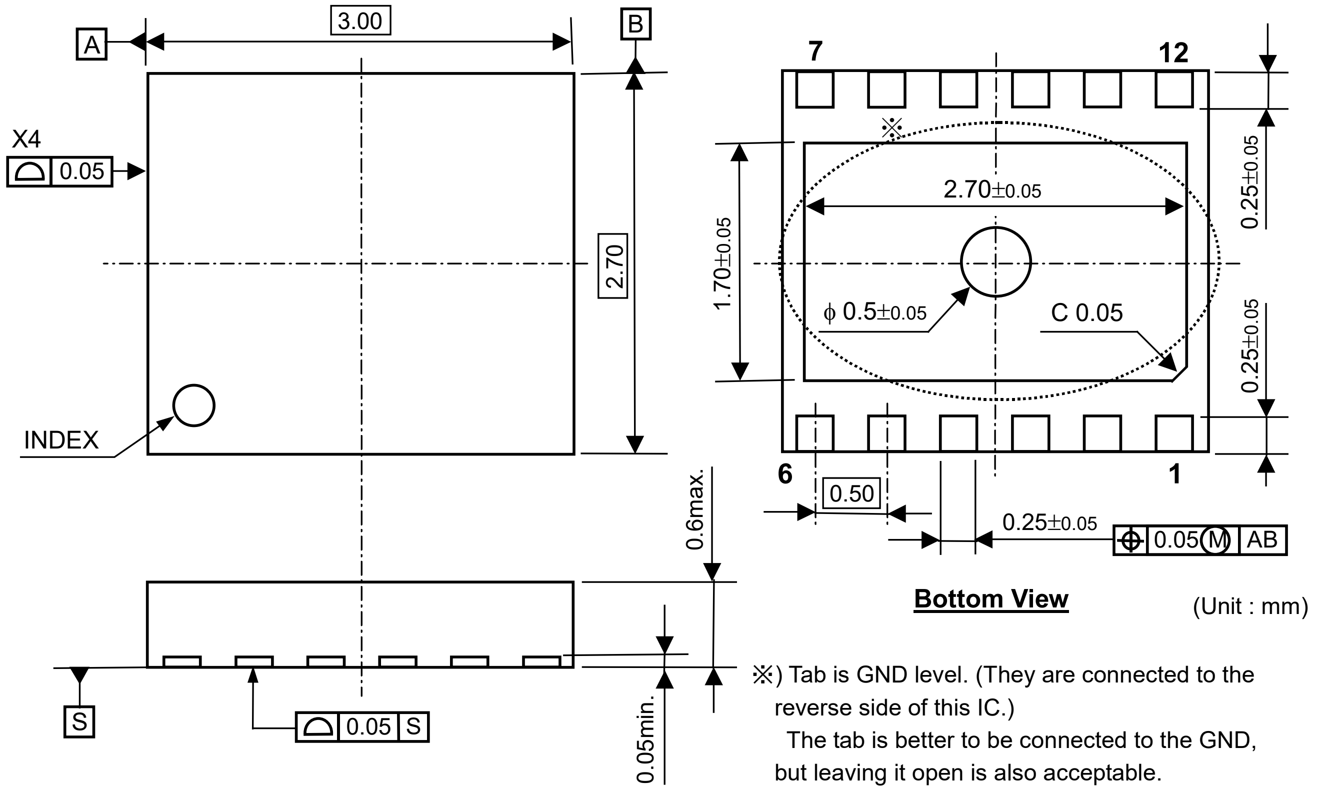
IC Mount Area (Unit : mm)



Measurement Board Pattern

IC Mount Area (Unit : mm)

Package Dimensions (DFN(PLP)2730−12)

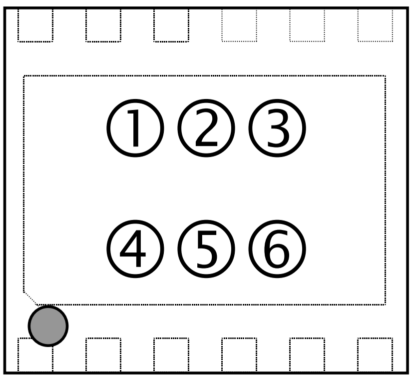


※) Tab is GND level. (They are connected to the reverse side of this IC.)
The tab is better to be connected to the GND, but leaving it open is also acceptable.

Mark Specification (DFN(PLP)2730−12)

①②③④: Product Code … **DT01**

⑤⑥: Lot Number … Alphanumeric Serial Number



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