

### NCE P-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE60P14AK uses advanced trench technology and design to provide excellent  $R_{\text{DS}(\text{ON})}$  with low gate charge .This device is well suited for use as a load switch or in PWM applications.

#### **General Features**

V<sub>DS</sub> =-60V,I<sub>D</sub> =-14A

 $R_{DS(ON)}$  <75m $\Omega$  @  $V_{GS}$ =-10V

 $R_{DS(ON)}$  <96m $\Omega$  @  $V_{GS}$ =-4.5V

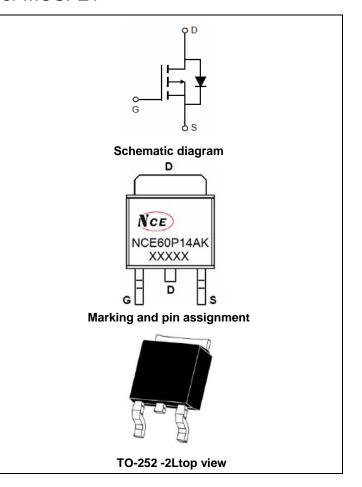
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

#### **Application**

- Load switch
- PWM application

100% UIS TESTED!

100% ΔVds TESTED!



### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE60P14AK	NCE60P14AK	TO-252-2L	-	-	-

### Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-60	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous	I <sub>D</sub>	-14	А
Pulsed Drain Current	I <sub>DM</sub>	-56	Α
Maximum Power Dissipation	P <sub>D</sub>	50	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 175	$^{\circ}$

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{ heta JC}$	3.0	°C/W
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# Electrical Characteristics (T<sub>C</sub>=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA -60		-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V,V <sub>GS</sub> =0V	-	-	-1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-1.0	1.5	-2.0	V
Drain-Source On-State Resistance	5	$V_{GS}$ =-10V, $I_D$ =-14A	-	63	75	mΩ
Diam-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	-	74	96	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-5V,I <sub>D</sub> =-14A	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ 20\/\/ 0\/	-	930	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-30V, $V_{GS}$ =0V, F=1.0MHz	-	85	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	r=1.0lvln2	-	35	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-30V, $R_L$ =2 $\Omega$ ,	-	4	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10 $V$ , $R_{G}$ =3 $\Omega$	-	32	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	7	-	nS
Total Gate Charge	Qg	V 00 L 44A	-	25	-	nC
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =-30,I <sub>D</sub> =-14A,	-	3	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =-10V	-	7	-	nC
Drain-Source Diode Characteristics	1					I.
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-14A	-		-1.2	V
Diode Forward Current (Note 2)	Is		-	-	-14	Α
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> =- 14A	-	25		nS
Reverse Recovery Charge	Qrr	$di/dt = -100A/\mu s^{(Note3)}$	-	31		nC

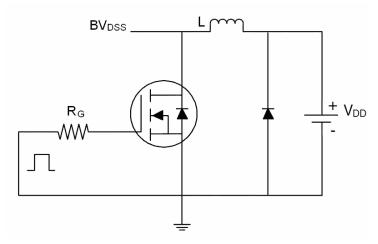
### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production

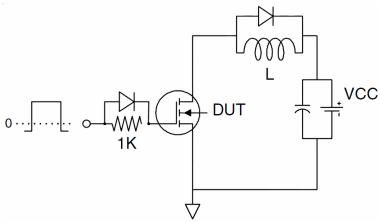


## **Test Circuit**

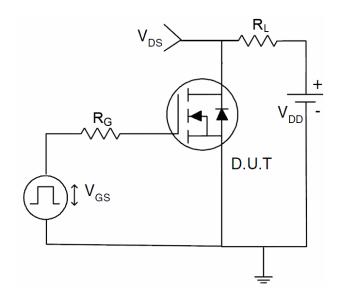
# 1) E<sub>AS</sub> test Circuit



# 2) Gate charge test Circuit



## 3) Switch Time Test Circuit





### Typical Electrical and Thermal Characteristics (Curves)

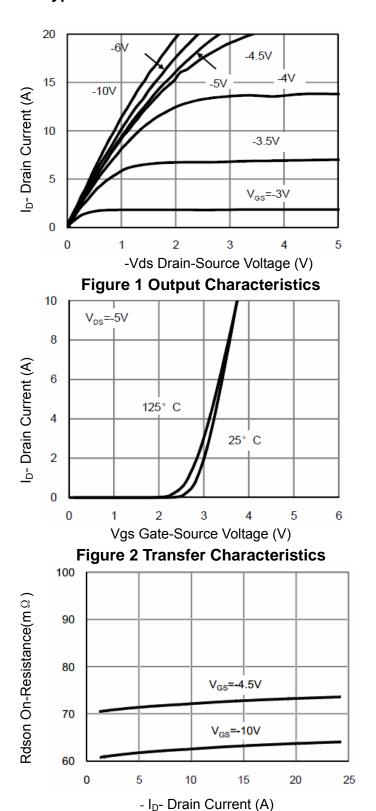
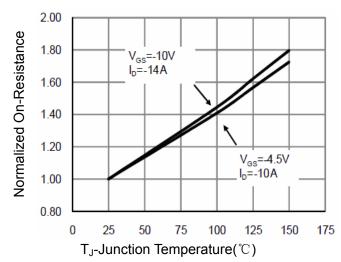
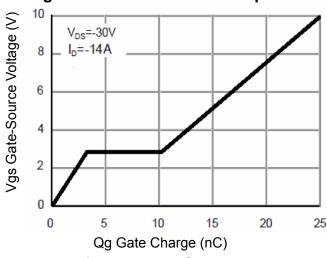


Figure 3 Rdson- Drain Current



**Figure 4 Rdson-Junction Temperature** 



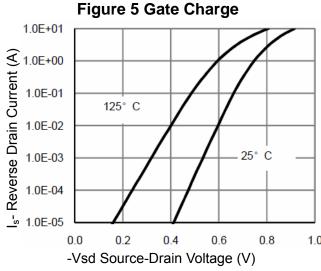
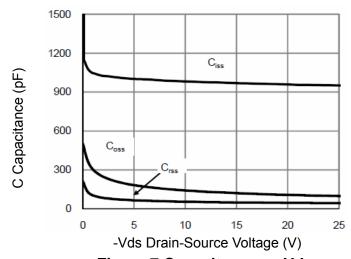
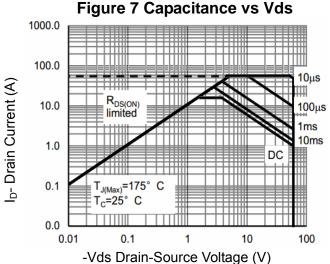


Figure 6 Source- Drain Diode Forward







**Figure 8 Safe Operation Area** 

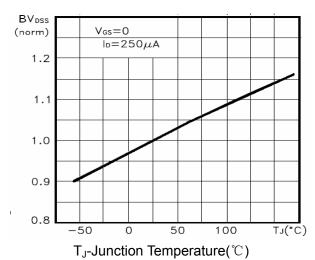


Figure 9 BV<sub>DSS</sub> vs Junction Temperature

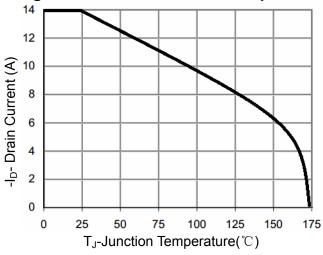


Figure 10 ID Current De-rating

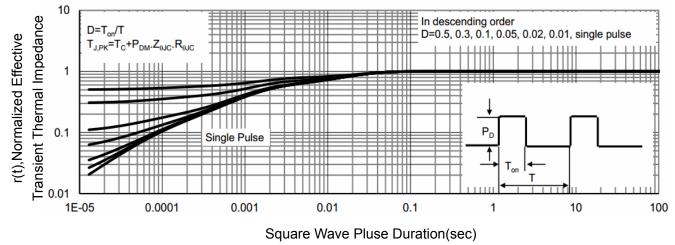
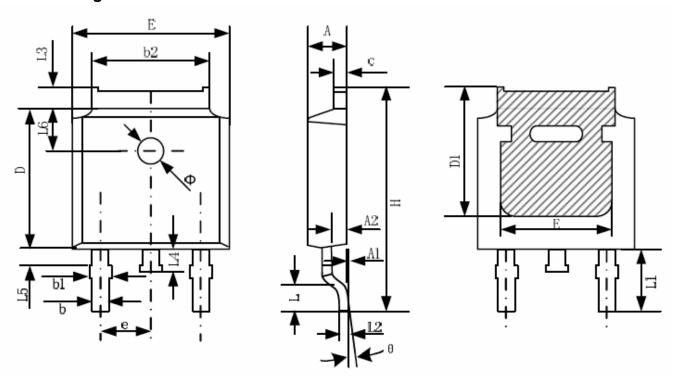


Figure 11 Normalized Maximum Transient Thermal Impedance



# **TO-252-2L Package Information**



Comple at	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	2.20	2.38	0.087	0.094	
A1	0.00	0.10	0.000	0.004	
A2	0.90	1.10	0.035	0.043	
b	0.72	0.85	0.028	0.033	
b1	0.72	0.90	0.028	0.035	
b2	5.13	5.46	0.202	0.215	
С	0.47	0.60	0.019	0.024	
D	6.00	6.20	0.236	0.244	
D1	5.25		0.207		
Е	6.50	6.70	0.256	0.264	
E1	4.70		0.185		
e	2.19	2.39	0.086	0.094	
Н	9.80	10.40	0.386	0.409	
L	1.40	1.70	0.055	0.067	
L1	2.90 REF		0.114 REF		
L2	0.508	BSC	0.020 BSC		
L3	0.90	1.25	0.035	0.049	
L4	0.60	1.00	0.024	0.039	
L5	0.15	0.75	0.006	0.030	
L6	1.80 REF		0.071 REF		
Φ	1.20	1.40	0.047	0.055	
θ	0°	8°	0°	8°	

### http://www.ncepower.com

# NCE60P14AK

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