

Features

- Split Gate Trench MOSFET Technology
- High Density Cell Design For Low $R_{DS(on)}$
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

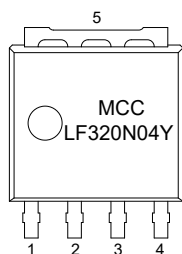
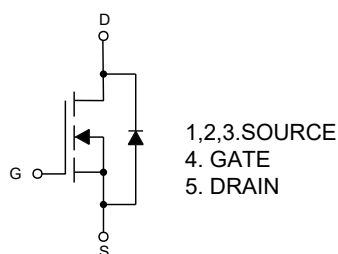
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 50°C/W Junction to Ambient (Note 2)
- Thermal Resistance: 0.65°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	
Pulsed Drain Current (Note 3)	I_{DM}	1280	A
Total Power Dissipation (Note 4)	P_D	230	W
Avalanche Energy (Note 5)	E_{AS}	441	mJ

Note:

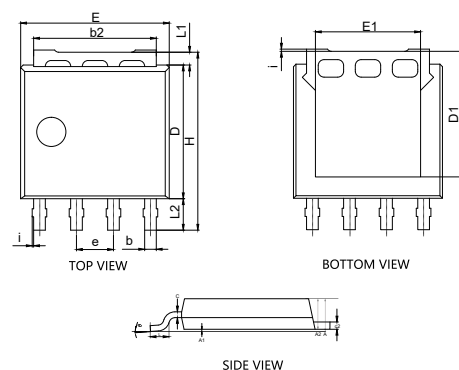
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz. copper, in a still air environment with $T_A=25^\circ\text{C}$.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using junction-case thermal resistance.
5. $T_j=25^\circ\text{C}$, $V_{DD}=30\text{V}$, $R_G=25\Omega$, $V_{GS}=10\text{V}$, $L=0.5\text{mH}$.

Internal Structure and Marking Code



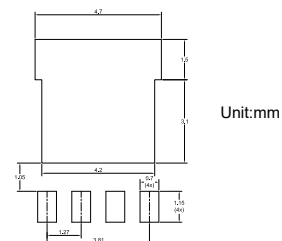
N-CHANNEL MOSFET

LFPAK56E



DIM	INCHES		Millimeter		NOTE
	MIN	MAX	MIN	MAX	
A	0.039	0.051	1.00	1.30	
A1	0.000	0.006	0.00	0.15	
A2	0.039	0.044	0.98	1.12	
b	0.014	0.020	0.35	0.50	
b2	0.158	0.174	4.02	4.41	
c	0.007	0.010	0.19	0.25	
c2	0.009	0.012	0.24	0.30	
D	0.175	0.185	4.45	4.70	
D1		0.175		4.45	
E	0.195	0.209	4.95	5.30	
E1	0.138	0.146	3.50	3.70	
e	0.050		1.27		TYP
H	0.234	0.246	5.95	6.25	
i		0.010		0.25	
L	0.016	0.033	0.40	0.85	
L1	0.011	0.022	0.27	0.57	
L2	0.031	0.051	0.80	1.30	
θ	0°	8°	0°	8°	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =1mA	40			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =32V, V _{GS} =0V			1	μA
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2	2.9	4	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =75A		0.48	0.62	mΩ
		V _{GS} =6V, I _D =20A		1.3	1.9	
Gate Resistance	R _g	f=1MHz, Open drain		3.0		Ω
Diode Characteristics						
Continuous Body Diode Current	I _S				320	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A			1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F =40A,di/dt=100A/us		83		ns
Body Diode Reverse Recovery charge	Q _{rr}			125		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =20V,V _{GS} =0V,f=1MHz		6485		pF
Output Capacitance	C _{oss}			3365		
Reverse Transfer Capacitance	C _{rss}			129		
Total Gate Charge	Q _g	V _{DS} =20V,V _{GS} =10V,I _D =75A		80		nC
Gate-Source Charge	Q _{gs}			27.6		
Gate-Drain Charge	Q _{gd}			18.6		
Turn-On Delay Time	t _{d(on)}	V _{GS} =10V, V _{DS} =20V, R _G =3Ω, I _D =160A		17.5		ns
Turn-On Rise Time	t _r			32		
Turn-Off Delay Time	t _{d(off)}			55		
Turn-Off Fall Time	t _f			54		

Curve Characteristics

Fig.1 - Typical Output Characteristics

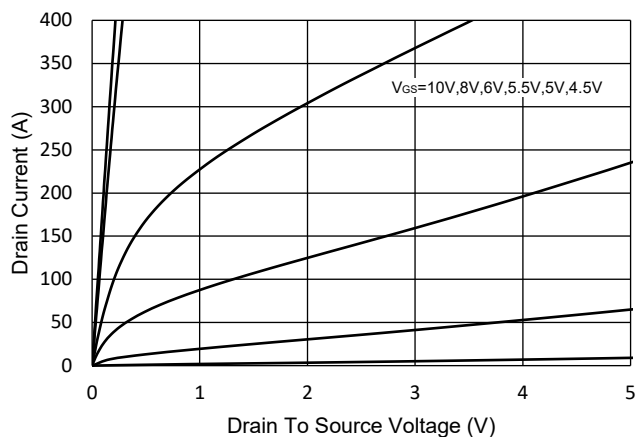


Fig.2 - Transfer Characteristic

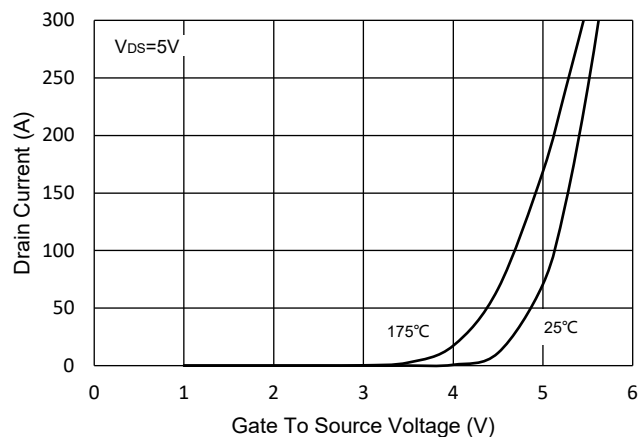


Fig.3 - $R_{DS(ON)}$ - V_{GS}

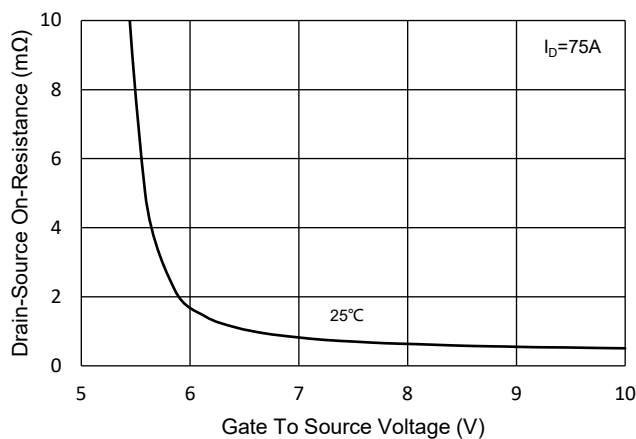


Fig.4 - $R_{DS(ON)}$ - I_D

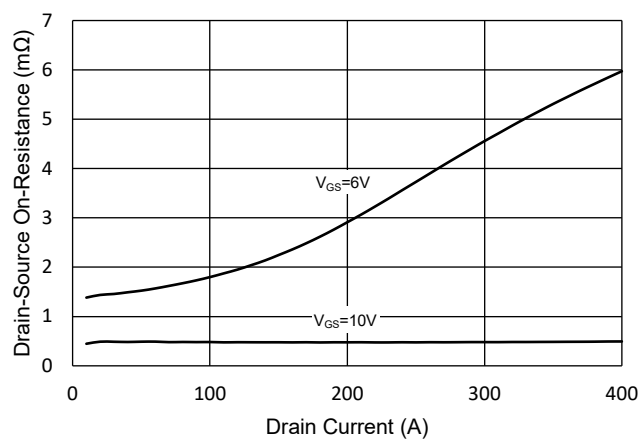


Fig.5 - Capacitance Characteristics

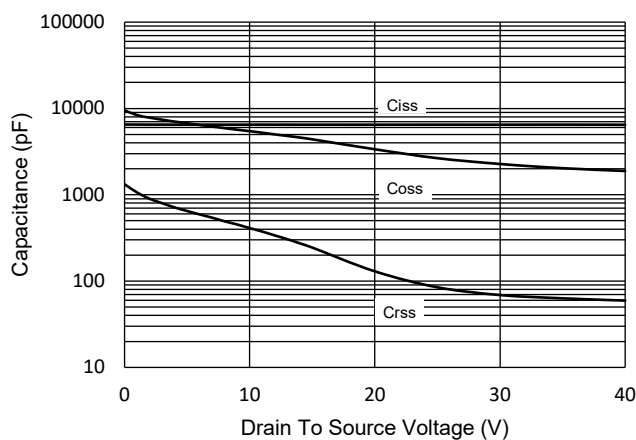
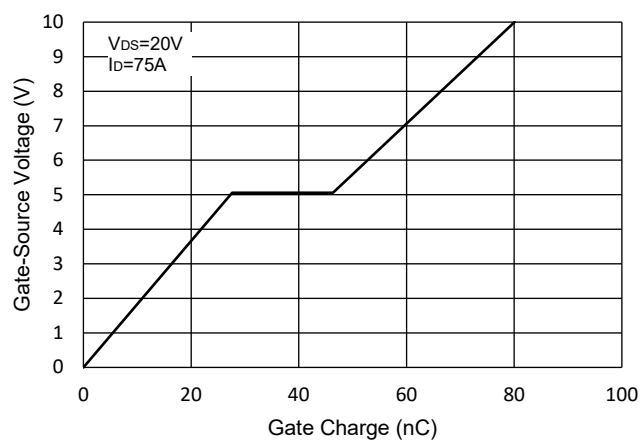


Fig.6 - Gate Charge



Curve Characteristics

Fig.7 - Normalized Threshold Voltage

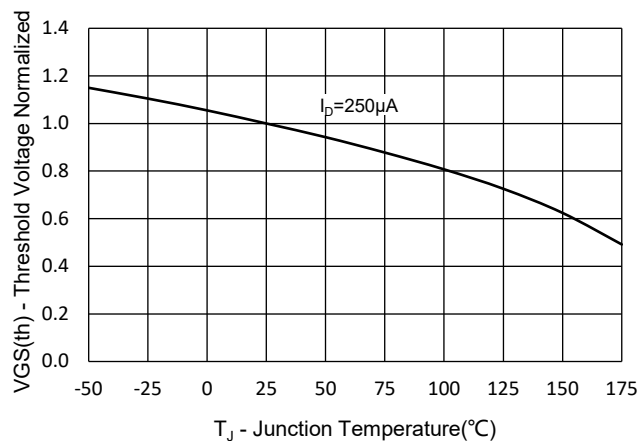


Fig.8 - Normalized On Resistance Characteristics

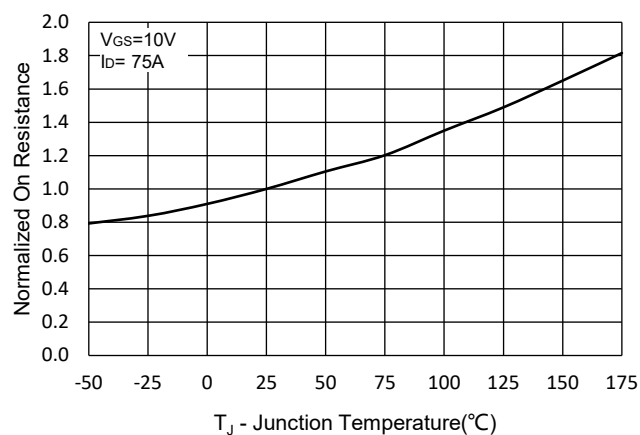


Fig.9 - $I_S - V_{SD}$

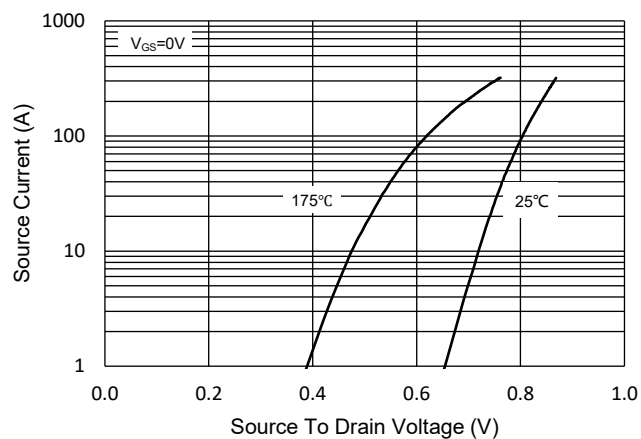


Fig.10 - Drain Current

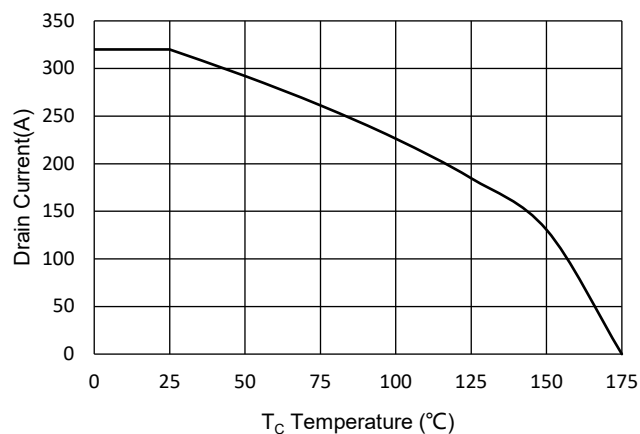
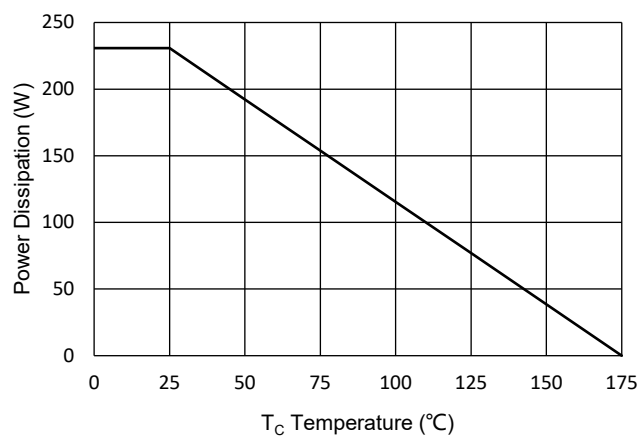


Fig.11 - PD Dissipation



Curve Characteristics

Fig.12 - Safe Operation Area

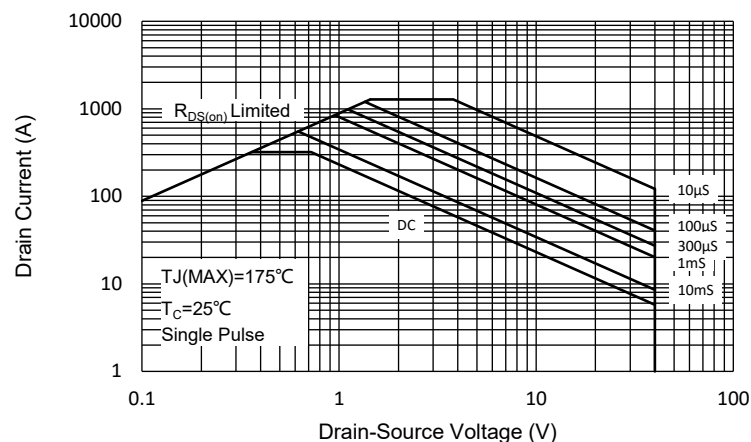
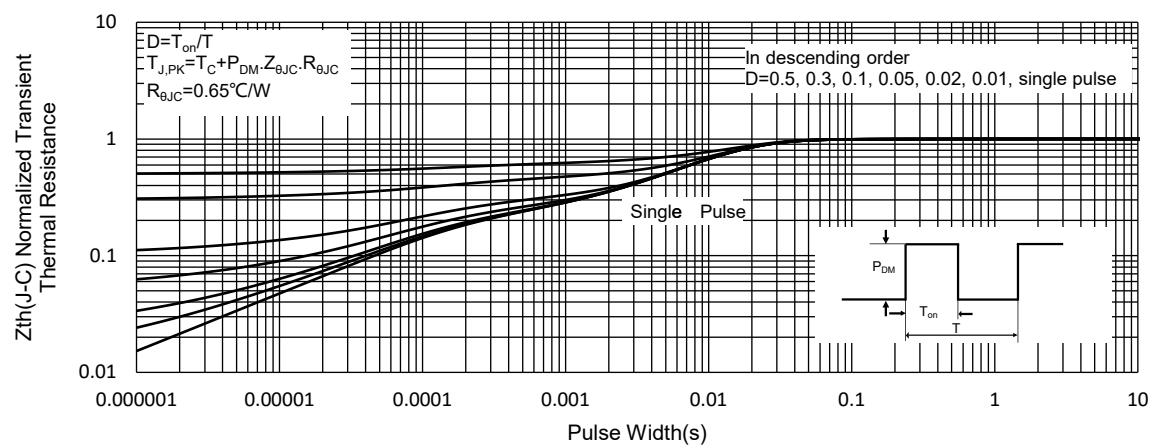


Fig.13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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