

■Features

- •Kyocera's series of Multilayer Ceramic Chip Capacitors are designed to meet a wide variety of needs. We offer a complete range of products for both general and specialized applications.
- •We have a network worldwide in order to supply our global customer bases quickly and efficiently.
- •All our products are highly reliable due to their monolithic structure of high-purity and superfine uniform ceramics and their integral internal electrodes.
- •Our stringent quality control in every phase of production from material procurement to shipping ensures consistent manufacturing and superior quality.
- Kyocera components are available in a wide choice of dimensions, temperature characteristics, rated voltages, and terminations to meet specific configurational requirements.

■KYOCERA PART NUMBER

OPTION:

STOlerance: ±20%

Above digits are used to track individual

Termination: Sn specification or thickness.

(Example) : CM Series(General) 1) Series 2) Size 3) Dielectric : 0201 : X5R

④Capacitance: 2.2μF

8 Packaging : Cavity pitch 2mm / Reel Size φ180

1 SERIES CODE CODE Type СМ General CT Low Profile High-Q

> Three Terminal Capacitors

②SIZE CODE

_				
CODE	EIA	JIS		
02	01005	0402		
03	0201	0603		
05	0402	1005		
105	0603	1608		
21	0805	2012		
316	1206	3216		
32	1210	3225		

3 DIELECTRIC CODE

Temperature Compensation Type						
CODE Temperature Range (°C) ppm/°C						
CG	-55 to 125	0	±30			
CH	-55 (0 125	U	±60			

- All parts of COG will be marked as "CG" but will conform to the above table.
- Temperature coefficients are determined by calculation based on measurement at 20°C and 85°C.

	High Dielectric Constant Type							
CODE	Temperature Range (°C)	∆C max. (%)	Standard Temperature (°C)					
X5R	-55 to 85	±15						
X6S	-55 to 105	±22						
X6T	-55 10 105	+22/-33	25					
X7R		±15	23					
X7S	-55 to 125	±22						
X7T		+22/-33						

4 CAPACITANCE CODE

Capacitance expressed in pF. Two significant digits plus number of zeros. For Values < 10pF, Letter R denotes decimal point, $<1,000pF=1nF,1,000nF=1\mu F>$

CODE	Capacitance
R50	0.5pF
1R0	1pF
100	10pF
101	100pF
102	1nF
103	10nF
104	100nF
105	1μF
106	10µF
107	100µF

E STANDARD NUMBER							
E3	E6	E12	E24				
	1.0	1.0	1.0	1.1			
1.0	1.0	1.2	1.2	1.3			
1.0	1.5	1.5	1.5	1.6			
	1.5	1.8	1.8	2.0			
	2.2	2.2	2.2	2.4			
2.2	3.3	2.7	2.7	3.0			
2.2		3.3	3.3	3.6			
		3.9	3.9	4.3			
	4.7	4.7	4.7	5.1			
4.7	4.7	5.6	5.6	6.2			
4.7	6.8	6.8	6.8	7.5			
	0.0	8.2	8.2	9.1			

(5) TOLERANCE CODE

Temperature Compensation Type (C0G)					
CODE	Tolerance				
A*	±0.05pF				
В	±0.1pF				
С	±0.25pF				
D	±0.5pF				
G*	±2%				
J	±5%				
K	±10%				

^{*:} Option

High Dielectric Constant Type					
(X5R/X6S/X6T/X7R/X7S/X7T)					
CODE Tolerance					
J* ±5%					
K ±10%					
М	±20%				

^{*:} Option

(6) VOLTAGE CODE

CODE	Rated Vltage	CODE
02	2.5Vdc	Α
04	4Vdc	• Please
06	6.3Vdc	is nee
10	10Vdc	
16	16Vdc	
25	25Vdc	
35	35Vdc	
50	50Vdc	
100	100Vdc	

7TERMINATION CODE

CODE	Termination						
Α	A Nickel Barrier/ Tin						
Please contact us if Au termination is needed.							

(8) PACKAGING CODE

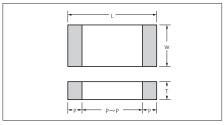
CODE	Size Code	Cavity pitch	Reel size	
Т	105 to 32	4mm		
Н	02 to 05	2mm	φ180	
Q	03/05	1mm	Ψιου	
Р	02	1mm		
L	105 to 32	4mm		
N	02 to 05	2mm	φ330	
W	03/05	1mm		



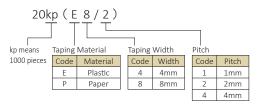


Dimension

■CM/CT/CU Series

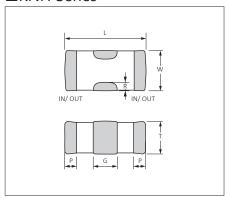


■Packaging Code



C' .	Со	de	Dimension			Dimension (mm)			Quantity	per reel				
Size	EIA	JIS	Code	L	W	Т	P min.	P max.	P to P min.	φ180 Reel	φ330 Reel				
02	01005	0402	А	0.4±0.02	0.2±0.02	0.2±0.02	0.07	0.14	0.13	40kp(E4/1) 20kp(P8/2)	- 80kp(P8/2)				
			A B	0.6±0.03	0.3±0.03	0.22 max. 0.3±0.03	0.10	0.20	0.20	30kp(P8/1) 15kp(P8/2)	150kp(P8/1) 50kp(P8/2)				
03	0201	0603	С	0.6±0.05	0.3±0.05	0.3±0.05				13kp(10/2)	30κρ(10/2)				
05	0201	0003	D			0.22 max.	0.13	0.23	0.19	15kp (P8/2)	50kp(P8/2)				
			Е	0.6±0.09	0.3±0.09	0.3±0.09	0.13	0.23	0.19		30KP(F0/2)				
			F			0.5±0.05				10kp (P8/2)	_				
			Α			0.22 max.]			20kp(P8/1)	100kp(P8/1)				
			В	1.0±0.05	0.5 ± 0.05	0.33 max.]				10kp(P8/2)	50kp(P8/2)			
			С			0.5±0.05]								
			D	1.0±0.10	0.5±0.05	0.22 max.]			10kp (P8/2)	50kp(P8/2)				
05	0402	1005	Е	1.0±0.15	0.5±0.15	0.5±0.15	0.15 0.35 0.35 0.35 0.35 0.35 0.35	0.30	10kp (P8/2)	40kp(P8/2)					
03	0402	102	F			0.33 max.		0.55	0.50	10kp (P8/2)	_				
			G			0.50 max.				10kp (P8/2)	50kp(P8/2)				
			Н	1.0±0.20	0.5±0.20	0.55 max.]							10kp (P8/2)	_
			J			0.5±0.20				10kp (P8/2)	40kp(P8/2)				
			K			0.80 max.				10kp (P8/2)	30kp(P8/2)				
			Α	1.6±0.10	0.8±0.10	0.55 max.									
105	0603	1608	В		***	0.8±0.10	0.20	0.20 0.60 0.50	0.20 0.60 0.50 4kp(P8/4)	10kp(P8/4)					
103	0003	1000	С	1.6±0.15	0.8±0.15	0.8±0.15	0.20			0.00	0.50	ΤΟΚΡ(ΤΟ/47			
			D	1.6±0.20	0.8±0.20	0.8±0.20									
			В	2.0±0.10	1.25±0.10	1.25±0.10				3kp (E8/4)	10kp (E8/4)				
21	0805	2012	С	2.0±0.15	1.25±0.15	0.95 max.	0.20	0.75	0.70	4kp(P8/4)	10kp(P8/4)				
	0003	2012	Е	2.0±0.20	1.25±0.20	0.95 max.		0.73	0.70	4kp(P8/4)	10kp(P8/4)				
			F	2.0-0.20		1.25±0.20				3kp (E8/4)	10kp (E8/4)				
			Α	3.2±0.20	1.6±0.15	1.6±0.15	0.30	0.85	1.40	2.5kp(E8/4)	5kp(E8/4)				
316	1206	3216	В		1.6±0.20	1.6±0.20				,	3				
			С	3.2±0.30	1.6±0.30	1.6±0.30	0.30	0.85	1.90	2kp(E8/4)	_				
32	1210	3225	А	3.2±0.30	2.5±0.20	2.5±0.20	0.30	1.00	1.40	1kp (E8/4)	4kp (E8/4)				

■KNH Series



Size	Со	de	Dimension		Dimension (mm)					Packaging	
Size	EIA	JIS	Code	L	W	Т	G	Р	R	φ180 Reel	φ330 Reel
KNH			Α	1.0±0.10	0.5±0.20	0.5 max.					
05	1 0402 1 1	0402 1005	В	1.0±0.15	0.5±0.15	0.5±0.15	0.3 ± 0.10	0.15±0.10	≥0.05	10kp(P8/2)	_
05			С	1.0±0.20	0.5±0.20	0.5±0.20					





Low Profile CT Series

【RoHS Compliant Products】

■Features

This low profile series is ideal where height clearance is limited

■Applications

Circuits requiring a compact, low-profile design, such as module and memory cards.

X5R Dielectric

Standard Spec.1 Standard Spec.2 •Capacitance chart

Size (EIA Cod	,	CT03 (0201)		CT105 (0603)	CT (08	
Rated Voltage(Capacitano	ce	6.3	6.3	16	16	50
	0 μF	A8				
474 0.4 105 1.	2 μF 17 μF .0 μF		B8	A8		
475 4.	.2 μF .7 μF 0 μF		F9		C8	E3

Standard Capacitor Value : E3 Series>

Please contact for capacitance value other than standard.

Please refer here for the test method and specifications of Standard Specification 1.

Please refer <u>here</u> for the test method and specifications of Standard Specification 2.

Two digits alphanumerics in capacitance chart denote dimensions and $\tan\delta$. Please refer to the above table for detail.

(Example) In case of "A8" for CT03;

L: 0.6±0.03mm, W: 0.3±0.03mm, T: 0.22mm max., Tanδ: 12.5% max.

Tan δ Code	Tan δ
3	5.0% max.
8	12.5% max.
9	15.0% max.

		D	imansian (mr	m)	Packaging										
Size	Dimension	U	imension (mr	11)			φ180 Reel					φ330 Reel			
3126	Code	L	W	Т	Code	Quantity	Taping Material	Taping Width	Cavity Pitch	Code	Quantity	Taping Material	Taping Width	Cavity Pitch	
03	А	0.6±0.03 0.3±	0.3±0.03	0.22 max.	Q	30,000	Paper	8mm	1mm	W	150,000	Paper	8mm	1mm	
05			0.5±0.05		Н	15,000	Paper	8mm	2mm	N	50,000	Paper	8mm	2mm	
	В	1.0±0.05 0.5±0.05	0.5.0.05	0.33 max.	Q	20,000	Paper	8mm	1mm	W	100,000	Paper	8mm	1mm	
05			0.5±0.05		Н	10,000	Paper	8mm	2mm	N	50,000	Paper	8mm	2mm	
	F	1.0±0.20	0.5±0.20	0.33 max.	Н	10,000	Paper	8mm	2mm	_	_	_	_	_	
105	Α	1.6±0.10	0.8±0.10	0.55 max.	T	4,000	Paper	8mm	4mm	L	10,000	Paper	8mm	4mm	
21	С	2.0±0.15	1.25±0.15	0.95 max.	T	4,000	Paper	8mm	4mm	L	10,000	Paper	8mm	4mm	
21	Е	2.0±0.20	1.25±0.20	0.95 max.	T	4,000	Paper	8mm	4mm	L	10,000	Paper	8mm	4mm	

X6T Dielectric

 Capacitance chart Optional Spec.

(EIA	Size Code)	CT03 (0201)	CT (04	05 02)
	/oltage(Vdc) acitance	4	2.5	4
104	0.10 µF			
224	0.22 µF			
474 105	0.47 μF 1.0 μF	Ø D8 ∅		Ø D8 ∅
225	2.2 µF			
475	4.7 μF		P 70	
106	10 μF		€ G8 €	

<Standard Capacitor Value: E3 Series>

Please contact for capacitance value other than standard.

Two digits alphanumerics in capacitance chart denote dimensions and $\tan \delta$. Please refer to the above table for detail.

(Example) In case of "D8" for CT03; L: 0.6 ± 0.09 mm, W: 0.3 ± 0.09 mm, T: 0.22mm max., Tan δ : 12.5% max.

Tan δ Code	Tan δ
8	12.5% max.

		D	imension (mr	m)	Packaging										
Size	Dimension	Di	imension (mi	11)			φ180 Reel					φ330 Reel			
3126	Code	L	W	Т	Code	Quantity	Taping Material	Taping Width	Cavity Pitch	Code	Quantity	Taping Material	Taping Width	Cavity Pitch	
03	D	0.6±0.09	0.3±0.09	0.22 max.	Н	15,000	Paper	8mm	2mm	N	50,000	Paper	8mm	2mm	
05	D	1.0±0.10	0.5±0.05	0.22 max.	Н	10,000	Paper	8mm	2mm	N	50,000	Paper	8mm	2mm	
05	G	1.0±0.20	0.5±0.20	0.50 max.	Н	10,000	Paper	8mm	2mm	N	50,000	Paper	8mm	2mm	





Test Conditions and Standards

Test Conditions and Specifications for High Dielectric Type (X5R, X7R, X7S) CM / CT Series (Standard Spec.1)

Test	Items			Te	est Conditions			Specifications		
Capacitance Va	lue (C)	Measu	ire after hea	at treat	ment			Within tolerance		
Ταηδ		CapacitanceFrequencyVoltC≤10μF1kHz±10%1.0±0.2VrmsC>10μF120Hz±10%0.5±0.2Vrms*CM02X5R104□06A#120Hz±10%0.5±0.2VrmsThe charge and discharge current of the capacitor must not exceed 50mA.						Refer to capacitance chart		
Insulation Resis	tance (IR)	Apply the rated voltage for 1 minute, and measure it in normal temperature and humidity. The charge and discharge current of the capacitor must not exceed 50mA.						Over $10000M\Omega$ or $500M\Omega$ μ F, whichever is less		
Dielectric Resist	tance	*ĊM31 The ch	6X5R225, C	M316X	ated voltage for 7S225/100V: twic ge current of the	ce		No problem observed		
Appearance		Micros						No problem observed		
Termination Str	ength	note : i	2N for 020 le CT series	1 size, a with th	f 500g (5N) to a and 1N for 0100 nickness of less t	5 size. han 0.66mm.		No problem observed		
Bending Streng	th	second	ds.		n spacing: 90mm nickness of less t		10	No significant damage with 1mm bending		
Vibration Test	Appearance	Take the initial value after heat treatment. Vibration frequency: 10 to 55 (Hz)						No problem observed		
	ΔC	Amplit	ude: 1.5mm	,	55→10Hz/ 1 minu	te in X, Y and Z o	direc-	Within tolerance		
				rs in total, and pla easure the sampl		Within tolerance				
Soldering Heat	Appearance	Take the initial value after heat treatment. Soak the sample in 260°C±5°C solder for 10±0.5 seconds and						No problem observed		
Resistant	ΔC		in normal te reatment.	empera	ture and humid	ity, and measur	Within±7.5%			
	Tanδ	(Pre-he	eating cond			T:		Within tolerance		
	IR	Order Temperature Time 1 80 to 100°C 2 minutes						Over $10000M\Omega$ or $500M\Omega$ * μ F, whichever is less		
	Withstanding Voltage	2 150 to 200°C 2 minutes The charge and discharge current of the capacitor must not exceed 50mA for IR and withstanding voltage measurement.					Resist without problem			
Solderablity			Sn-3Ag-0. Sn63 Sold	5Cu er	245±5°C 235±5°C	3±0.5 sec. 2±0.5 sec.		Solder coverage : 95% min.		
Temperature	Appearance	Take th (Cycle)		lue afte	er heat treatmen	t.		No problem observed		
Cycle	ΔC		temperatui	re (3 mi	in.)→			Within±7.5%		
	Tanδ IR				rature (30 min.)	\rightarrow		Within tolerance Over 10000MΩ or 500MΩ•μF, whichever is less		
	Withstanding Voltage	Highes After 5 The ch	cycles, me narge and d	n tempe easure a lischard	in.)→ erature(30 min.) after heat treatm ge current of the withstanding vo	capacitor must	not nent.	Resist without problem		
Moisture	Appearance	Take th	ne initial valu	ie after	heat treatment.			No problem observed		
Resistant Load	ΔC	tion of	pplying rate 40°C±2°C a	d voltag	ge for 500+12/ – o 95%RH, and pla	u nours in the co ace in normal ten	ndı- npera-	Within±12.5%		
	Tanδ	ture an	nd humidity,	then m	easure the sampl	e after heat treat	ment.	200% max. of initial value		
	IR		arge and dis I 50mA for II		current of the ca urement.	pacitor illust 110t		Over $500M\Omega$ or $25M\Omega$ • μ F, whichever is less		
High- Temperature	Appearance				r heat treatment		ation	No problem observed		
Load	ΔC	temper after he	rature for 10 eat treatme	000+12, nt in no	/ –0 hours, and r ormal temperatur	neasure the sam e and humidity.	ple	Within±12.5%		
	Tanδ	exceed	l 50mA for I	R meas	current of the courement.			200% max. of initial value		
		exceed 50mA for IR measurement. *Apply 1.5 times when the rated voltage is 10V or less. Applied voltages for respective products are indicated in the chart below.				IUV or Iess Annli	I .			

Heat treatment Expose sample in the temperature of 150+0/ -10°C for 1 hour and leave the sample in normal temperature and humidity for 24±2 hours.

Voltage to be applied in the High Temperature Load (Applied voltage is the multiple of the rated voltage)

Applied Voltage	Rated Voltage	Products						
×1.0	10V	CM02X5R104						
^1.0	100V	CM316X5R225, CM316X7S225						
×1.3	6.3V	CM02X5R153-104, CT03X5R104						
	16V	CM02X5R101-103, CM05X5R224, CM105X5R225, CM21X5R106, CM316X5R226, CM02X7R101-222,						
		CM105X7R105, CM316X7R106, CM32X7R226, CT105X5R105, CT21X5R475						
×1.5	25V	CM03X5R332-103, CM105X5R105, CM21X5R225-475, CM316X5R106, CM32X5R106-226,						
	23 V	CM05X7R104, CM21X7R105-225, CM316X7R475, CM32X7R106						
	50V	CM21X5R105, CM316X5R475, CM32X5R106, CM21X7R105, CM32X7R106, CT21X5R225,						





Test Conditions and Standards

Test Conditions and Specifications for High Dielectric Type (X5R, X6S, X7R, X7S, X7T) CM / CT Series (Standard Spec.2)

Test	Items			Tes	t Conditions			Specifications	
Capacitance Va	lue (C)	Measure	e after heat	treatn	nent			Within tolerance	
Ταηδ	Capacitance Frequency Volt C≤10µF 1kHz±10% 1.0±0.2Vrms *1kHz±10% 0.5±0.2Vrms C>10µF 120Hz±10% 0.5±0.2Vrms *CM02X5R474M06A#, CM03X5R225□06A#, CM03X5R225M06A#035, CM03X5R475M06A#055, CM03X5R475M06A#, CM05X5R106M06A#, CT05XSR475M06A#033 The charge and discharge current of the capacitor must not exceed 50mA.						Refer to capacitance chart		
Insulation Resis	tance (IR)	mal tem The charg	perature a ge and discha	nd hur rge curr	nidity. ent of the capacito	I measure it in r		Over 50MΩ•μF	
Dielectric Resis	tance	*CM21X7 The charg	S105, CM316 ge and discha	X7S475,		econds. or must not exceed	I 50mA.	No problem observed	
Appearance		Microsc			500 (51)	200		No problem observed	
Termination Str	ength	note : 21 Exclude	N for 0201 CT series v	size, a vith thi	nd 1N for 0100 ckness of less t	han 0.66mm.		No problem observed	
Bending Streng	th	Glass epo Exclude (oxy PCB: Ful CT series wit	crum sp h thickr	pacing: 90mm, du ness of less than	ration time 10 se 0.66mm.	conds.	No significant damage with 1mm bending	
Vibration Test	Appearance	Vibratio	n frequenc		heat treatmen 55 (Hz)	t.	No problem observed		
	ΔC		de: 1.5mm	10→55	i→10Hz/1 minut	e in X V and 7 d	irec-	Within tolerance	
	Sweeping condition: $10 \rightarrow 55 \rightarrow 10$ Hz/ 1 minute in X, Y and Z directions: 2 hours each, 6 hours in total, and place in normal temperature and humidity, then measure the sample after heat treatment.						Within tolerance		
Soldering Heat					No problem observed				
Resistant	ΔC		normal ter			ty, and measure		Within±7.5%	
	Tanδ	(Pre-hea	ating condi			T =:		Within tolerance	
	IR	Order Temperature Time 1 80 to 100°C 2 minutes						Over 50MΩ•μF	
	Withstanding Voltage	2 150 to 200°C 2 minutes The charge and discharge current of the capacitor must not exceed 50mA for IR and withstanding voltage measurement.				e capacitor mu	st not nent.	Resist without problem	
Solderablity			condition Sn-3Ag-0.5 Sn63 Solde		245±5°C 235±5°C	3±0.5 sec. 2±0.5 sec.		Solder coverage : 95% min.	
Temperature Cycle	Appearance		initial valu	ie after	heat treatmen	t.		No problem observed	
Cycle	ΔC	(Cycle) Room te	emperature	e (3 mir	n.)→Lowest ope	ration tempera	ture	Within±7.5%	
	Tanδ		.)→Room te ature(30 mi		iture (3 min.) → I	Highest operation	on	Within tolerance	
	IR				ter heat treatm	ent.		Over 50MΩ•μF	
	Withstanding Voltage	exceed !	50mA for I	R and v	vithstanding vo	capacitor must Itage measurer		Resist without problem	
Moisture Resistant Load	Appearance	Take the	e initial valu	ie after	heat treatmen	t.) hours in the co	ndi-	No problem observed	
Nesistant Load	ΔC	tion of 4	0°C±2°C an	d 90 to	95%RH, and pla	ice in normal ten	npera-	Within±12.5%	
	Tanδ					e after heat treat apacitor must no		200% max. of initial value	
	IR		50mA for IR			.pacitor must mo		Over 10MΩ•μF	
High- Temperature	Appearance	After app	plying *□ tir	nes the	heat treatmen rated voltage a	t the highest ope	eration	No problem observed	
Load	ΔC					neasure the sample and humidity.	ole	Within±12.5%	
	Tanδ	after heat treatment in normal temperature and humidity. The charge and discharge current of the capacitor must not exceed 50mA for IR measurement						200% max. of initial value	
	exceed 50mA for IR measurement. *Apply 1.0 times when the rated voltage is 4V or less. Applied voltages for respective products are indicated in the chart below.						20070 Max. Of Micial Value		

Heat treatment Expose sample in the temperature of 150+0/ -10°C for 1 hour and leave the sample in normal temperature and humidity for 24±2 hours.

Voltage to be applied in the High Temperature Load (Applied voltage is the multiple of the rated voltage)

Applied Voltage	Rated Voltage	Products
	6.3V	CM02X5R224, CM02X5R474, CM03X5R225, CM03X5R475, CM05X5R106 CM05X5R156, CM05X5R226, CM21X5R476, CM03X6S105, CM105X6S226 CT05X5R105, CT05X5R225, CT05X5R475
	10V	CM03X5R225, CM105X5R226, CM21X6S226
×1.0	16V	CM03X5R105, CM05X5R225, CM05X5R475, CM105X5R226 CM21X6S226, CM21X7R475
	25V	CM05X5R105, CM05X5R225, CM05X5R475,CM105X5R475 CM105X5R106, CM21X5R226
	35V	CM05X5R105, CM105X5R475, CM105X5R106
	100V	CM21X7S105, CM316X7S475

	Applied Voltage	Rated Voltage	Products
	×1.2	6.3V	CM03X5R105
1		6.3V	CM03X5R474
	×1.3	10V	CM03X5R223-224, CM05X5R105-225
1		16V	CM05X5R105
1		6.3V	CM21X6S226, CM05X7S105
		0.3 V	CM105X7T106, CM21X7T226
	×15	10V	CM05X5R474, CM05X5R475, CM21X5R226
	^ 1.5	100	CM105X6S106, CM105X7T475
1		25V	CM105X7R105, CM316X7R106
1		50V	CM316X7R475





(Unit: mm)

Test Conditions and Standards

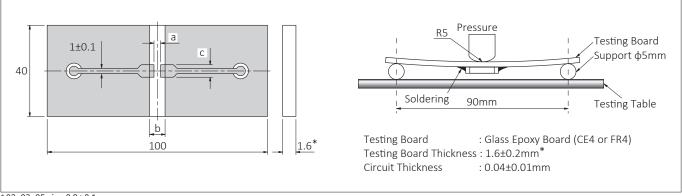
Substrate for Adhesion Strength Test, Vibration Test, Soldering Heat Resistance Test, Temperature Cycle Test, Load Humidity Test, High-Temperature with Loading Test.

С

Size (EIA Code)	a	b	С		
02 (01005)	0.15	0.50	0.20		
03 (0201)	0.26	0.92	0.32		
05 (0402)	0.4	1.4	0.5		
105 (0603)	1.0	3.0	1.2		
21 (0805)	1.2	4.0	1.65		
316 (1206)	2.2	5.0	2.0		
32 (1210)	2.2	5.0	2.9		

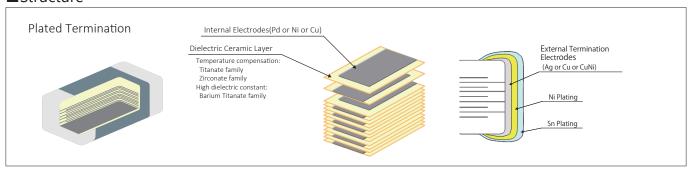
Substrate for Bending Test

(Unit: mm)



*02, 03, 05 size 0.8±0.1mm

■Structure



About official Standards Certification

- The sites that manufacture the products listed in this catalog have acquired ISO 9001 quality management system (certification).
- The production site is Kagoshima Kokubu Plant.

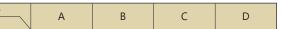


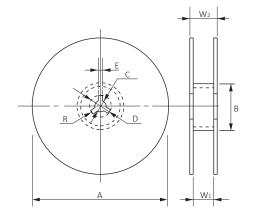


(Unit: mm)

Packaging Options Tape and Reel

Reel

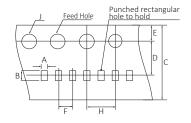




Code Reel	А	В	С	D	
7-inch Reel (CODE: T, H, Q)	180 +0				
7-inch Reel (CODE: P)	178±2.0	φ60 min.	13±0.5	21±0.8	
13-inch Reel (CODE: L, N, W)	330±2.0				
Code Reel	E	W 1	W2	R	
7-inch Reel (CODE: T, H, Q)		10.5±1.5	16.5 max.		
7-inch Reel (CODE: P)	2.0±0.5	4.35±0.3	6.95±1.0	1.0	
13-inch Reel (CODE: L, N, W)		9.5±1.0	16.5 max.		

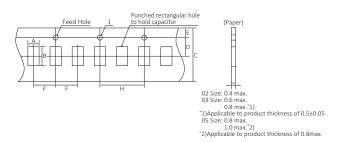
Carrier Tape (Unit: mm)

F=1mm (02 Size)

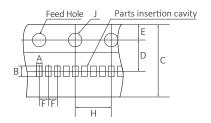




F=2mm (02, 03, 05 Size)

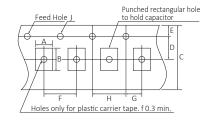


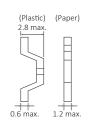
F=1mm (02, 03, 05 Size)





F=4mm (105, 21, 316, 32 Size)





(Unit: mm)

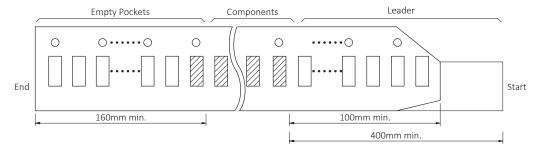
Size	Α	В	С	D	Е	F	G	Н		Carrie	r Tape
(EIA Code)	A	Ь		D			G	П	,	Width	Material
02 (01005)*	0.24±0.02	0.44±0.02	4.0±0.08	1.8±0.02	0.9±0.05	1.0±0.02	_	2.0±0.04	0.8±0.04	4mm	Plastic
02 (01005)	0.25±0.03	0.45±0.03	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05		4.0±0.1	1.5+0.1/-0	8mm	Paper
	0.37±0.03	0.67±0.03	8.0+0.3/-0.1	3.5±0.05	1.75±0.1	1.0±0.05	_	4.0±0.05	1.5+0.1/-0		
	0.57 ± 0.05	0.07 ± 0.03	8.0±0.3	3.3±0.03	1.75±0.1	2.0±0.05		4.0±0.1	1.5+0.1/-0		
03 (0201)*	0.39±0.03	0.69±0.03	8.0 ± 0.3	3.5 ± 0.05	1.75±0.1	2.0±0.05	_	4.0±0.1	1.5+0.1/-0	8mm	Paper
	0.42±0.03	0.72±0.03	8.0 ± 0.3	3.5 ± 0.05	1.75±0.1	2.0±0.05	_	4.0±0.1	1.5+0.1/-0		
	0.44±0.05	0.74±0.05	8.0 ± 0.3	3.5 ± 0.05	1.75±0.1	2.0±0.05	_	4.0±0.1	1.5+0.1/-0		
	0.65±0.1 1.15±0.1	8.0+0.3/-0.1		1.0±0.05	_	4.0±0.05					
05 (0402)*		1.15±0.1	8.0±0.3	3.5 ± 0.05	1.75±0.1	2.0±0.05	_	4.0±0.1	1.5+0.1/-0	8mm	Paper
03 (0402)	0.75±0.1		8.0±0.5							OIIIIII	Taper
	0.8±0.1	1.3±0.1	8.0 ± 0.3	3.5 ± 0.05	1.75±0.1	2.0±0.05	_	4.0±0.1	1.5+0.1/-0		
105 (0603)*	1.0±0.2	1.8±0.2	8.0 ± 0.3	3.5 ± 0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5+0.1/-0	8mm	Paper
103 (0003)	1.1±0.2	1.9±0.2	8.0 ± 0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5+0.1/-0	OIIIIII	
21 (0805)	1.5±0.2	2.3±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5+0.1/-0	8mm	Paper
21 (0003)	1.3±0.2	2.3 ± 0.2	0.0±0.3	3.3±0.03	1.73±0.1	4.0±0.1	2.0±0.03	4.0±0.1	1.5+0.1/-0	8mm	Plastic
316 (1206)	2.0±0.2	3.6±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5+0.1/-0	8mm	Paper
` ′		3.0±0.2	0.0±0.3	3.3±0.03		4.0±0.1		4.0±0.1		8mm	Plastic
32 (1210)	2.9±0.2	3.6±0.2	8.0 ± 0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5+0.1/-0	8mm	Plastic

^{*} Option



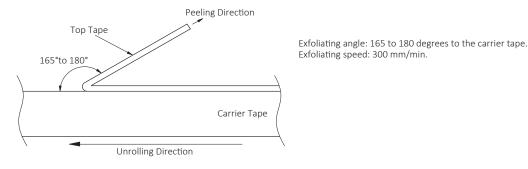
Packaging Options

Detail of leader and trailer



Adhesive tape

- 1) The exfoliative strength when peeling off the top tape from the carrier tape by the method of the following figure shall be *0.1 to 0.7N. *02 Size: 0.1 to 0.5N
- 2) When the top tape is peeled off, the adhesive stays on the top tape.
- 3) Chip capacitors will be in a state free without being stuck on the thermal adhesive tape.



Carrier tape

- 1) Chip will not fall off from carrier tape or carrier tape will not be damaged by bending than within a radius of 25mm.
- 2) The chip are inserted continuously without any empty pocket.
- 3) Chip will not be mis-mounted because of too big clearance between components and cavity. Also the waste of carrier tape will not fill a nozzle hole of mounting machine.



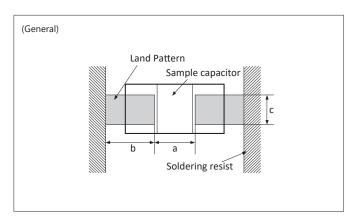


Surface Mounting Information

Dimensions for recommended typical land

Since the amount of solder (size of fillet) to be used has direct influence on the capacitor after mounting, the sufficient consideration is necessary.

When the amounts of solder is too much, the stress that a capacitor receives becomes larger. It may become the cause of a crack in the capacitor. When the land design of printed wiring board is considered, it is necessary to set up the form and size of land pattern so that the amount of solder is suitable.



General

(Unit: mm)

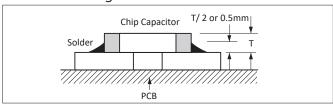
Size	Dime	nsion	Recomme	ended land di	mensions		
(EIA Code)	L	W	а	b	С		
02 (01005)	0.4±0.02	0.2±0.02	0.13 to 0.20	0.12 to 0.18	0.20 to 0.23		
	0.6±0.03	0.3±0.03	0.20 +0.0 25	0.25 to 0.35	0.20 to 0.40		
03 (0201)	0.6±0.05	0.3±0.05	0.20 10 0.25	0.23 10 0.33	0.50 10 0.40		
	0.6±0.09	0.3±0.09	0.23 to 0.30	0.25 to 0.35	0.30 to 0.45		
	1.0±0.05	0.5±0.05	0.30 to 0.50	0.35 to 0.45	0.40 to 0.60		
05 (0402)	1.0±0.15	0.5±0.15	0.40 to 0.60	0.40 to 0.50	0.50 to 0.75		
	1.0±0.20	0.5±0.20	0.40 10 0.00	0.40 10 0.30	0.30 to 0.73		
	1.6±0.10	0.8±0.10	0.70 to 1.00	0.80 to 1.00	0.60 to 0.90		
105 (0603)	1.6±0.15	0.8±0.15					
103 (0003)	1.6±0.20	0.8±0.20	0.80 to 1.00	0.80 to 1.00	0.80 to 1.10		
	1.6±0.25	0.8±0.25					
	2.0±0.10	1.25±0.10	1.00 to 1.30	1.00 to 1.20	1.00 to 1.45		
21 (0805)	2.0±0.15	1.25±0.15	1 00 to 1 20	1.00 to 1.20	1 25 +0 1 55		
	2.0±0.20	1.25±0.20	1.00 to 1.30	1.00 to 1.20	1.23 (0 1.33		
	3.2±0.20	1.6±0.15	2.10 to 2.50	1.10 to 1.30	1.40 to 1.90		
316 (1206)	3.2±0.20	1.6±0.20	2 10 to 2 50	1.10 to 1.30	1 60 to 2 00		
	3.2±0.30	1.6±0.30	2.10 (0 2.30	1.10 (0 1.50	1.00 10 2.00		
32 (1210)	3.2±0.30	2.5±0.20	2.10 to 2.50	1.10 to 1.30	1.90 to 2.80		

^{*} Recommended land dimensions may differ depending on dimensional tolerance.

Design of printed circuit and Soldering

The recommended fillet height shall be 1/2 of the thickness of capacitors or 0.5mm. When mounting two or more capacitors in the common land, it is necessary to separate the land with the solder resist strike so that it may become the exclusive land of each capacitor.

Ideal Solder Height



Item	Prohibited	Recommended example : Separation by solder resist
Multiple parts mount		Solder resist
Mount with leaded parts	Leaded parts	Solder resist Leaded parts
Wire soldering after mounting	Soldering iron Wire	Solder resist
Side by side layout	Solder resist	Solder resist

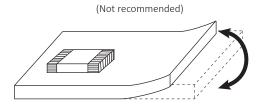


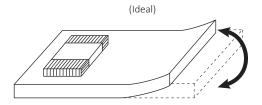
Surface Mounting Information

Mounting Design

The chip could crack if the PCB warps during processing after the chip has been soldered.

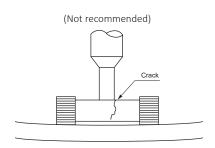
Recommended chip position on PCB to minimize stress from PCB warpage

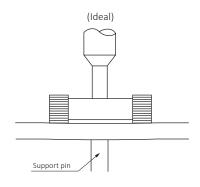




Actual Mounting

- 1) If the position of the vacuum nozzle is too low, a large force may be applied to the chip capacitor during mounting, resulting in cracking.
- 2) During mounting, set the nozzle pressure to a static load of 1 to 3 N.
- 3) To minimize the shock of the vaccum nozzle, provide a support pin on the back of the PCB to minimize PCB flexture.





4) Bottom position of pick up nozzle should be adjusted to the top surface of a substrate which camber is corrected.

Resin Mold

- 1) If a large amount of resin is used for molding the chip, cracks may occur due to contraction stress during curing. To avoid such cracks, use a low shrinkage resin.
- 2) The insulation resistance of the chip will degrade due to moisture absorption. Use a low moisture absorption resin.
- 3) Check carefully that the resin does not generate a decomposition gas or reaction gas during the curing process or during normal storage. Such gases may crack the chip capacitor or damage the device itself.





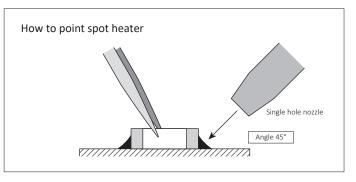
Surface Mounting Information

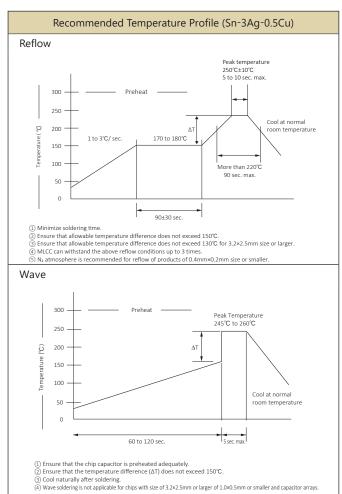
Soldering Method

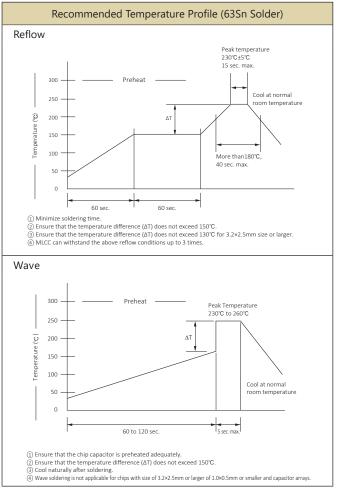
- 1) Ceramic is easily damaged by rapid heating or cooling. If some heat shock is unavoidable, preheat enough to limit the temperature difference (Delta T) to within 150 degree Celsius.
- 2) The product size 1.6×0.8mm to 3.2×1.6mm can be used in reflow and wave soldering, and the product size of bigger than 3.2×1.6mm, or smaller than 1.6×0.8mm can be used in reflow.
 - Circuit shortage and smoking can be created by using capacitors which are used neglecting the above caution.
- 3) Please see our recommended soldering conditions.
- 4) In case of using Sn-Zn Solder, please contact us in advance.
- 5) The following condition is recommended for spot heater application.

Recommended spot heater condition

ltem	Condition
Distance	5mm min.
Angle	45°
Projection Temp.	400°C max.
Flow rate	Set at the minimum
Nozzle diameter	2φ to 4φ (Single hole type)
Application time	10 sec. max. (1206 and smaller) 30 sec. max. (1210 and larger)











Precautions

Circuit Design

capacitors.

- 1. Once application and assembly environments have been checked, the capacitor may be used in conformance with the rating and performance which are provided in both the catalog and the specifications. Use exceeding that which is specified may result in inferior performance or cause a short, open, smoking, or flaming to occur, etc.
- 2. Please consult the manufacturer in advance when the capacitor is used in devices such as: devices which deal with human life, i.e. medical devices; devices which are highly public orientated; and devices which demand a high standard of liability.
 Accident or malfunction of devices such as medical devices, space equipment and devices having to do with atomic power could generate grave consequence with respect to human lives or, possibly, a portion of the public. Capacitors used in these devices may require high reliability design different from that of general purpose
- 3. Please use the capacitors in conformance with the operating temperature provided in both the catalog and the specifications.
 - Be especially cautious not to exceed the maximum temperature. In the situation the maximum temperature set forth in both the catalog and specifications is exceeded, the capacitor's insulation resistance may deteriorate, power may suddenly surge and short-circuit may occur.
 - The capacitor has a loss, and may self-heat due to equivalent series resistance when alternating electric current is passed therethrough. As this effect becomes especially pronounced in high frequency circuits, please exercise caution.
 - When using the capacitor in a (self-heating) circuit, please make sure the surface of the capacitor remains under the maximum temperature for usage. Also, please make certain temperature rises remain below 20°C.
- 4. Please keep voltage under the rated voltage which is applied to the capacitor. Also, please make certain the peak voltage remains below the rated voltage when AC voltage is super-imposed to the DC voltage. In the situation where AC or pulse voltage is employed, ensure average peak voltage does not exceed the rated voltage. Exceeding the rated voltage provided in both catalog and specifications may lead to defective withstanding voltage or, in worst case situations, may cause the capacitor to smoke or flame.
- 5. When the capacitor is to be employed in a circuit in which there is continuous application of a high frequency voltage or a steep pulse voltage, even though it is within the rated voltage, please inquire to the manufacturer. In the situation the capacitor is to be employed using a high frequency AC voltage or a extremely fast rising pulse voltage, even though it is within the rated voltage, it is possible capacitor reliability will deteriorate.
- 6. It is a common phenomenon of high-dielectric products to have a deteriorated amount of static electricity due to the application of DC voltage.
 Due caution is necessary as the degree of deterioration varies depending on the quality of capacitor materials, capacity, as well as the load voltage at the time of operation.
- 7. Do not use the capacitor in an environment where it might easily exceed the respective provisions concerning shock and vibration specified in the catalog and specifications.

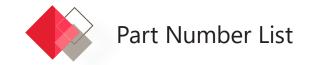
 In addition, it is a common piezo phenomenon of high dielectric products to have some voltage due to vibration or to have noise due to voltage change. Please contact sales in such case.
- 8. If the electrostatic capacity value of the delivered capacitor is within the specified tolerance, please consider this when designing the respective product in order that the assembled product function appropriately.
- 9. Please contact us upon using conductive adhesives.

Storage

Please note the following regarding the storage of delivered products.

- 1. Set the storage temperature to + 5 to + 40 $^{\circ}$ C and humidity to 20 \sim 70% RH. Other meteorological conditions are in accordance with classification 1 K2 of JIS C 60721 -3 -1.
- 2. Store in a place where corrosive gas (H₂S, SO₂, NO₂, Cl₂, etc.) does not exist in the atmosphere. Also, avoid exposure to salty moisture. In either case, this may cause oxidation corrosion of the terminal electrode, reducing solderability.
- If you store the above delivered products according to the conditions listed above, it will satisfy the solderability standard for 6 months from the shipping date.

Safety application guideline and detailed information of electrical properties are also provided in kyocera web site; URL: https://ele.kyocera.com/en/product/capacitor/





Low Profile CT03Series Size (JIS Code): 0201(0603) # Packaging Code (Packaging quantity): H(15,000pcs.) / N(50,000pcs.) / Q(30,000pcs.) / W(150,000pcs.)

Dielectric code	Capacitance	e ¤:Tolerance	Voltage [V]	Part Number	Tanδ		# Packaging Code		
					[%]	L[mm]	W[mm]	T[mm]	(quantity)
X5R	0.1µF	K:±10% / M:±20%	6.3	CT03X5R104 06A#022	12.5	0.6±0.03	0.3±0.03	0.22 max.	H/N/Q/W
X6T	1.0µF	M:±20%	4	CT03X6T105M04A#022	12.5	0.6±0.09	0.3±0.09	0.22 max.	H/N

Low Profile CT05 Series Size (JIS Code): 0402(1005) # Packaging Code (Packaging quantity): H(10,000pcs.) / N(50,000pcs.) / Q(20,000pcs.) / W(100,000pcs.)

Dielectric code	Capacitance :Tolerance				Tanδ		# Packaging Code		
	Сараспапсе	□.Toterarice	[V]	Part Number	[%]	L[mm]	W[mm]	T[mm]	(quantity)
	1.0µF	K:±10% / M:±20%	6.3	CT05X5R105 = 06A#033	12.5	1.0±0.05	0.5±0.05	0.33 max.	H/N/Q/W
X5R	2.2µF	M:±20%	6.3	CT05X5R225M06A#033	12.5	1.0±0.05	0.5±0.05	0.33 max.	H/N/Q/W
	4.7µF	M:±20%	6.3	CT05X5R475M06AH033	15.0	1.0±0.20	0.5±0.20	0.33 max.	Н
X6T	1.0µF	M:±20%	4	CT05X6T105M04A#022	12.5	1.0±0.10	0.5±0.05	0.22 max.	H/N
701	10μF	M:±20%	2.5	CT05X6T106M02A#050	12.5	1.0±0.20	0.5±0.20	0.50 max.	H/N

Low Profile CT105 Series Size (JIS Code): 0603(1608) # Packaging Code (Packaging quantity): T(4,000pcs.) / L(10,000pcs.)

Dielectric code	Capacitance	□:Tolerance	Voltage [V]	Part Number	Tanδ	Dimension			# Packaging Code
Dielectric code					[%]	L[mm]	W[mm]	T[mm]	(quantity)
X5R	1.0µF	K:±10%/M:±20%	16	CT105X5R105=16A#055	12.5	1.6±0.10	0.8±0.10	0.55 max.	T/L

Low Profile CT21 Series Size (JIS Code): 0805(2012) # Packaging Code (Packaging quantity): T(4,000pcs.) / L(10,000pcs.)

Dielectric code	Capacitance	□:Tolerance	Voltage [V]	Part Number	Tanδ [%]	Dimension			# Packaging Code
		ii. Toterance				L[mm]	W[mm]	T[mm]	(quantity)
X5R	2.2µF	K:±10% / M:±20%	50	CT21X5R225 = 50A#095	5.0	2.0±0.20	1.25±0.20	0.95 max.	T/L
X5K	4.7µF	K:±10% / IVI:±20%	16	CT21X5R475 = 16A#095	12.5	2.0±0.15	1.25±0.15	0.95 max.	T/L





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- 2. Contents in this catalog are subject to change without notice. It is recommended to confirm the latest information at the time of usage. Also, Kyocera Electronic Components Catalog is revised once a year. We may not be able to accept requests based on old catalogs.
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