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DESCRIPTION

The JW[®]1967A/JW1967B/JW1967C /JW1967EH (JW1967X series) is a constant current LED regulator which apply to single stage step-down power factor corrected LED drivers.

JW1967X series integrates high voltage power source, and can be supplied by line voltage directly, auxiliary winding and VCC capacitor are not needed.

Patented current sensing and digital compensation technics ensure a unit power factor and high accuracy output current. The critical conduction mode operation reduces the switching losses and increases the efficiency.

JW1967X series has multi-protection functions which largely enhance the safety and reliability of the system, including LED open protection, LED short protection and over-temperature protection.

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FEATURES

- No auxiliary winding and VCC capacitor
- Supplied from line voltage directly
- High current accuracy of line and load regulation
- Internal compensation PFC technics
- Critical conduction mode
- High efficiency over wide operating range
- High voltage power MOSFET integrated
- LED open protection
- LED short protection
- Internal over-temperature protection
- SOP7 and DIP7 package

APPLICATIONS

Non-isolation Offline LED driver



JW1967A/JW1967B/JW1967C /JW1967EH Rev0.83

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ORDER INFORMATION

DEVICE ¹⁾	PACKAGE	TOP MARKING ²⁾
JW1967ASOPA#TRPBF	SOP7	JW1967A YW 🗆 🗆 🗆
JW1967BSOPA#TRPBF	SOP7	JW1967B YW 🗆 🗆 🗆 🗆
JW1967CSOPA#TRPBF	SOP7	JW1967C YW 🗆 🗆 🗆
JW1967EHSOPA#TRPBF	SOP7	JW1967EH YW 🗆 🗆 🗆
JW1967EHDIPA#PBF	DIP7	JW1967EH YW 🗆 🗆 🗆

Notes:



DEVICE INFORMATION

DEVICE	MOS BV	MOS RDSON
JW1967ASOPA#TRPBF	500V	8.5Ω
JW1967BSOPA#TRPBF	550V	5.5Ω
JW1967CSOPA#TRPBF	500V	3Ω
JW1967EHSOPA#TRPBF	600V	1.8Ω

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATING¹⁾

OVP,ISP Voltage	VIN Voltage	
Junction Temperature ^{2) 3)}		
Storage Temperature65°C to +150°C	Storage Temperature	-65°C to +150°C
ESD Susceptibility (Human Body Model)		

RECOMMENDED OPERATING CONDITIONS

VIN Voltage	
0	-40°C to 125°C

Minimum Output Voltage(V_{0_MIN})

JW1967A/B/EH	
JW1967C	

THERMAL PERFORMANCE⁴⁾

SOP7	96	45°C/W
DIP7		45°C/W

Note:

- 1) Exceeding these ratings may damage the device.
- 2) Guarantees robust performance from -40°C to 150°C junction temperature. The junction temperature range specification is assured by design, characterization and correlation with statistical process controls.
- 3) Includes thermal protection that is intended to protect the device in overload conditions. Thermal protection is active when junction temperature exceeds the maximum operating junction temperature. Continuous operation over the specified absolute maximum operating junction temperature may damage the device.

4) Measured on JESD51-7, 4-layer PCB.

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 θ_{JA}

 θ_{JC}

ELECTRICAL CHARACTERISTICS

T_A =25 °C, unles	s otherwise stated.						
	tem	Symbol	Condition	Min.	Тур.	Max.	Units
POWER SUPP	LY						
VIN Start-Up Volta	age	V_{IN_ST}			7	8	V
VIN Quiescent Cu	rrent	I _{INQ}			320	450	μA
CURRENT REC	GULATION				-		
ISP Sample Refer	rence	V _{REF}		192	200	207	mV
Minimum On Time	e of MOSFET ⁵⁾	T _{ON_MIN}			1.2		μS
Maximum On Tim	e of MOSFET	T _{ON_MAX}		19	25	34	μS
Minimum Off Time	e of MOSFET ⁵⁾	T _{OFF_MIN}			2	Š	μS
Maximum Off Time	e of MOSFET ⁵⁾	T _{OFF_MAX}			400		μS
Maximum Switch	Frequency ⁵⁾	f _{MAX}		0	80		KHz
Switching Period	d of VINL ⁵⁾	T_{VINL}			65		μS
PROTECTION					-		
ISP Over Voltage	Protection Threshold	V _{ISP_MAX}		1.08	1.2	1.32	V
		V _{O_OVP1}	V _{OVP} =2V	65	74	82	V
Vo Over Voltage F	Protection Threshold ⁵⁾	V _{O_OVP2}	V _{OVP} =4.8V	102	114	126	V
		V _{O_OVP3}	V _{OVP} =0V	156	174	192	V
Thermal Protectio	n Threshold ⁵⁾	T _{OTP}	2.	140	150		°C
MOS							
	JW1967A	X			8.5	10	Ω
MOS Rdson ⁵⁾	JW1967B	Ddaan	Vgs=10V		5.5	6.5	Ω
MOS Ruson	JW1967C	Rdson	vgs-10v		3	3.5	Ω
	JW1967EH	\mathcal{O}^{r}			1.8	2.2	Ω
	JW1967A			1.6			А
MOS Saturation	JW1967B			2			А
Current ⁵⁾	JW1967C	ld	Vgs=10V	6			А
JW1967EH				8			А
	JW1967A/C			500			V
Breakdown	JW1967B	BV		550			V
Voltage	JW1967EH			600			V

5) Guaranteed by design

PIN DESCRIPTION

Pin No.	Name	Descript	ion
1	GND	Chip ground.	
2	OVP	Set OVP threshold.	
3	NC	Not connect.	
4	VIN	Line voltage input.	
5,6	DRAIN	DRAIN of the MOSFET	
7	ISP	Output current sense.	
I I			

BLOCK DIAGRAM



FUNCTIONAL DESCRIPTION

The JW1967X series is a constant current LED driver which applies to non-isolation step-down LED system with power factor correction. JW1967X series can achieve excellent line and load regulation, high efficiency and low system cost with few peripheral components.

Start Up

JW1967X series is supplied by line voltage directly. When VIN reaches VIN start up voltage (V_{IN_ST}) , the chip begins to switch. Once VIN is lower than VIN under voltage lockout, JW1967X series stops switching.

Constant Current Control

The JW1967X series controls the output current from the information of the sensing resistor voltage. The output LED mean current can be calculated as:

 $I_{LED} = V_{REF} / R_{SNS}$

Where

V_{REF} – ISP sample reference;

 $\ensuremath{\mathsf{R}_{\mathsf{SNS}}}$ – The sensing resistor connected between ISP and GND.

Critical Conduction Mode Operation

JW1967X series works in the Critical conduction mode of the inductor current. When the power MOSFET is turned on, the inductor current begins to increase from zero. The turn on time of the MOSFET can be calculated as:

$$T_{ON} = I_{PK} \times L / (V_{IN} - V_{OUT})$$

Where,

L --inductance.

 I_{PK} – peak current in one switch cycle.

 V_{IN} – input voltage after rectification and

filtering.

V_{OUT} – output LED voltage.

When the power MOSFET is turned off, the inductor current begins to decrease. The power MOSFET turns on again when the inductor current is zero. The turn off time of the MOSFET can be calculated as:

 $T_{OFF} = I_{PK} \times L / V_{OUT}$

And the inductance of the system can be calculated as:

 $L = V_{OUT} \times (V_{IN} - V_{OUT}) / (f \times I_{PK} \times V_{IN})$

Where, f is the switching frequency of the step-down system.

Over Temperature Protection

When internal temperature of the chip exceeds Thermal Protection Threshold(T_{OTP}), JW1967X series decrease LED current to help the chip cooling.

LED Open Protection

The OVP threshold (V_{O_OVP}) is set by the OVP pin. When Vo is higher than V_{O_OVP} , LED open protection is triggered and the chip stops switching for 800ms. The following table shows the V_{O_OVP} design guide:

OVP Pin	$V_{O_OVP}(V)$	
Connected with 510KQ	Vo_ovp1	
resistor		
Not connected	V _{O_OVP2}	
Short connected	V _{O_OVP3}	

APPLICATION NOTES

1: R_{VIN} and V_{O_OVP} design guide

To enhance the surge capability, VIN pin of JW1967X series should be connected to input capacitor by a resistor R_{VIN} (0805/1206 package, no less than 4.7K Ω is recommended). Larger R_{VIN} means better surge capability but please note that too large R_{VIN} may decrease the drive current, and the maximum R_{VIN} is limited by





 R_{VIN} and V_{O_OVP} design guide is shown in following Tab:

V _{O_PEAK} (V)	$\begin{array}{c} \text{Recommended} \\ \text{R}_{\text{VIN}}\left(\Omega\right) \end{array}$	OVP(V)
36~45	5.1K	M
46~50	10K	V _{O_OVP1}
51~100	10K~15K	Vo_ovp2
101~145	10K~20K	Vo_ovp3

Where $V_{O_{PEAK}}$ is the peak value of the Vo, the ripple of the Vo and suitable margin should be taken into consideration when designing the OVP.

2: PCB Design

When designing the PCB of the JW1967X series system, please follow the directions:

- 1. Make the area of the power loop as small as possible in order to reduce the EMI radiation.
- 2. The chip should be far away from the heating element, such as the power inductor and the freewheel diode.

REFERENCE DESIGN

This reference design is suitable for $5 \sim 9W$ non-isolated Step-down LED driver, using JW1967B, with high efficiency, excellent line regulation.

Reference :

V_{IN}: 90VAC~264VAC V_{OUT}: 40~75V I_{OUT}: 100mA PF: >0.9 L1 3mH(8*10) LED+ RI 3.9K/1%/0805 Bl HD06 1 NC C2 0.1uF/275VAC GND ESIJ . อ 100K/5%/1206 C3 0.1uF/400V 90-264Vac El 58uF/100V VR1 ß ā 320VAC GND S ٨IN NC OVP N 1 $\mathbb{R}4$ DRAIN DRAIN ISP RS1 3R9/1%/1206 L2 1.6mH LED-9 Ś RS2 3R9/1%/1206 GND GND Y1 NC

PACKAGE OUTLINE



JW1967A/JW1967B/JW1967C /JW1967EH

DIP7 UNIT: mm E. MILLIMETER SYMBOL MIN NOM MAX 3.71 4,31 А 3.15 3.60 A2 0.38 0.57 b 0.46 Bl 1.47 1.52 1.57 ŀ 0.20 0.25 0.36 с 9.00 9.20 9.40 D Е 6.10 _ 6.60 El 7.32 7.62 7.92 e 2.54BSC E2 8.00 9.02 ____ L 3.00 3.30 3.60 6 5 Л Γ

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