International Rectifier

• Generation V Technology

- Ultra Low On-Resistance
- Dual N-Channel MOSFET
- Surface Mount
- Fully Avalanche Rated
- Lead-Free

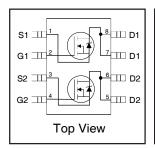
Description

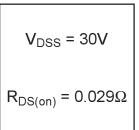
Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

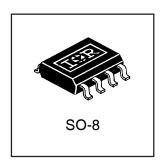
The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and multiple-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infra red, or wave soldering techniques.

IRF7313PbF

HEXFET® Power MOSFET







Absolute Maximum Ratings (T_A = 25°C Unless Otherwise Noted)

		Symbol	Maximum	Units	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current®	T _A = 25°C		6.5		
	T _A = 70°C	- I _D	5.2	Α	
Pulsed Drain Current		I _{DM}	o _M 30		
Continuous Source Current (Diode Conduction)		Is	2.5		
Maximum Power Dissipation ®	T _A = 25°C	- Po	2.0	w	
	T _A = 70°C	LD	1.3	VV	
Single Pulse Avalanche Energy ②		E _{AS}	82	mJ	
Avalanche Current		I _{AR}	4.0	Α	
Repetitive Avalanche Energy		E _{AR}	0.20	mJ	
Peak Diode Recovery dv/dt ③		dv/dt	5.8	V/ ns	
Junction and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to + 150	℃	

Thermal Resistance Ratings

Parameter	Symbol	Limit	Units
Maximum Junction-to-Ambient®	$R_{\theta JA}$	62.5	°C/W

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	30			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		0.022		V/°C	Reference to 25°C, I _D = 1mA
P	Static Drain-to-Source On-Resistance		0.023	0.029	Ω	V _{GS} = 10V, I _D = 5.8A ④
R _{DS(on)}			0.032	0.046		V _{GS} = 4.5V, I _D = 4.7A ⊕
V _{GS(th)}	Gate Threshold Voltage	1.0			V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
9 _{fs}	Forward Transconductance		14		S	V _{DS} = 15V, I _D = 5.8A
I _{DSS}	Drain-to-Source Leakage Current			1.0	μΑ	V _{DS} = 24V, V _{GS} = 0V
				25		$V_{DS} = 24V, V_{GS} = 0V, T_{J} = 55^{\circ}C$
loos	Gate-to-Source Forward Leakage			100	nA	V _{GS} = 20V
I _{GSS}	Gate-to-Source Reverse Leakage			-100	IIA	$V_{GS} = -20V$
Qg	Total Gate Charge		22	33		I _D = 5.8A
Q _{gs}	Gate-to-Source Charge		2.6	3.9	nC	V _{DS} = 15V
Q _{gd}	Gate-to-Drain ("Miller") Charge		6.4	9.6		V_{GS} = 10V, See Fig. 10 \oplus
t _{d(on)}	Turn-On Delay Time		8.1	12		V _{DD} = 15V
t _r	Rise Time		8.9	13	ne	$I_{D} = 1.0A$
t _{d(off)}	Turn-Off Delay Time		26	39	ns	$R_G = 6.0\Omega$
t _f	Fall Time		17	26		$R_D = 15\Omega \oplus$
C _{iss}	Input Capacitance		650			V _{GS} = 0V
C _{oss}	Output Capacitance		320		pF	V _{DS} = 25V
C _{rss}	Reverse Transfer Capacitance		130			f = 1.0MHz, See Fig. 9

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			0.5		MOSFET symbol
	(Body Diode)			- 2.5		showing the
I _{SM}	Pulsed Source Current			30	A	integral reverse
	(Body Diode) ①					p-n junction diode.
V _{SD}	Diode Forward Voltage		0.78	1.0	V	T _J = 25°C, I _S = 1.7A, V _{GS} = 0V ③
t _{rr}	Reverse Recovery Time		45	68	ns	$T_J = 25^{\circ}C$, $I_F = 1.7A$
Q _{rr}	Reverse RecoveryCharge		58	87	nC	di/dt = 100A/µs ③

Notes:

- $\ensuremath{\mathbb{O}}$ Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)
- $\text{ Starting T}_{J} = 25^{\circ}\text{C}, \text{ L} = 10\text{mH} \\ \text{R}_{G} = 25\Omega, \text{ I}_{AS} = 4.0\text{A}.$
- $\label{eq:loss} \begin{array}{l} \text{ } \\ \text{ }$
- 4 Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$.

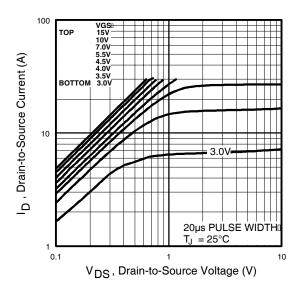


Fig 1. Typical Output Characteristics

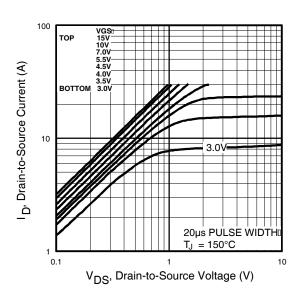


Fig 2. Typical Output Characteristics

VDS

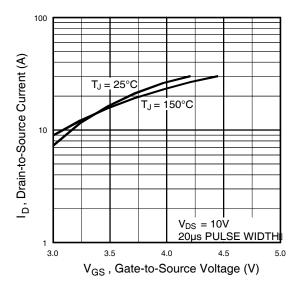


Fig 3. Typical Transfer Characteristics

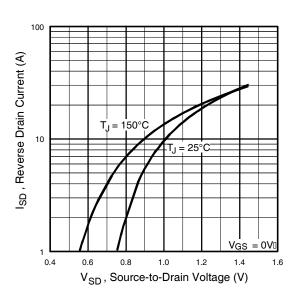


Fig 4. Typical Source-Drain Diode Forward Voltage

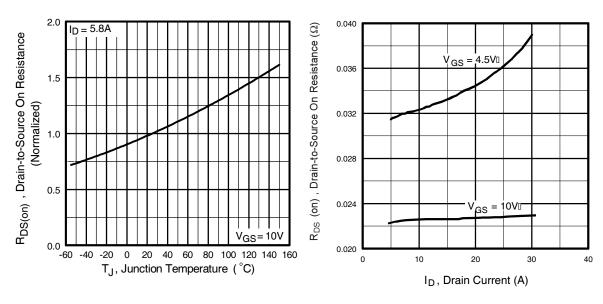


Fig 5. Normalized On-Resistance Vs. Temperature

Fig 6. Typical On-Resistance Vs. Drain Current

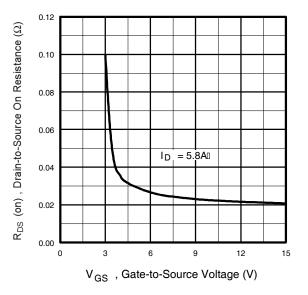


Fig 7. Typical On-Resistance Vs. Gate Voltage

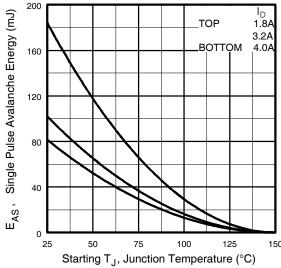


Fig 8. Maximum Avalanche Energy Vs. Drain Current

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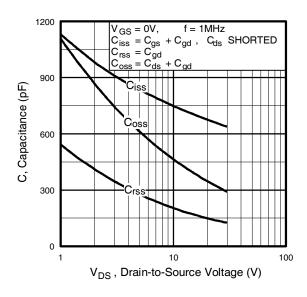


Fig 9. Typical Capacitance Vs. Drain-to-Source Voltage

Fig 10. Typical Gate Charge Vs. Gate-to-Source Voltage

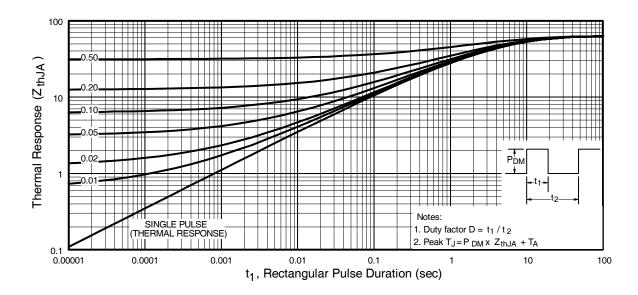
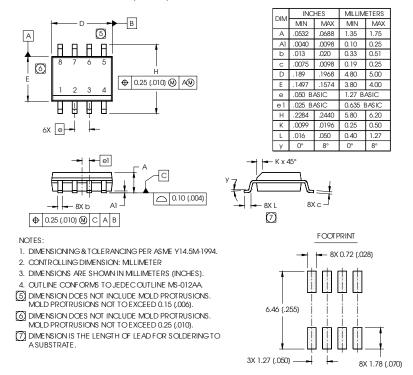


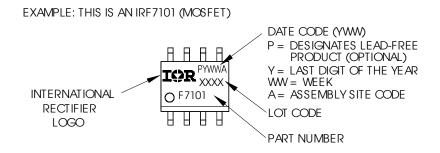
Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

SO-8 Package Outline

Dimensions are shown in milimeters (inches)

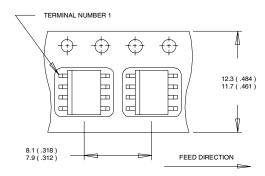


SO-8 Part Marking Information (Lead-Free)



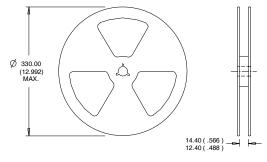
SO-8 Tape and Reel

Dimensions are shown in milimeters (inches)



NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETER.
 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



- NOTES:
 1. CONTROLLING DIMENSION: MILLIMETER.
 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.
- Data and specifications subject to change without notice. This product has been designed and qualified for the Consumer market. Qualifications Standards can be found on IR's Web site.



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