

EVAL-1ED3012MC12I-SiC

Evaluation board description and getting started guide

About this document

Scope and purpose

This user guide provides an overview of the EVAL-1ED3012MC12I-SiC gate driver evaluation board featuring the Infineon EiceDRIVER™ [1ED301xMC12I](#) gate driver IC. It outlines the functionality and highlights the key features of this gate driver IC in a half-bridge topology.

The EVAL-1ED3012MC12I-SiC board is designed to evaluate the functionality and capability of the 1ED3012MC12I gate driver IC. The board is designed with high-frequency decoupling capacitance to provide a low-inductive path in the commutation loop, ensuring optimal switching performance and minimizing the switching noise. While the primary purpose of the decoupling capacitance is to support high-frequency operation, an additional external high-voltage DC link capacitance is strongly recommended for double-pulse testing. To simplify this setup, EVAL-1ED3012MC12I-SiC is designed to be compatible with the [EVAL-DCLink-DPT](#) board from the Infineon [Infineon modular evaluation platform](#). Alternatively, users can connect any other suitable high-voltage capacitance externally to meet their specific testing requirements.

Intended audience

This document is intended for all technical specialists who want to evaluate the functionality, performance, and features of 1ED3012MC12I gate driver ICs. The evaluation board is intended to be used under laboratory conditions only by trained specialists. Read the datasheet of the 1ED3012MC12I IC to become familiar with the features and the datasheet parameters of the gate driver.

Evaluation board

This board is to be used during the design-in process for evaluating the functionality and the performance of the gate driver IC.

Note: *The PCB and auxiliary circuits are NOT optimized for final customer design.*

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Safety precautions

Safety precautions

Note: Please note the following warnings regarding the hazards associated with development systems.

Table 1 **Safety precautions**

	Warning: The DC link potential of this board is up to 900 VDC. When measuring voltage waveforms by oscilloscope, high voltage differential probes must be used. Failure to do so may result in personal injury or death.
	Warning: The evaluation board contains DC bus capacitors which take time to discharge after removal of the main supply. Before working on the evaluation board, ensure with measurement on the high-voltage supply terminals that the capacitors are discharged to safe voltage levels. Failure to do so may result in personal injury or death. Darkened display LEDs are not an indication that capacitors have discharged to safe voltage levels.
	Warning: Remove or disconnect power from the circuit before you disconnect or reconnect wires, or perform maintenance work. Ensure the discharge of the bus capacitors after removing power to the board. Do not attempt to service the circuit until the bus capacitors have discharged to zero. Failure to do so may result in personal injury or death.
	Caution: Only personnel familiar with the circuit, power electronics and associated machinery should plan, install, commission and subsequently service the system. Failure to comply may result in personal injury and/or equipment damage.
	Caution: The evaluation board contains parts and assemblies sensitive to electrostatic discharge (ESD). Electrostatic control precautions are required when installing, testing, servicing or repairing the assembly. Component damage may result if ESD control procedures are not followed. If you are not familiar with electrostatic control procedures, refer to the applicable ESD protection handbooks and guidelines.
	Caution: The evaluation board is shipped with packing materials that need to be removed prior to installation. Failure to remove all packing materials that are unnecessary for system installation may result in overheating or abnormal operating conditions.

Revision history

Document version	Date of release	Description of changes
v1.00	2025-09-08	Initial release

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1 The board at a glance

1 The board at a glance

The EVAL-1ED3012MC12I-SiC evaluation board is designed to be used by engineers to evaluate the 1ED3012MC12I EiceDRIVER™ isolated gate driver ICs and discrete power switches from Infineon in a half-bridge configuration. The evaluation board can be used to evaluate other pin-compatible ICs from the 1ED301xMC12I gate driver family by replacing the gate driver ICs on the board. See the 1ED3012MC12I gate driver [product pages](#) for more details.

The board includes two unassembled Infineon [IMZC120R012M2H](#) CoolSiC™ 1200 V SiC Trench MOSFETs in a TO247-4 package, as seen in [Figure 1](#). These MOSFETs can be mounted on the board for testing or replaced with other compatible switches, such as Infineon IGBTs, CoolSiC™ or CoolMOS™ transistors with the same TO247-4 package. This flexibility allows users to evaluate various switches based on their specific application requirements.

The board has a size of 100 mm x 60 mm x 32 mm without any power switches assembled. Designed primarily for non-continuous evaluation, such as double-pulse testing (DPT), special consideration must be given to the current-carrying capabilities of the power tracks and proper cooling of the power switches if the board is used in continuous testing mode. Additionally, high-voltage bulk capacitors must be added at the high-voltage input terminal for double-pulse testing.

The board is designed to be compatible with the EVAL-DCLink-DPT board from the Infineon modular evaluation platform, which not only provides the necessary capacitance but also offers connection points to facilitate the setup of a double-pulse test. **Therefore, it is highly recommended to also include an EVAL-DCLink-DPT board in the initial order.**

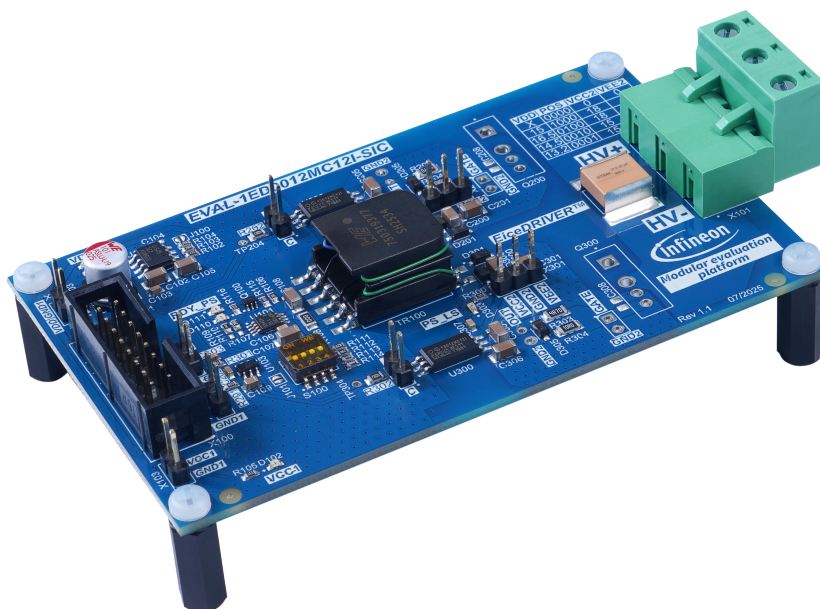


Figure 1 EVAL-1ED3012MC12I-SiC evaluation board

1 The board at a glance

1.1 Scope of supply

The delivery contains:

- The evaluation board EVAL-1ED3012MC12I-SiC
- Two IMZC120R012M2H CoolSiC™ 1200 V SiC Trench MOSFETs for assembly
- High-voltage PCB connector (plugged on high-voltage PCB terminal header)

1.2 Block diagram

Figure 2 shows the block diagram of the EVAL-1ED3012MC12I-SiC evaluation board.

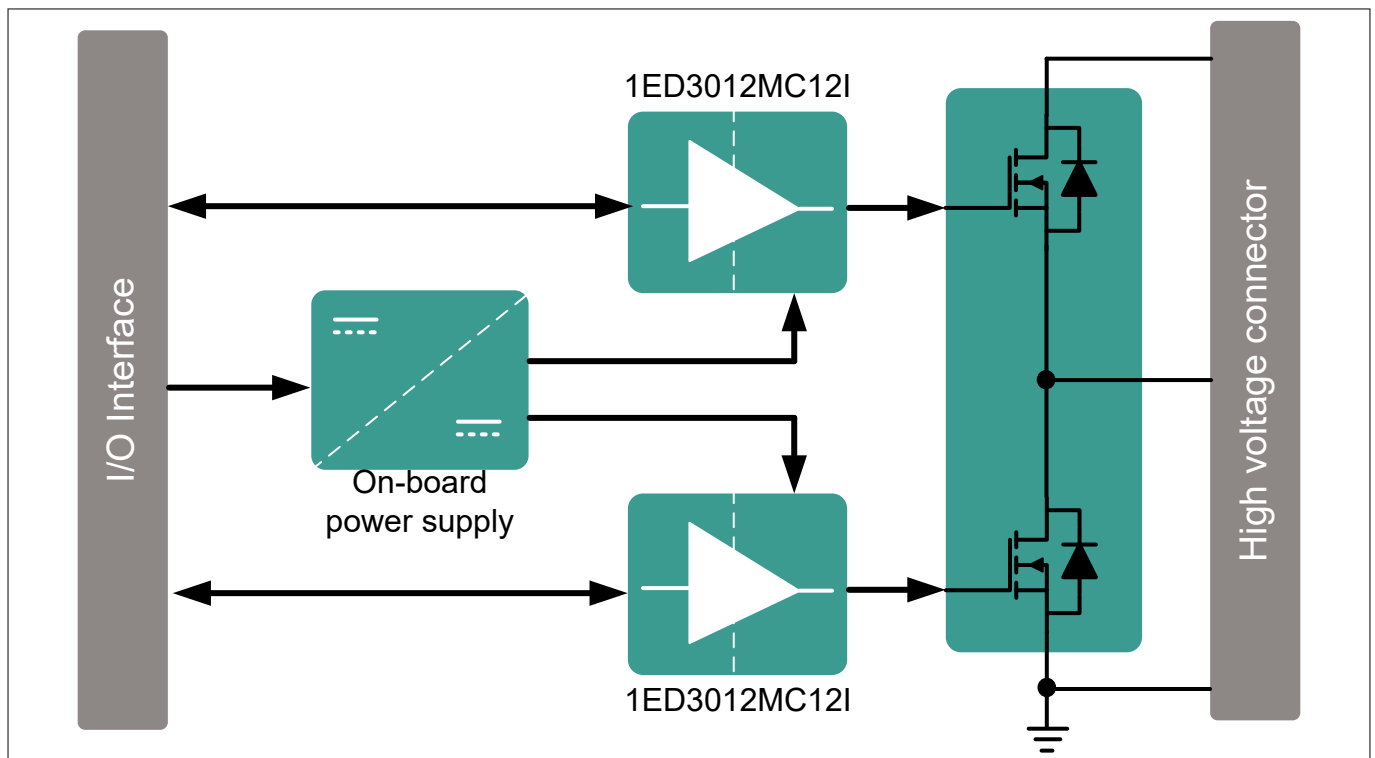


Figure 2 EVAL-1ED3012MC12I-SiC block diagram

1.3 Main features

The EVAL-1ED3012MC12I-SiC is an evaluation board for the 1ED3012MC12I gate driver ICs. It is built in a half-bridge configuration with a maximum DC-link voltage of 900 V. It is optimized for easy measurement and flexible adjustment of the gate driving components and the protection features.

The main features of the evaluation board are:

- Infineon IMZC120R012M2H CoolSiC™ 1200 V SiC Trench MOSFETs in TO247-4 (unassembled) in a half-bridge configuration
- PCB layout designed to ensure optimal switching performance of the half-bridge
- Built-in configurable on-board isolated power supply for the gate driver output side, easily adjustable to generate typical bipolar driving voltages
- Built-in power supply for gate driver input side, easily configurable to generate 3.3 V or 5 V
- Fast short circuit protection and feedback
- UVLO for all the gate driver supply voltages
- Interlocked PWM inputs for shoot-through protection
- Test points for monitoring critical signals

1 The board at a glance

The main features of the gate driver 1ED3012MC12I IC are:

- Single-channel isolated gate driver with opto-compatible input
- Pin-to-Pin compatible, drop-in replacement and upgrade for opto-isolated gate drivers
- Pure-PMOS sourcing stage for optimal turn-on efficiency
- Up to 6.5 A typical peak output current
- 35 V absolute maximum output supply voltage
- 40 ns short propagation delay (typ.) and tight IC-to-IC propagation delay matching
- High common-mode transient immunity CMTI > 300 kV/μs
- Suitable for operation at high ambient temperature and in fast switching applications
- Gate driver safety certification:
 - UL 1577 (planned) recognized with $V_{ISO, test} = 6840$ V (rms) for 1 s, $V_{ISO} = 5700$ V (rms) for 60 s
 - Reinforced isolation as per IEC 60747-17 (planned) with $V_{IORM} = 1767$ V

The on-board isolated power supply is designed with the 2EP130R transformer driver IC. The main features of 2EP130R IC are:

- Wide input voltage range 4.5 V to 20 V
- Wide range of switching frequency from 50 kHz to 695 kHz
- High accuracy duty cycle adjustment from 10% to 50%
- Peak-current controlled soft-start
- Over-current and over-temperature protection
- Ready signal to indicate successful completion of soft-start

1.4 Board parameters and technical data

Table 2 Absolute maximum ratings

Parameter/Pin	Symbol	Conditions/Notes	Value			Unit
			Min.	Typ.	Max.	
Diode protected board input voltage	<i>VDD</i>	Referenced to <i>GND1</i>	-0.3		18	V
Non-inverting PWM input for low-side gate driver	<i>IN_LS</i>	Referenced to <i>GND1</i>	-0.5		6.5	V
Non-inverting PWM input for high-side gate driver	<i>IN_HS</i>	Referenced to <i>GND1</i>	-0.5		6.5	V
Ready output of the power supply	<i>RDY PSU</i>	Referenced to <i>GND1</i>	-0.3		6.5	V
Clock signal for bypass mode of power supply	<i>PWM PSU</i>	Referenced to <i>GND1</i>	-0.3		6.5	V
DC link voltage	<i>HV+</i>	Referenced to <i>HV-</i> power terminal. Limited by component ratings and design clearances. For voltages above 60 V, special safety measures should be taken	-0.2		900	V
Half-bridge midpoint connection	<i>PHASE</i>	Midpoint connection for the half-bridge. Referenced to <i>HV-</i> power terminal	-0.2		900	V

(table continues...)

1 The board at a glance

Table 2 (continued) Absolute maximum ratings

Parameter/Pin	Symbol	Conditions/Notes	Value			Unit
			Min.	Typ.	Max.	
Output-side positive supply voltage for high-side gate driver	<i>VCC2H</i>	Referenced to high-side <i>GND2</i> . To be used only when on-board power supply is deactivated	-0.3		35	V
Output-side positive supply voltage for low-side gate driver	<i>VCC2L</i>	Referenced to low-side <i>GND2</i> . To be used only when on-board power supply is deactivated	-0.3		35	V
Output-side negative supply voltages for high-side gate driver	<i>VEE2H</i>	Referenced to high-side <i>GND2</i> . To be used only when on-board power supply is deactivated	-35		0.3	V
Output-side negative supply voltages for low-side gate driver	<i>VEE2L</i>	Referenced to low-side <i>GND2</i> . To be used only when on-board power supply is deactivated	-35		0.3	V
Output-side differential supply voltage for high-side gate driver	<i>VCC2H</i>	Referenced to high-side <i>VEE2</i> . To be used only when on-board power supply is deactivated	-0.3		35	V
Output-side differential supply voltage for low-side gate driver	<i>VCC2L</i>	Referenced to low-side <i>VEE2</i> . To be used only when on-board power supply is deactivated	-0.3		35	V

Table 3 Recommended operating conditions

Parameter/Pin	Symbol	Conditions/Notes	Value			Unit
			Min	Typ.	Max	
Diode protected board input voltage	<i>VDD</i>	Referenced to <i>GND1</i>	13	15.1	17	V
Input-side supply voltage	<i>VCC1</i>	Referenced to <i>GND1</i>	3	3.3	5.5	V
Non-inverting PWM input for low-side gate driver	<i>IN_LS</i>	Referenced to <i>GND1</i>	-0.1	<i>V</i> _{VCC1}	<i>V</i> _{VCC1} +0.1	V
Non-inverting PWM input for high-side gate driver	<i>IN_HS</i>	Referenced to <i>GND1</i>	-0.1	<i>V</i> _{VCC1}	<i>V</i> _{VCC1} +0.1	V
DC link voltage	<i>HV+</i>	Referenced to <i>HV-</i> power terminal.	0	-	800	V
Half-bridge midpoint connection	<i>PHASE</i>	Midpoint connection for the half-bridge. Referenced to <i>HV-</i> power terminal	0	-	800	V
Output-side positive supply voltage for high-side gate driver	<i>VCC2H</i>	Referenced to high-side <i>GND2</i> . To be used only when on-board power supply is deactivated	15		18	V
Output-side positive supply voltage for low-side gate driver	<i>VCC2L</i>	Referenced to low-side <i>GND2</i> . To be used only when on-board power supply is deactivated	15		18	V
Output-side negative supply voltages for high-side gate driver	<i>VEE2H</i>	Referenced to high-side <i>GND2</i> . To be used only when on-board power supply is deactivated	-5	-	-3	V

(table continues...)

1 The board at a glance

Table 3 (continued) Recommended operating conditions

Parameter/Pin	Symbol	Conditions/Notes	Value			Unit
			Min	Typ.	Max	
Output-side negative supply voltages for low-side gate driver	<i>VEE2L</i>	Referenced to low-side <i>GND2</i> . To be used only when on-board power supply is deactivated	-5	-	-3	V

2 System and functional description

2.1 Getting started

The EVAL-1ED3012MC12I-SiC is designed for convenient testing of the gate driver functionality and switching performance of the gate driver and power transistor in a half-bridge configuration. All the necessary supply voltages required to power the gate driver, including both input-side and output-side voltages, are generated directly on the board. This eliminates the need for multiple external power supplies and reduces setup complexity, allowing users to focus on evaluation and testing.

The board is designed to support both 5 V and 3.3 V for the gate driver input-side supply voltage $VCC1$, featuring a built-in linear power supply circuit to generate the required input voltage. It is important to note that the threshold values for the primary-side input signals are ratiometric, scaling with the $VCC1$ supply voltage.

For the output-side supply voltages of the high-side and low-side gate drivers, it is recommended to use the on-board isolated power supply. This configurable power supply is designed to generate the bipolar supply voltages typically used to drive SiC MOSFETs. The output of the on-board power supply can be configured using the DIP switch S100 position and by adjusting the board input voltage, VDD .

2.1.1 Prerequisites

- The EVAL-DCLink-DPT decoupling board or alternatively any other external high-voltage decoupling capacitor ($>60\ \mu\text{F}$) connected across the high-voltage power terminals: X101 ($HV+$) and X101 ($HV-$)
- A low-voltage power supply for supplying the input voltage of the board, capable of supplying up to 20 V and 500 mA: X102 (VDD) and X102 ($GND1$)
- Suitable pulse generator for double-pulse pattern generation
- High-voltage power supply for supplying the power stage via the EVAL-DCLink-DPT terminal or: X101 ($HV+$) and X101 ($HV-$)
- A suitable inductive load for double-pulse testing

2.1.2 Integration with the Infineon modular evaluation platform

This board is designed to be part of the [Infineon modular evaluation platform](#). Although the evaluation board, EVAL-1ED3012MC12I-SiC is equipped with a small DC link capacitance for high frequency-decoupling, additional bulk capacitance is required for testing high currents in the DPT. In order to enable convenient evaluation, the DC link board [EVAL-DCLink-DPT](#), from the Infineon modular evaluation platform can be used.

The EVAL-DCLink-DPT board is specifically designed to complement the EVAL-1ED3012MC12I-SiC, enabling faster and easier evaluation by providing the necessary capacitance and seamless integration between the two boards. In addition to supplying the required capacitance, the EVAL-DCLink-DPT board also offers dedicated connection points to facilitate the setup of a double-pulse test. The [Figure 3](#) shows the EVAL-DCLink-DPT board. A brief description of the connectors on the EVAL-DCLink-DPT is provided for a quick start. It is strongly recommended to refer to the [user guide](#) to become familiar with the DC link board's functionality. To integrate with the EVAL-DCLink-DPT, unplug the connector from the high-voltage PCB terminal, X101 of EVAL-1ED3012MC12I-SiC and connect the two boards together.

2 System and functional description

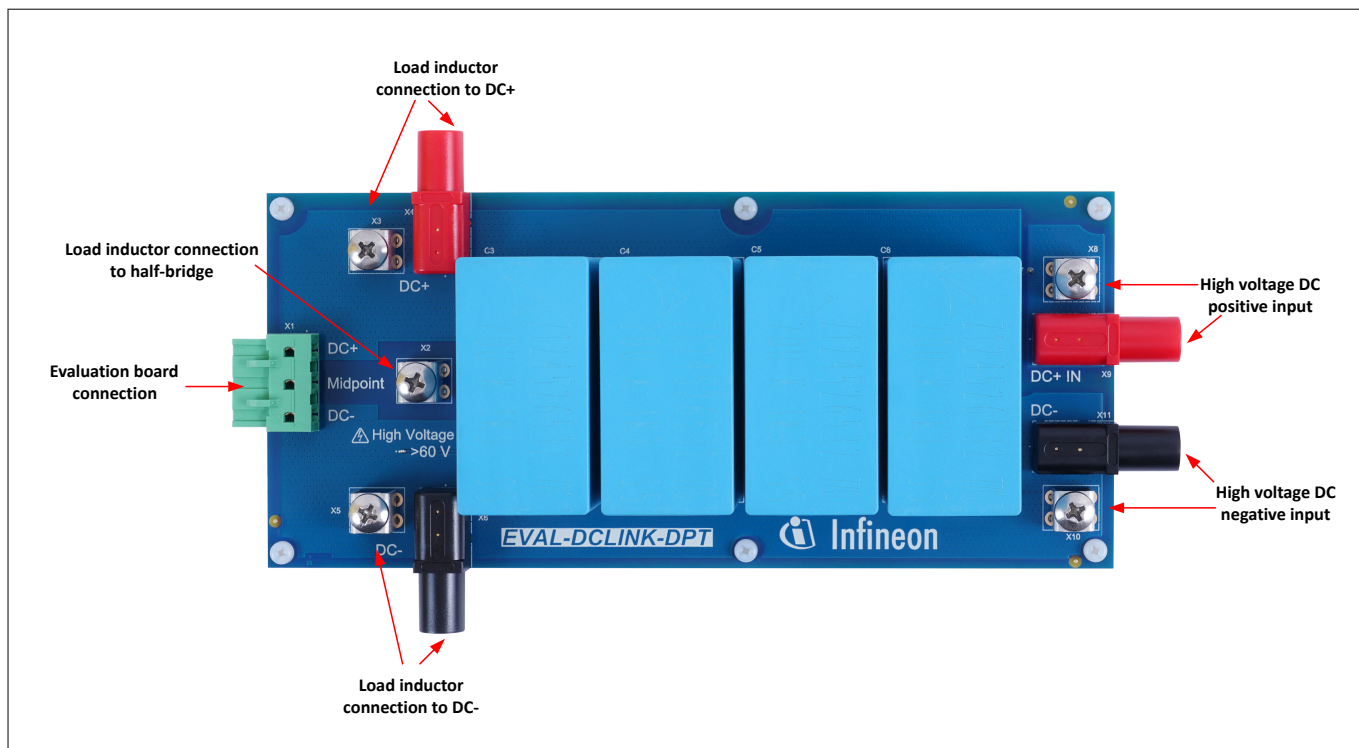


Figure 3 EVAL-DCLink-DPT

2.1.3 Power-up sequence

1. Mount the unassembled IMZC120R012M2H SiC MOSFETs or any other switches with the same package on the EVAL-1ED3012MC12I-SiC board.
2. Connect the double-pulse signal generator to the input pins of the board. For evaluating the low-side switching capability, connect the signal generator to the connector X300 (*IN_LS*) and X300 (*GND1*) and short the pins on the connector X200 (*IN_HS*) and X200 (*GND1*). For evaluation of the high-side switching capability, connect the signal generator to X200 (*IN_HS*) and X200 (*GND1*) and short X300 (*IN_LS*) to X300 (*GND1*).
3. High-voltage power supply and load connections:
 - a. When used with the optional EVAL-DCLink-DPT board, the high voltage power supply and the load should be connected on the DC link board. For more information, please refer the [user guide](#) of the DC link board.
 - b. When used as stand-alone, connect one end of the inductive load to terminal X101 (*PHASE*) and the other end, depending on the double-pulse test requirements, to either X101 (*HV+*) for the low-side testing or X101 (*HV-*) for the high-side testing. Connect the high-voltage power supply to the X101 (*HV+*) terminal and the power ground to the X101 (*HV-*) terminal.
4. Connect the low-voltage input supply for the board, *VDD* at the connector X102, and configure the four pole DIP switch, S100 based on the required gate driver supply voltages. The gate driver output-side power supply voltages can be configured as:

Table 4 Gate driver output-side supply voltage selection

<i>VDD</i> [V]	DIP switch configuration	<i>VCC2</i> [V]	<i>VEE2</i> [V]
X	0000 (Deactivated)	0	0

(table continues...)

2 System and functional description

Table 4 (continued) Gate driver output-side supply voltage selection

VDD [V]	DIP switch configuration	VCC2 [V]	VEE2 [V]
15.1	1000	18	-3
16.5	0100	18	-5
14.5	0010	15	-5
13.2	0001	15	-3

Note: The DIP switch configuration 1 implies the pole is in the ON position.

- If the gate drivers are to be supplied with external voltages, deactivate the on-board power supply using the DIP switch configuration explained in the previous step and supply the external voltages for the high-side gate driver on connector X201 and the low-side gate driver on the connector X301. Please note that the supply voltages for the high-side and the low-side gate drivers should be functionally isolated from each other.
- The internally generated VCC1 supply can be tailored for different signal and supply voltages by the jumper J100. The default configuration (jumper open) sets VCC1 to 3.3 V. This can be changed to 5 V by bridging the jumper J100.
- After the board is powered up, the following green LEDs will turn on indicating that the board is ready for operation:

LED	Symbol	Indication
D101	VDD	Board input-side supply voltage available
D102	VCC1	Gate drivers input-side supply voltage available
D203	PS_HS	High-side gate driver output-side supply voltage available
D303	PS_LS	Low-side gate driver output-side supply voltage available
D110	RDY_PS	On-board isolated power supply ready

- The board is now fully configured and ready for double-pulse evaluation.

2.2 Sample measurement results

The switches IMZC120R012M2H are assembled on the EVAL-1ED3012MC12I-SiC board which is connected to the DC link board, EVAL-DCLink-DPT. The gate drivers are supplied with the on-board power supply as described in [Chapter 2.1.3](#).

2.2.1 Startup behavior

The board is powered up with the gate drivers in a non-switching state.

2 System and functional description

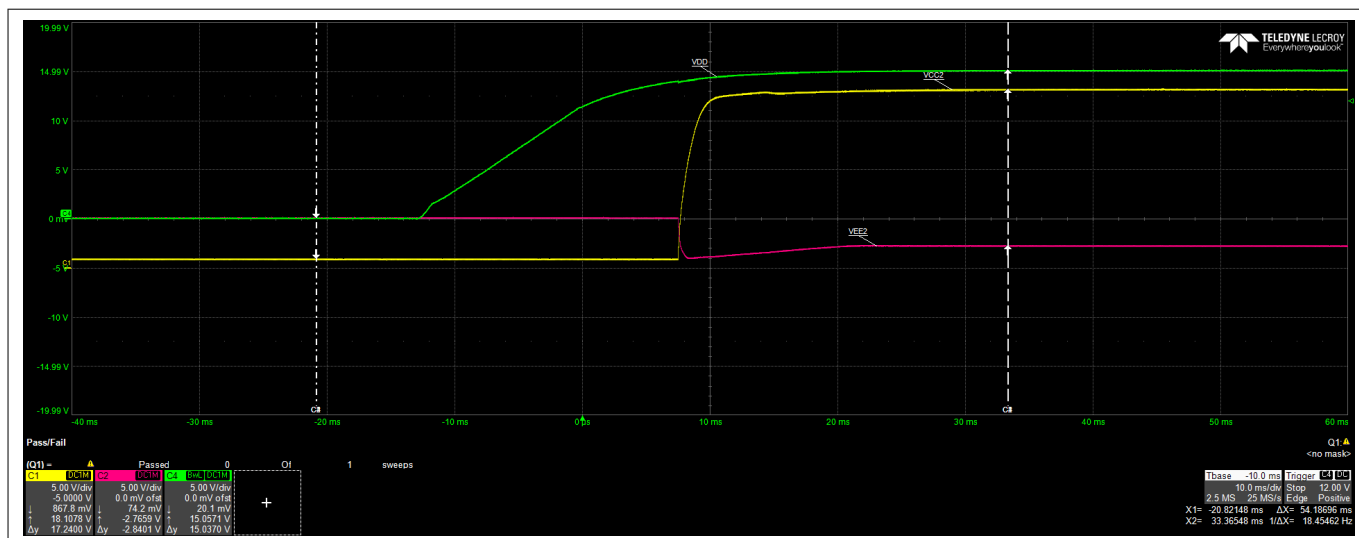


Figure 4 EVAL-1ED3012MC12I-SiC – Gate driver output-side supply voltages startup

Table 5 Measurement signals and scaling

Channel	Color	Signal	Vertical scale	Horizontal scale
Channel 1	Yellow	$V_{CC2_LS} - V_{GND2_LS}$	5 V/div	10 ms/div
Channel 2	Red	$V_{VEE2_LS} - V_{GND2_LS}$	5 V/div	10 ms/div
Channel 4	Green	V_{DD}	5 V/div	10 ms/div

Figure 4 shows the ramp-up of the output-side supply voltages of the low-side gate driver at startup. The positive supply voltage and the negative supply voltage reach 18 V and -2.8 V, respectively after the soft-start.

2.2.2 Double-pulse test

On this evaluation board, the double pulse test is conducted on the low-side switch to evaluate its switching performance and the gate driver's control functionality. The EVAL-DCLink-DPT board is connected to EVAL-1ED3012MC12I-SiC board to provide the bulk capacitance for the testing. The on-board isolated power supply is configured to provide +18 V and -3 V for the output side of the gate drivers. A 201 μ H inductor is connected between the terminals X4 (*DC+*) and X2 (*Midpoint*) on the EVAL-DCLink-DPT board to act as the load. During the test, an 800 V DC voltage is applied across the power terminals X9 (*DC+IN*) and the X9 (*DC-*) of the EVAL-DCLink-DPT board. This voltage appears across the X101 (*HV+*) and X101 (*HV-*) terminals. The block diagram of the measurement setup is shown in Figure 5.

2 System and functional description

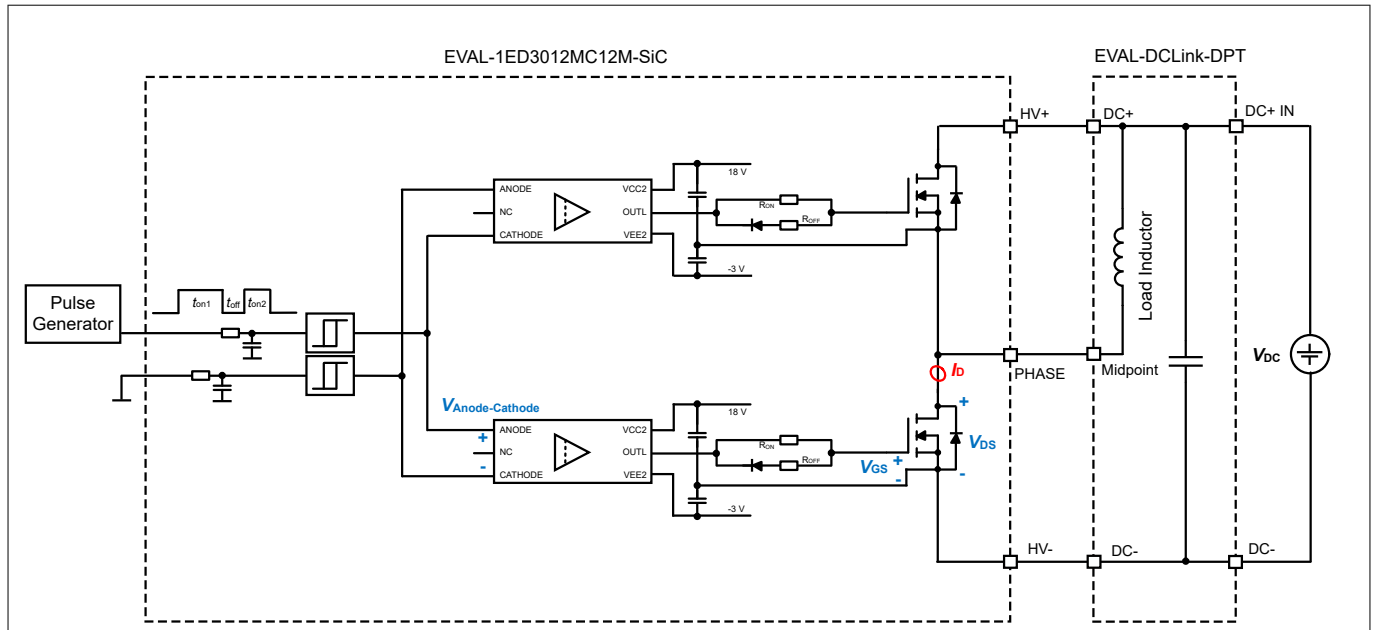


Figure 5 Measurement setup for the double-pulse test

Figure 6, Figure 7 and Figure 8 show the results of the double-pulse test on the EVAL-1ED3012MC12I-SiC board. During the test, the sequence of events is as follows:

1. When the first pulse is applied, the low-side switch turns on, causing the DC voltage to be applied across the load inductor, which leads to a steady ramp-up of the inductor current.
2. By the end of the first pulse, the low-side switch reaches the test current level and it is turned off. For this measurement, a test current of 50 A is chosen.
3. After the turn-off of the low-side switch, the inductor current commutates to the body diode of the high-side switch, allowing the current to continue freewheeling through the circuit.
4. When the low-side switch turns on again, the inductor current commutates from the body diode of the high-side switch back to the low-side switch, enabling the current to continue its natural ramp-up due to the applied DC voltage. At turn-on, the current in the low-side switch is close to the test level of 50 A as a result of the freewheeling process.

Therefore, the double-pulse test enables the characterization of the switch's turn-on and turn-off behavior and allows for the evaluation of the gate driver's ability to control high-current switching events.

2 System and functional description

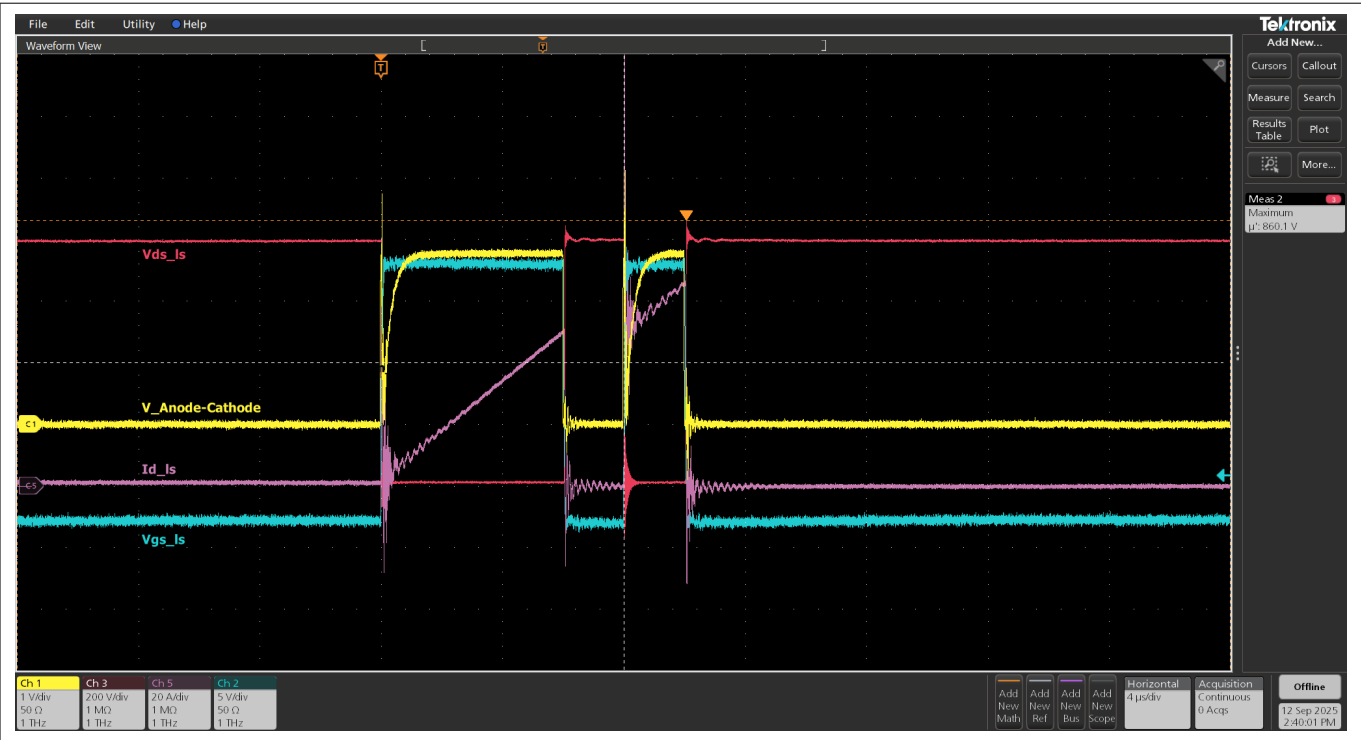


Figure 6

EVAL-1ED3012MC12I-SiC– Double-pulse test of the low-side switch

Table 6

Measurement signals and scaling

Channel	Color	Signal	Vertical scale	Horizontal scale
Channel 1	Yellow	$V_{\text{Anode-Cathode}}$	1 V/div	4 $\mu\text{s}/\text{div}$
Channel 2	Blue	$V_{\text{gs_Ls}}$	5 V/div	4 $\mu\text{s}/\text{div}$
Channel 3	Red	$V_{\text{ds_Ls}}$	200 V/div	4 $\mu\text{s}/\text{div}$
Channel 5	Magenta	$I_{\text{d_Ls}}$	20 A/div	4 $\mu\text{s}/\text{div}$

2 System and functional description

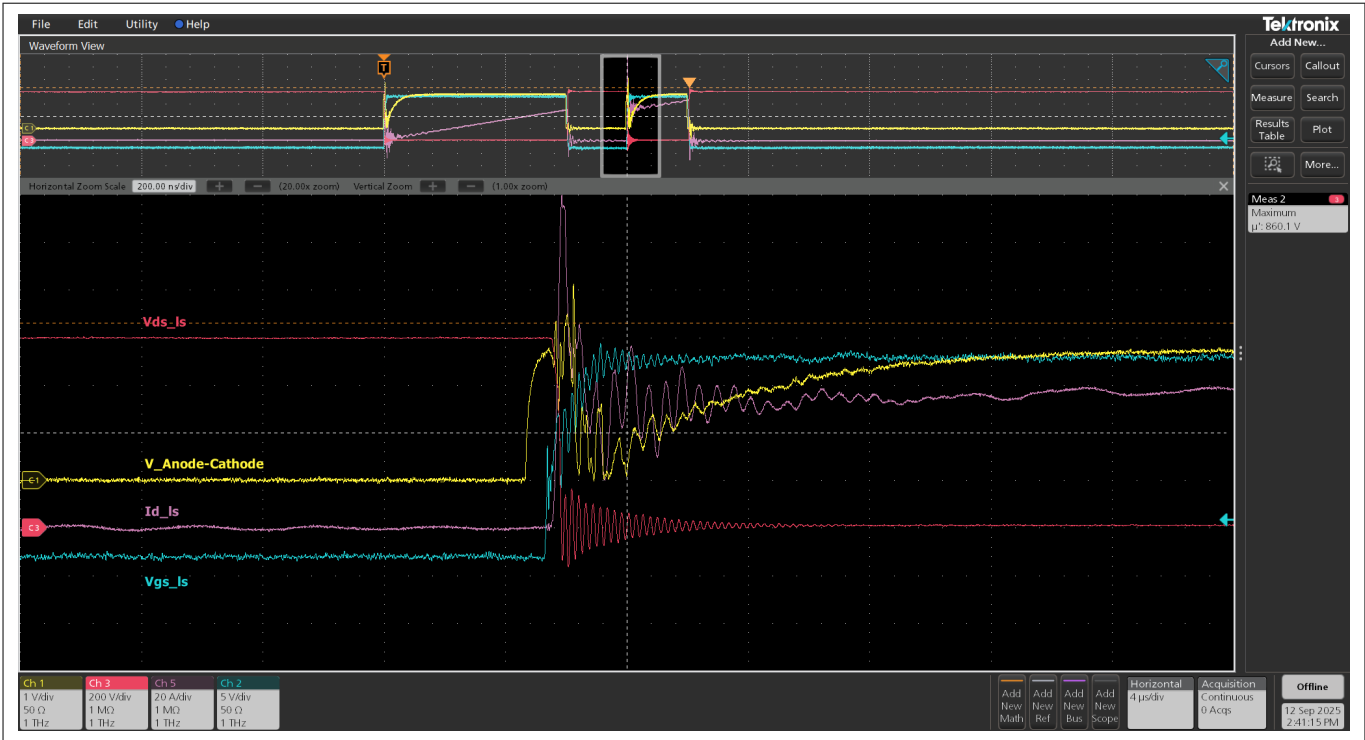


Figure 7 EVAL-1ED3012MC12I-SiC– Turn-on of the low-side switch

Table 7 Measurement signals and scaling

Channel	Color	Signal	Vertical scale	Horizontal scale
Channel 1	Yellow	$V_{\text{Anode-Cathode}}$	1 V/div	200 ns/div
Channel 2	Blue	$V_{\text{gs_ls}}$	5 V/div	200 ns/div
Channel 3	Red	$V_{\text{ds_ls}}$	200 V/div	200 ns/div
Channel 5	Magenta	$I_{\text{d_ls}}$	20 A/div	200 ns/div

2 System and functional description

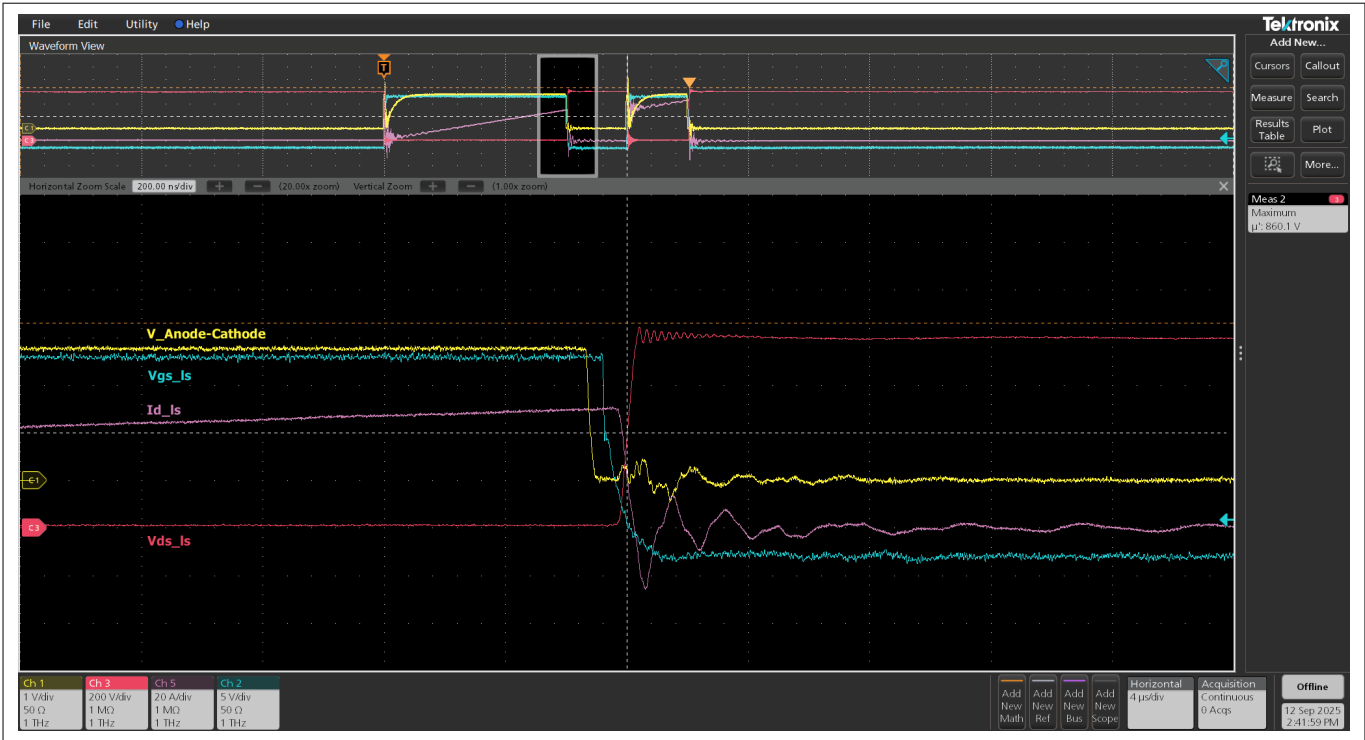


Figure 8 EVAL-1ED3012MC12I-SiC– Turn-off of the low-side switch

Table 8 Measurement signals and scaling

Channel	Color	Signal	Vertical scale	Horizontal scale
Channel 1	Yellow	$V_{\text{Anode-Cathode}}$	1 V/div	200 ns/div
Channel 2	Blue	$V_{\text{gs_Is}}$	5 V/div	200 ns/div
Channel 3	Red	$V_{\text{ds_Is}}$	200 V/div	200 ns/div
Channel 5	Magenta	$I_{\text{d_Is}}$	20 A/div	200 ns/div

3 System design

3 System design

The EVAL-1ED3012MC12I-SiC evaluation board is designed to evaluate the 1ED3012MC12I gate driver IC. To assist the user in quickly getting started with the design, this section provides the schematic, PCB layout details and the bill of materials (BOM). Furthermore, the schematic, the gerber files, the BOM and the complete Altium Designer project files are available for download on the EVAL-1ED3012MC12I-SiC evaluation board's homepage for comprehensive design support.

3.1 Schematics

This section presents the schematics of the evaluation board.

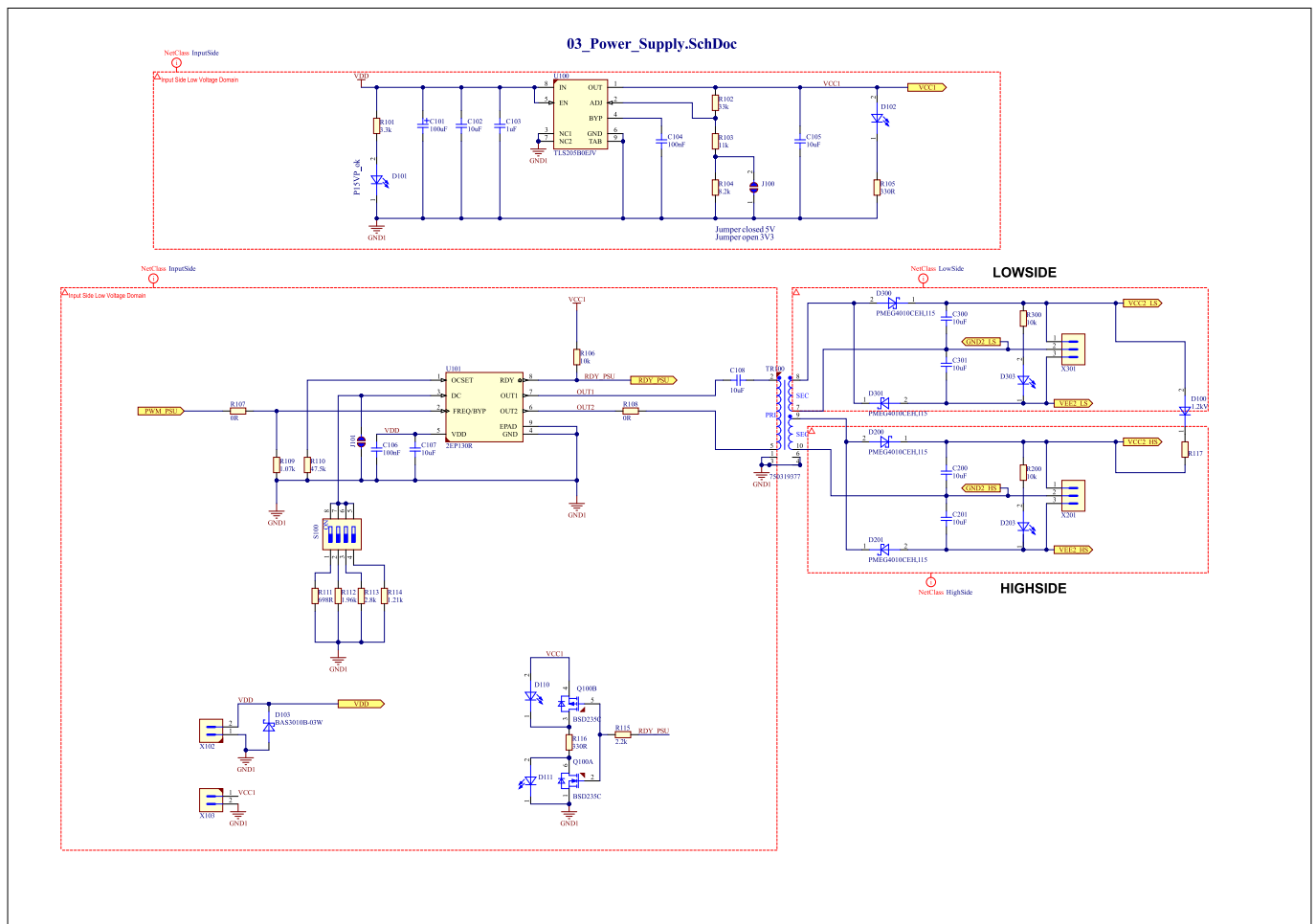


Figure 9 Power supply circuit schematic

3 System design

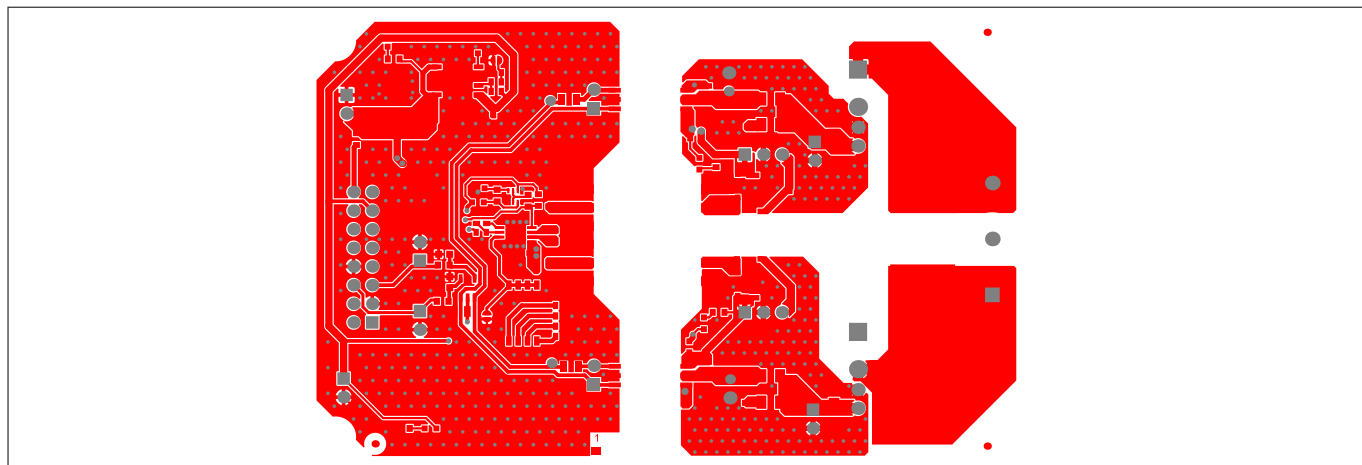


Figure 12 **PCB top copper layer**

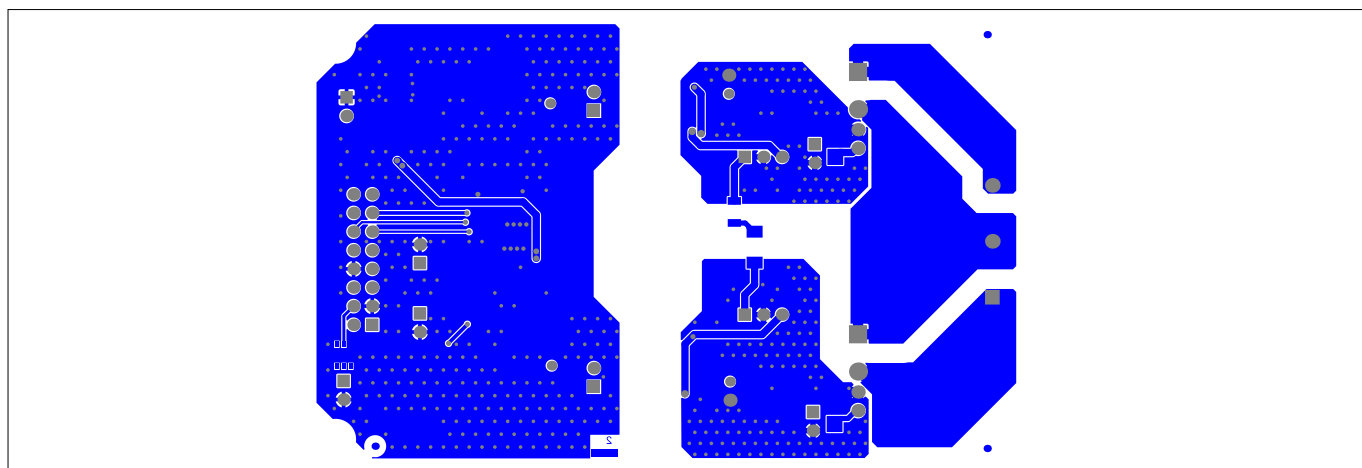


Figure 13 **PCB bottom copper layer**

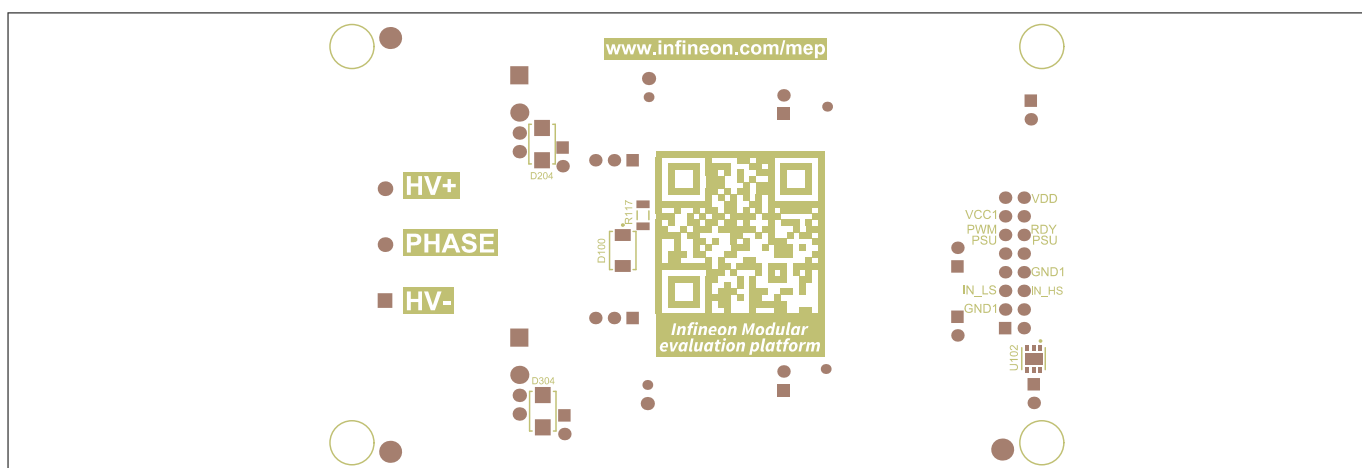


Figure 14 **PCB bottom overlay(mirrored)**

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3.3 Bill of materials

This chapter provides the Bill of Materials (BOM) table, detailing all components, part numbers and quantities required for the evaluation board

Table 9 Bill of materials

Designator	Quantity	Description	Manufacturer	Manufacturer P/N
C100	1	Capacitor, 250nF	TDK Corporation	B58031I9254M062
C101	1	Capacitor, 100uF	Wurth Elektronik	865090445008
C102, C105, C107, C108, C200, C201, C205, C206, C300, C301, C305, C306	12	Capacitor, 10uF, 1206 (3216)	TDK Corporation	C3216X7R1H106K160AC
C103	1	Capacitor, 1uF, 0603 (1608)	Wurth Elektronik	885012206076
C104, C106, C207, C307	4	Capacitor, 100nF, 0603(1608)	Wurth Elektronik	885012206095
C109	1	Capacitor, 100nF, 0805 (2012)	MuRata	GCM21BR72A104KA37
C203, C303	2	Capacitor, 100pF, 0603 (1608)	Wurth Elektronik	885012206053
D101, D102, D110, D203, D303	5	Diode, Green/5V, SMD	Wurth Elektronik	150060VS75000
D103	1	Diode, BAS3010B-03W, SOD323	Infineon Technologies	BAS3010B-03W
D111	1	Diode, Red/5V, 0603 (1608)	Wurth Elektronik	150060RS75000
D200, D201, D205, D300, D301, D305	6	Diode, PMEG4010CEH,115, SOD123F	Nexperia	PMEG4010CEH,115
MP1, MP2, MP3, MP4	4	Screw, D00688	Duratool	D00688
MP5, MP6, MP7, MP8	4	Standoff, 970150365	Wurth Elektronik	970150365
Q100	1	OptiMOS™ Complementary P and N Channel Small Signal Transistor	Infineon Technologies	BSD235C
Q998, Q999	2	CoolSiC™ MOSFET, IMZC120R012M2H, PG-T0247-4-17	Infineon Technologies	IMZC120R012MC12H
R100	1	Resistor, 0R, 0603(1608)	Yageo	RC0603JR-070RL
R101	1	Resistor, 3.3k, 0603 (1608)	Yageo	RC0603FR-073K3L
R102	1	Resistor, 33k, 0603 (1608)	Vishay	CRCW060333K0FK
R103	1	Resistor, 11k, 0603 (1608)	Vishay	CRCW060311K0FK
R104	1	Resistor, 8.2k, 0603 (1608)	Yageo	RC0603FR-078K2L
R105, R116	2	Resistor, 330R, 0603 (1608)	Wurth Elektronik	560112116020
R106, R200, R300	3	Resistor, 10k, 0603 (1608)	Yageo	RC0603FR-0710KL
R108	1	Resistor, 0R, 1206 (3216)	Wurth Elektronik	560112132007
R109	1	Resistor, 1.07k, 0603 (1608)	Vishay	CRCW06031K07FK

(table continues...)

3 System design

Table 9 (continued) **Bill of materials**

Designator	Quantity	Description	Manufacturer	Manufacturer P/N
R110	1	Resistor, 47.5k, 0603 (1608)	Vishay	CRCW060347K5FK
R111	1	Resistor, 698R, 0603 (1608)	Vishay	CRCW0603698RFK
R112	1	Resistor, 1.96k, 0603 (1608)	Vishay	CRCW06031K96FK
R113	1	Resistor, 2.8k, 0603 (1608)	Vishay	CRCW06032K80FK
R114	1	Resistor, 1.21k, 0603 (1608)	Würth Elektronik	560112116120
R115	1	Resistor, 2.2k, 0603 (1608)	Würth Elektronik	560112116098
R201, R301	2	Resistor, 100R, 0603 (1608)	Würth Elektronik	560112116009
R202, R302	2	Resistor, 40.2R, 0805 (2012)	Vishay	CRCW080540R2FK
R203, R303	2	Resistor, 4.7R, 1206 (3216)	Würth Elektronik	RCS12064R70FKEA
R204, R304	2	Resistor, 10R, 1206 (3216)	Yageo	RC1206FR-0710RL
S100	1	DIP Switch, 416131160804, SMD	Würth Elektronik	416131160804
TP200, TP300, X200, X300	4	Header, 2.54mm pitch, 2 pin, vertical, single row	Samtec	HTSW-102-07-L-S
TR100	1	Transformer, 750319377, SMD	Würth Elektronik	750319377
U100	1	Power Supply, TLS205B0EJV, PG-DSO-8-52	Infineon Technologies	TLS205B0EJV
U101	1	Full-bridge transformer driver, 2EP130R, PG-TSSOP-8-2	Infineon Technologies	2EP130R
U103	1	Dual Non-Inverting Schmitt Trigger	Nexperia	74LVC2G17GV,125
U200, U300	2	Single-channel isolated gate driver IC with opto-emulator input, PG-LDSO-6-1	Infineon Technologies	1ED3012MC12I
X100	1	BHD Male Box Header, 2x8	Würth Elektronik	61201621621
X101	1	PCB Header, 7.62 mm Pitch, 3 Pin, Right Angle, 12A, 630V	Phoenix Contact	1766246
X102, X103	2	Header, 2.54mm pitch, 2 pin, vertical, single row	Würth Elektronik	61300211121
X201, X301	2	Header, 2.54mm pitch, 3 pin, vertical, single row	Samtec	HTSW-103-07-L-S
X999	1	PCB Connector, Screw Connection with Tension Sleeve, 12A, 630V	Phoenix Contact	1767012

3 System design

3.4 Connectors

General information about the input and output connectors of the EVAL-1ED3012MC12I-SiC evaluation board is provided in this section.

This table shows the pinout of the input-side low-voltage connectors. It includes the connectors X102, X103, X100, X200 and X300

Table 10 **Input side connectors pinout**

Connector	Pin	Symbol	Function
X100	1		Not connected
X100	2		Not connected
X100	3	<i>GND1</i>	Common ground reference for the input side
X100	4	<i>ID</i>	IO pin of 1-Wire EEPROM
X100	5	<i>IN_LS</i>	Non-inverting input for the low-side gate driver
X100	6	<i>IN_HS</i>	Non-inverting input for the high-side gate driver
X100	7		Not connected
X100	8	<i>GND1</i>	Common ground reference for the input side
X100	9		Not connected
X100	10		Not connected
X100	11	<i>PWM_PSU</i>	External PWM signal for bypass mode of the on-board isolated power supply circuit. Remove the resistor R109 and bridge the solder jumper J101 for bypass mode.
X100	12	<i>RDY_PSU</i>	Ready state of the transformer driver 2EP130R, active high signal
X100	13	<i>VCC1</i>	Supply voltage for the input side of the gate drivers
X100	14		Not connected
X100	15		Not connected
X100	16	<i>VDD</i>	Supply voltage for the input side of the board
X102	1	<i>GND1</i>	Common ground reference for the input side
X102	2	<i>VDD</i>	Supply voltage for the input side of the board
X103	1	<i>VCC1</i>	Supply voltage for the input side of the gate drivers
X103	2	<i>GND1</i>	Common ground reference for the input side
X200	1	<i>IN_HS</i>	Non-inverting input for the high-side gate driver
X200	2	<i>GND1</i>	Common ground reference for the input side
X300	1	<i>IN_LS</i>	Non-inverting input for the low-side gate driver
X300	2	<i>GND1</i>	Common ground reference for the input side

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This table shows the pinout of the output-side gate driver power supply connectors X201 and X301. Use these connectors only if the on-board isolated power supply circuit is deactivated.

Table 11 Output side power supply connectors pinout

Connector	Pin	Symbol	Function
X201	1	VCC2	Positive supply voltage for the high-side gate driver
X201	2	GND2	Ground reference for the supply voltage of the high-side gate driver
X201	3	VEE2	Negative supply voltage for the high-side gate driver
X301	1	VCC2	Positive supply voltage for the low-side gate driver
X301	2	GND2	Ground reference for the supply voltage of the low-side gate driver
X301	3	VEE2	Negative supply voltage for the low-side gate driver

This table shows the pinout of the high-voltage power terminal connector X101.

Table 12 High-voltage terminal connector pinout

Connector	Pin	Symbol	Function
X101	1	HV-	High-voltage DC ground connection
X101	2	Phase	Half-bridge midpoint for connecting to the load inductor
X101	3	HV+	High-voltage DC positive input

3.5 Test points

This chapter provides an overview of all test points available on the evaluation board, detailing their functions and intended measurements.

This table summarizes all the test points available on the evaluation board for measurement of signals on the input side

Table 13 Input side test points

Test point	Pin	Symbol	Function
TP200	1	C_HS	Cathode pin of the high-side gate drivers
TP200	2	A_HS	Anode pin of the high-side gate drivers
TP300	1	C_LS	Cathode pin of the low-side gate drivers
TP300	2	A_LS	Anode pin of the high-side gate drivers

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This table summarizes all the test points available on the evaluation board for measurement of signals on the output side

Table 14 **Output side test points**

Test point	Pin	Symbol	Function
TP202		<i>GATE</i>	Gate voltage of the high-side switch
TP202		<i>GND2</i>	Output-side ground reference for high-side gate driver
TP302		<i>GATE</i>	Gate voltage of the low-side switch
TP302		<i>GND2</i>	Output-side ground reference for low-side gate driver
GP200		<i>GND2</i>	Output-side ground reference for high-side gate driver
TP203		<i>OUT</i>	High-side gate driver output
GP300		<i>GND2</i>	Output-side ground reference for low-side gate driver
TP303		<i>OUT</i>	Low-side gate driver output

4 References and appendices

4 References and appendices

4.1 References

- [1] Datasheet of EiceDRIVER™ [1ED301xMC12I](#)
- [2] Datasheet of EiceDRIVER™ Power [2EP130R](#)
- [3] User guide of [EVAL-2EP130R-PR-SiC](#)
- [4] Datasheet of CoolSiC™ [IMZC120R012M2H](#)

4.2 Ordering information

Base Part Number	Package	Standard Pack		Orderable Part Number
EVAL-1ED3012MC12I-SiC	-	Boxed	1	EVAL1ED3012MC12ISICTOBO1
1ED3012MC12I	DSO-16	TAPE & REEL	1000	1ED3012MC12IXUMA1
2EP130R	PG-TSSOP-8	TAPE & REEL	1000	2EP130RXTMA1
IMZC120R012M2H	PG-TO247-4	Tube	240	IMZC120R012M2HXKSA1

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