

# BFP520F

### Low profile high gain silicon NPN RF bipolar transistor



### **Product description**

The BFP520F is a low noise device based on a grounded emitter (SIEGET<sup>\*\*</sup>) that is part of Infineon's established fifth generation RF bipolar transistor family. Its transition frequency  $f_T$  of 45 GHz, high gain and low noise make the device suitable for applications up to 15 GHz. It remains cost competitive without compromising on ease of use.



### **Feature list**

- Minimum noise figure NF<sub>min</sub> = 0.95 dB at 1.8 GHz, 2 V, 2 mA
- High gain G<sub>ms</sub> = 22.5 dB at 1.8 GHz, 2 V, 20 mA
- *OIP*<sub>3</sub> = 23.5 dBm at 1.8 GHz, 2 V, 20 mA

### **Product validation**

Qualified for industrial applications according to the relevant tests of JEDEC47/20/22.

### **Potential applications**

- Radio-frequency oscillators such as local oscillator in LNB
- Broadband low noise amplifiers (LNAs) for CATV, DVB-T, DAB/DMB and FM/AM radio
- LNAs for wireless communications such as cordless phones

## **Device information**

Table 1	<b>Part information</b>
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Product name / Ordering code	Package	Pin configuration				Marking	Pieces / Reel
BFP520F / BFP520FH6327XTSA1	TSFP-4-1	1 = B	2 = E	3 = C	4 = E	APs	3000

Attention: ESD (Electrostatic discharge) sensitive device, observe handling precautions



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Absolute maximum ratings

## 1 Absolute maximum ratings

Table 2Absolute maxi	mum ratings at T <sub>A</sub>	= 25 °C (ui	nless other	rwise sp	ecified)
Parameter	Symbol	Values		Unit	Note or test condition
		Min.	Max.		
Collector emitter voltage	V <sub>CEO</sub>	-	2.5	V	Open base
			2.4		T <sub>A</sub> = −55 °C, open base
Collector emitter voltage	V <sub>CES</sub>		10		E-B short circuited
Collector base voltage	V <sub>CBO</sub>		10		Open emitter
Emitter base voltage	V <sub>EBO</sub>		1		Open collector
Base current	I <sub>B</sub>		5	mA	-
Collector current	I <sub>C</sub>		50		
Total power dissipation <sup>1)</sup>	P <sub>tot</sub>		120	mW	<i>T</i> <sub>S</sub> ≤ 98 °C
Junction temperature	TJ		150	°C	-
Storage temperature	T <sub>Stg</sub>	-55			

#### Attention: Stresses above the max. values listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability. Exceeding only one of these values may cause irreversible damage to the integrated circuit.

<sup>&</sup>lt;sup>1</sup>  $T_{\rm S}$  is the soldering point temperature.  $T_{\rm S}$  is measured on the emitter lead at the soldering point of the PCB.



Thermal characteristics

## 2 Thermal characteristics

Table 3	Thermal resistance

Parameter	Symbol Values			Unit	Note or test condition	
		Min.	Тур.	Max.		
Junction - soldering point	R <sub>thJS</sub>	-	430	-	K/W	-





Total power dissipation  $P_{tot} = f(T_S)$ 



**Electrical characteristics** 

### 3 Electrical characteristics

### 3.1 DC characteristics

#### Table 4DC characteristics at $T_A = 25 \degree C$

Parameter	Symbol Values			lues Ur		Note or test conditior	
		Min.	Тур.	Max.			
Collector emitter breakdown voltage	V <sub>(BR)CEO</sub>	2.5	3	3.5	V	$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0,$ open base	
Collector emitter leakage current	I <sub>CES</sub>	-	-	10 <sup>2)</sup>	μA	V <sub>CE</sub> = 10 V, V <sub>BE</sub> = 0, E-B short circuited	
Collector base leakage current	I <sub>CBO</sub>			200 <sup>2)</sup>	nA	$V_{CB} = 5 \text{ V}, I_E = 0,$ open emitter	
Emitter base leakage current	I <sub>EBO</sub>			35 <sup>2)</sup>	μA	$V_{\rm EB} = 1 \text{ V}, I_{\rm C} = 0,$ open collector	
DC current gain	h <sub>FE</sub>	70	110	170		$V_{CE} = 2 V, I_C = 20 mA,$ pulse measured	

### 3.2 General AC characteristics

#### Table 5General AC characteristics at $T_A = 25 \text{ °C}$

Parameter	Symbol	Symbol Values				Note or test condition
		Min.	Тур.	Max.		
Transition frequency	f <sub>T</sub>	32	45	-	GHz	$V_{CE} = 2 \text{ V}, I_{C} = 30 \text{ mA},$ f = 2 GHz
Collector base capacitance	C <sub>CB</sub>	-	0.07	0.14	pF	$V_{CB} = 2 V, V_{BE} = 0,$ f = 1 MHz, emitter grounded
Collector emitter capacitance	C <sub>CE</sub>		0.25	-		$V_{CE} = 2 V, V_{BE} = 0,$ f = 1 MHz, base grounded
Emitter base capacitance	C <sub>EB</sub>		0.31			$V_{\text{EB}} = 0.5 \text{ V}, V_{\text{CB}} = 0,$ f = 1  MHz, collector grounded

<sup>&</sup>lt;sup>2</sup> Maximum values not limited by the device but by the short cycle time of the 100% test.



**Electrical characteristics** 

### **3.3 Frequency dependent AC characteristics**

Measurement setup is a test fixture with Bias-T's in a 50  $\Omega$  system,  $T_A$  = 25 °C.



Figure 2 Testing circuit

#### Table 6AC characteristics, $V_{CE} = 2 V, f = 1.8 GHz$

Parameter	Symbol Values		Values L		Note or test condition	
		Min.	Тур.	Max.		
Power gain		-		-	dB	
Maximum power gain	G <sub>ms</sub>		22.5			I <sub>C</sub> = 20 mA
Transducer gain	S <sub>21</sub>   <sup>2</sup>		20.5			
Noise figure						
Minimum noise figure	NF <sub>min</sub>		0.95			I <sub>C</sub> = 2 mA
Linearity					dBm	$Z_{\rm S} = Z_{\rm S.opt}, Z_{\rm L} = Z_{\rm L.opt},$
• 3rd order intercept point at output	OIP <sub>3</sub>		23.5			$Z_{\rm S} = Z_{\rm S,opt}, Z_{\rm L} = Z_{\rm L,opt},$ $I_{\rm C} = 20 \text{ mA}$
• 1 dB gain compression point at output	OP <sub>1dB</sub>		10.5			

Note:  $G_{ms} = IS_{21} / S_{12}I$  for k < 1;  $G_{ma} = IS_{21} / S_{12} I(k - (k^2 - 1)^{1/2})$  for k > 1. In order to get the NF<sub>min</sub> values stated in this chapter, the test fixture losses have been subtracted from all measured results. OIP<sub>3</sub> value depends on termination of all intermodulation frequency components. Termination used for this measurement is 50  $\Omega$  from 0.1 MHz to 6 GHz.



Package information TSFP-4-1







Package outline



#### Figure 4 Foot print



#### Figure 5 Marking layout example







**Revision history** 

# **Revision history**

Document version	Date of release	Description of changes
Revision 2.0	2019-01-25	New datasheet layout.

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Edition 2019-01-25 Published by Infineon Technologies AG 81726 Munich, Germany

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Document reference IFX-tac1525437283834

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