

Features

- Two DTC114Ychips in a package
- Transistor elements are independent, eliminating interference
- Mounting cost and area can be cut in half



SOT-363



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HUMH9NTN	SOT-363	H9	3000

Maxmim Ratings (Ta=25 unless otherwise noted)

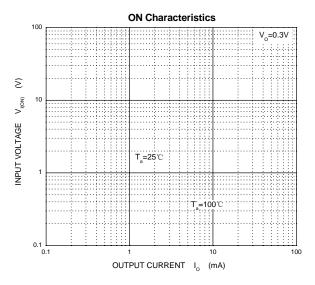
Symbol	Parameter	Limits	Unit
Vcc	Supply Voltage	50	V
V _{IN}	Input Voltage	- 6 ∼ + 40	V
lo	Output Current	70	mA
I _{CM}	Peak Collector Current	100	mA
P _D	Power Dissipation	150	mW
T _J ,T _{stg}	Operation Junction and Storage Temperature Range	-55∼+150	$^{\circ}$

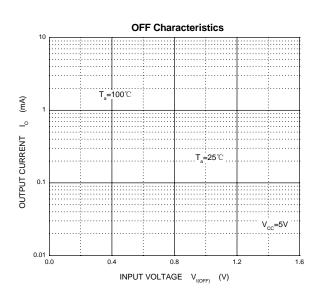
Electrcal Charcteristics (Ta=25 unless otherwise specified)

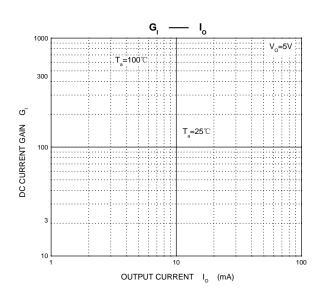
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Input voltage	$V_{I(off)}$	V _{CC} =5V,I _O =100μA	0.3			V
	V _{I(on)}	V _O =0.3V,I _O =1mA			1.2	V
Output voltage	$V_{O(on)}$	I _O /I _I =5mA/0.25mA			0.3	V
Input current	l _l	V _I =5V			0.88	mA
Output current	I _{O(off)}	V _{CC} =50V,V _I =0			0.5	μΑ
DC current gain	Gı	V _O =5V,I _O =5mA	68			
Input resistance	R ₁		7	10	13	kΩ
Resistance ratio	R ₂ /R ₁		3.7	4.7	5.7	
Transition frequency	f⊤	V _O =10V,I _O =-5mA,f=100MHz		250		MHz

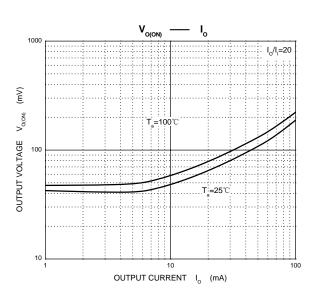


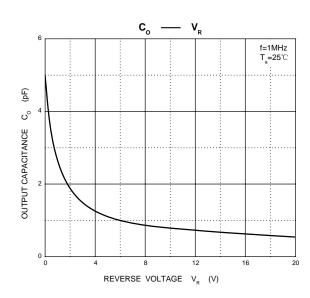
Typical Electrical

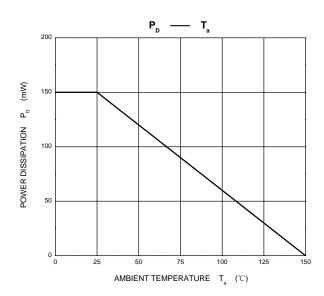






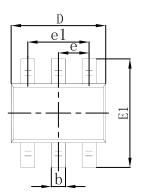


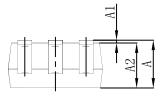


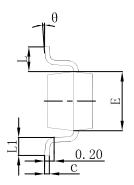




SOT-363 Package Outline Dimensions







Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.900	1.100	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.000	0.035	0.039	
b	0.150	0.350	0.006	0.014	
С	0.100	0.150	0.004	0.006	
D	2.000	2.200	0.079	0.087	
E	1.150	1.350	0.045	0.053	
E1	2.150	2.400	0.085	0.094	
е	0.650 TYP		0.026 TYP		
e1	1.200	1.400	0.047	0.055	
L	0.525 REF		0.021 REF		
L1	0.260	0.460	0.010	0.018	
θ	0°	8°	0°	8°	



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DTC114EUA-TP SMUN5237DW1T1G SMUN5213DW1T1G SMUN5114DW1T1G DTC124ECA-TP DTA114ECA-TP DTC113EM3T5G

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NSBA113EF3T5G MUN2235T1G NSBC143ZDXV6T5G NSVDTA114EM3T5G MUN2138T1G DCX124EUQ-7-F MUN2141T1G

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