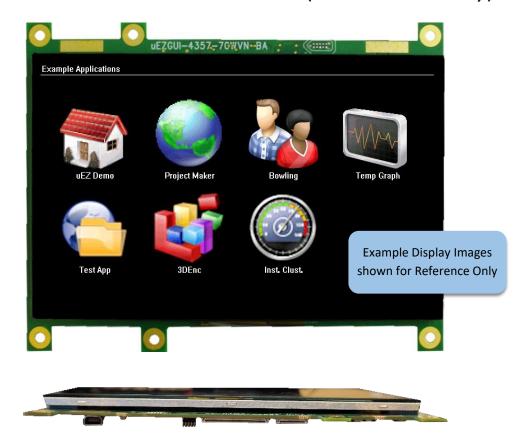
# μEZ® GUI User's Manual

Covers the following products:

UEZGUI-4357-70WVN (Dev Kit)
UEZGUI-4357-70WVN-BA (Board Assembly)



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### 1. Introduction

The UEZGUI-4357-70WVN-BA/UEZGUI-4357-70WVN (Dev Kit) provides a quick and easy solution for implementing a Graphical User Interface (GUI) based design by providing the basic functions necessary for most customer products.

# 2. Block Diagram

# uEZGUI-4357-70WVN Block Diagram

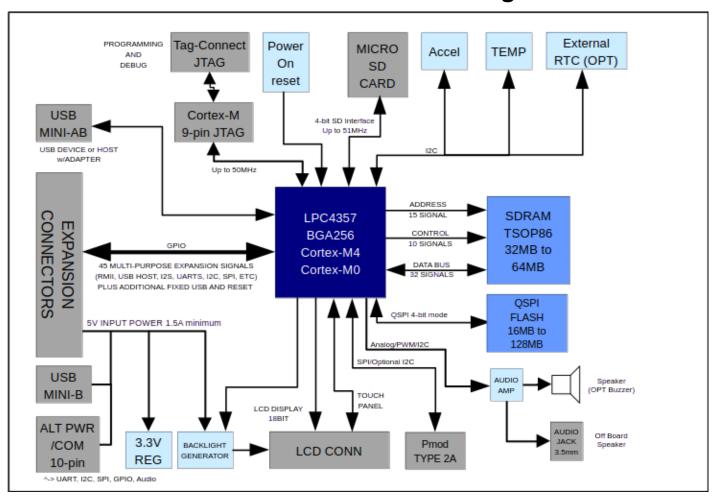


Figure 1 – UEZGUI-4357-70WVN-BA Block Diagram

## 3. Kit Contents for FDI PN: UEZGUI-4357-70WVN

- UEZGUI-4357-70WVN-BA module with 7.0" Capacitive Touch Screen LCD
- SEGGER J-Link Lite Cortex-M Debug Probe
  - 9 Pin Samtec Cable (Keyed)
- Micro SD card
- USB Type A to USB Type Mini B Cable (2x)
- Universal AC to 5V USB plug Power Supply
- Quick Start Guide



### 4. Useful links

Complete Users Manuals, Schematics, and documentation are available on the Micro-SD card provided with the  $\mu$ EZ GUI Kit and are also available from the following websites (please refer to the websites for the latest updates):

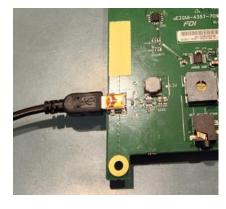
- Future Designs Support Page <a href="https://www.teamfdi.com/support/">https://www.teamfdi.com/support/</a>
- μEZ Source Code, user's manuals, and quick start guides <a href="https://sourceforge.net/projects/uez/">https://sourceforge.net/projects/uez/</a>
- SEGGER J-Link Debugger https://www.SEGGER.com/cms/jlink-software.html
- Rowley CrossWorks IDE download for 30-day evaluation <a href="https://www.rowley.co.uk/arm/index.htm">https://www.rowley.co.uk/arm/index.htm</a>

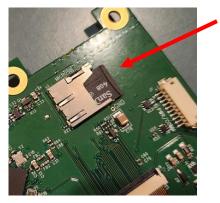
# 5. Functional Description

- LPC4357 Microcontroller with 204 MHz Cortex-M4 and Cortex-M0 Cores
- SDRAM 32MB, optional up to 64MB
- QSPI (Serial Flash operating in quad mode) 16MB, optional up to 128MB memory mapped.
- Internal on-chip 16KB EEPROM
- RTC Real Time Clock with Optional Super Capacitor backup
- Temperature Sensor
- 3-axis Accelerometer
- Speaker
- Micro-SD Card Socket for up to 64GB SDHC storage, microSD card included with kit only
- Cortex-M 9-pin JTAG connector
- PMOD Type 2A connector
- Power-on Reset Generator power-on reset supervisor and voltage monitor (SW1)
- Expansion Connector for customer specific applications

# 6. Startup procedure

The µEZ GUI kit comes with a pre-installed micro-SD card that contains files required for the various demos to run. It also contains users' manuals, schematics, and documentation for the product.





Power is supplied via the USB cable provided in the kit. To power on the unit connect the USB cable to the USB 5V 2A power adapter provided in the kit and to P2, the mini-B USB connector. DO NOT try to power the unit from a standard computer USB port. It does not provide enough power.

The following screen should appear once power has been applied to the kit:





At this point the unit is ready for software demonstrations and user operation.

## 7. Demonstration Software

Note: The Demonstration Software is subject to change at any time.

The following software demonstrations are available from the Main Menu:

### a) Example Applications

- uEZ Demo this will install the main μEZ GUI demo application, which demonstrates the capabilities of uEZ
   GUI to display slideshows, play videos, and store data. This will be covered in more detail below.
- Bowling This demonstration is an example of a bowling alley user interface.
- **Project Maker** This is a demonstration example application developed using the Project Maker utility from Future Designs, Inc. Currently, this demonstration is a Temperature and Time/Date application.

#### Sensors

Select the Sensor button to display on-board accelerometer readings and the temperature from the on-board temperature sensor. To return to the main menu, select 'Back'.

### • Time and Date

Select this feature to display the current time and date from the internal real time clock (RTC).

- To change the time or date:
  - 1. Select the field you want to change.
  - 2. Enter the numeric value.
  - 3. Select 'Enter'.
  - 4. Select 'Cancel' if you do not want to change the field. Or wait until the entry screen times out and returns to the Time Date Settings screen.
- o If invalid values are entered, the software will default to the maximum valid value for the field.
- Once set, an on-board super capacitor will back-up the time and date for several days (typically) while the unit is powered off.
- Select 'Back' to return to the Settings Menu.



#### Temp Graph

The temp graph demonstrates the use of emWin and interfacing with the on-board temperature sensor and real time clock (RTC) to draw a graph of the device's temperature and the humidity level within the room, as well as show the time and date.

#### Test App

The test application is a simple GUI app for testing various uEZ GUI features, such as the SD card's storage using a slideshow, reading, and writing to a file, and the touchscreen.

#### b) uEZ Demo

#### Slideshow

Selecting the film reel icon demonstrates the uEZGUI's capability to display a series of slides from different subfolders. The process for creating a slideshow is detailed in section 8 below.

### Brightness

Selecting the brightness icon under Settings shows a menu with color bars and a brightness adjustment slider.

- Move the slider up or down to adjust the brightness.
- Select 'Back' to return to the Settings Menu.

### • Time/Date

Located under Apps, use this application to set the time and date from the internal real time clock (RTC). This is the time and date displayed in the Settings menu upper right corner.

- To change the time or date simply select the field you want to change:
  - 1. Enter the numeric value
  - Select 'Enter'
  - 3. Select 'Cancel' if you do not want to change the field, or simply wait until the entry screen times out and returns to the Time Date Settings screen
- If invalid values are entered, the software will default to the maximum valid value for the field.
- Once set an on-board super capacitor will back-up the time and date for several days (typically) while the unit is powered off.
- Select 'Back' to return to the Settings Menu.

#### Video Player

This feature demonstrates the uEZ GUI's capability to play videos. The process for creating a video for a uEZGUI is detailed below in section 9.

#### emWin Demo

emWin is software used by uEZGUI for creating UI elements such as buttons, icons, etc.

This is a short, automated demo that gives the user a short example of the various GUI applications that emWin software can be used for



# 8. Setting up a Slideshow

We have created a document and included tools for creating your own slideshows for  $\mu EZ$  GUI units. Production module units require 24-bit uncompressed Targa (.TGA) format for all images. Development kit units require bitmap (.BMP) format for all slideshow images.

https://www.teamfdi.com/wp-content/uploads/Converting-a-PowerPoint-Slideshow-for-use-on-uEZGUI-Products\_zip

This guide also covers making speaker notes that can play alongside your slides.

# 9. Setting up a Video

Suggested procedures on creating videos for playback using the  $\mu$ EZ video player provided in  $\mu$ EZ v2.13.100 (and later) on supported  $\mu$ EZ GUI hardware, is available in the FDI Video Creation Guide (included in uEZ). This guide also suggests basic procedures for downloading videos from YouTube. This guide assumes that a source video is available in AVI uncompressed format with a resolution of 480×272 or greater.

For this uEZGUI up to 800x480 full-screen video running at 8 FPS is supported from the microSD card. For lower resolutions, 15-24 FPS video is typically supported. A lower resolution video can be displayed on a white or black screen or even a background frame, so for example a 20 FPS boot-up animated logo could be played centered on a black background.

# 10. µEZ Doxygen online HTML documentation

μΕΖ has built-in comment documentation that follows the Doxygen comment standard. This standard writes code comments and annotations in a certain manner so that it is compiled along with source code into HTML documentation.

FDI provides pre-compiled HTML documentation at this link: https://www.teamfdi.com/uez/docs/

In the  $\mu$ EZ source code, "uEZ/uEZDoxyfile" is the main project file for the Doxygen generator. When Doxygen is recompiled, the new Doxygen files will be found here: uEZ\Docs\ Doxygen\_Documentation.html

FDI updates the documentation periodically. We recommend reviewing your device for updates to the documentation as you learn a new  $\mu EZ^{\otimes}$  release or drivers are added to the system.

For more information, see the Doxygen website: https://www.stack.nl/~dimitri/doxygen/

# 11. µEZ Bootloader

The uEZ bootloader allows users to install their own applications without the need for special tools like J-Link. All that is needed is an SD card or USB drive. The bootloader consists of two primary components: The Base Bootloader (BBL), which contains a base set of functions and never changes, and the Application Bootloader (ABL) that contains the intelligence needed to program internal and external flash memory and can be updated dynamically since it is loaded from local memory such as an SD card or USB drive.



Each time the unit is powered on or reset, the BBL will look to see if an application is already installed, and if so, control is passed to the ABL which then launches the application. If no application is found, the BBL will search for the necessary files on an SD Card or USB drive, and if found, installs the ABL before passing control to it so the application may be installed and booted. If no application and no SD card/USB drive are found, the user is prompted to install an SD card/USB drive with the appropriate files.

The demo kit comes with an SD card on which are several demos for the user to try out. Out of box, the uEZGUI comes preinstalled with a unique 'bootloader application' that allows the user to select one of these demos to install.

The uEZ bootloader can be configured using the file named INSTALL.INI located in one of the demo applications' folders. When "rename=true", and after an application is installed, the file INSTALL.INI in the root of the SD card is renamed to INSTALL.FIN, which when viewed by the uEZ bootloader, will indicate that the application installation is final. At this point, every time the uEZGUI is powered on or reset, the bootloader will boot to the installed application.

This can be undone by renaming the INSTALL.FIN file back to INSTALL.INI. Doing so will allow the uEZ bootloader to boot back to the demo selection screen and permit the user to install a different application.

# 12. μEZ Project Maker

FDI has provided a project maker to help create new projects for  $\mu$ EZ GUI hardware. It is available for download at <a href="https://sourceforge.net/projects/uez/">https://sourceforge.net/projects/uez/</a>.

To create a new project, run the executable file included with uEZ, and follow the onscreen instructions. It will create a demo project using an emWin example GUI that will provide basic peripheral functionality. The project maker speeds up the development process for new applications. Example projects are ready to be compiled and programmed onto  $\mu$ EZ GUI hardware using the included J-Link debugger, with no extra project configuration necessary.

For this uEZGUI the Crossworks project option will generate a second project for the secondary M0 core. Both projects should be opened and built in Crossworks. Then the primary project (for the M4 core) will automatically program the secondary project's application. Using the secondary project, the J-Link can be connected to the second core.

Note: The M0 core project now has full FreeRTOS, SEGGER RTT/SystemView, and uEZ API support.

**Note:** When connected to the M0 core, the J-Link cannot program the LPC4357's flash banks. This is why the M4 project automatically programs the M0 project's output. The option "Additional Load File[0]" enables this programming and can be easily removed if the M0 core is not used.

**Note:** The second core must be enabled in the shared Config Build.h file, otherwise it will not be started.

**Note:** The Crossworks projects use variables to subtract RAM or Flash from the M4 core to assign to the M0 core. By default, the last 64KB of each 512KB Flash bank (128KB total), the entirety of SRAM2, and the last 512KB of SDRAM is assigned. See the following "Memory Map Macros" for typical application configuration: SDRAM\_CM0\_SIZE, FLASHA\_CM0\_SIZE, FLASHB\_CM0\_SIZE, SPIFI\_CM0\_SIZE. When changing a macro size, make sure to use the same size in both \*.hzp project files to prevent overlap. If SRAM banks were swapped between cores, assembly startup code will need to be modified. uEZ\_Platform\_Start\_Additonal\_Cores() needs to be updated to match the starting flash address if changed.

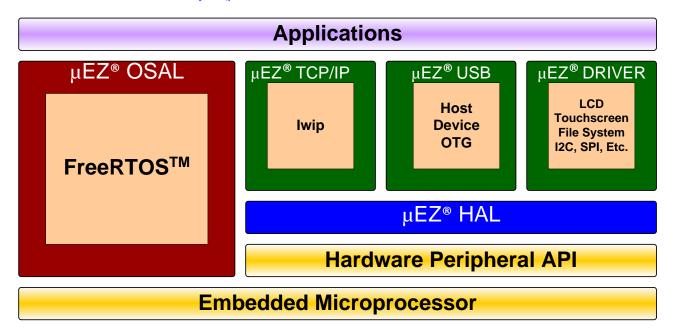


### 13. Software

**μEZ®** takes its name from the Muses of Greek mythology. A Muse was a goddess who inspired the creation process for the arts and sciences. Like its ancient Greek namesake, the **μEZ®** platform inspires rapid development by supplying customers with an extensive library of open-source software, drivers, and processor support - all under a common framework. **μEZ®** development works on the premise of "design once, reuse many times". This provides an open-source standard for embedded developers to build upon and support. **μEZ®** allows companies to focus on innovation and on their own value-added applications while minimizing development time and maximizing software reuse.

The diagram below shows a typical embedded application stack.  $\mu EZ^{\otimes}$  has three primary categories of components that help simplify embedded application development:

- 1. Operating System Abstraction Layer (μEZ® OSAL)
- 2. Sub-system drivers (µEZ® TCP/IP, µEZ® USB, µEZ® Driver)
- 3. Hardware Abstraction Layer (µEZ® HAL)



The selection of an RTOS can be one of the most daunting aspects of an embedded system development. With  $\mu EZ^{\otimes}$  the primary features of common multi-tasking operating systems are abstracted, thus easing the transition to an open source or low-cost RTOS. The  $\mu EZ^{\otimes}$  OSAL provides applications access to the following features in an OS-independent fashion:

- Pre-emptive multitasking
- Stack overflow detection
- Unlimited number of tasks

- Queues
- Semaphores (binary, counting, mutex)

The **µEZ®** sub-system drivers utilize the OSAL functions to provide protected access to the processor peripherals. The sub-system driver API functions are typically protocol layer interfaces (TCP/IP, USB, etc.) designed as high-level access routines such as open, close, read, write, etc. where possible.



**μΕΖ**<sup>®</sup> is ideally suited for Embedded Systems with standard features such as:

- Processor and Platform BSPs (Board Support Packages)
- Real Time Operating System (RTOS)
- Memory Management
- NAND/NOR Flash
- SDRAM and DDR Memory

- TCP/IP stack
- USB Device/Host Libraries
- Mass Storage Devices
- LCD Displays with Touch Screen
- Input / Output Devices

# 14. Code Read Protection (CRP) in LPC Devices

### WARNING: Code Read Protection (CRP) in LPC Devices

NXP LPC MCUs can use CRP to protect your Intellectual Property (IP) when it is in the MCUs memory. If you wish to use CRP, please refer to NXP's application notes, User's Manuals, or the community forums.

CRP is controlled by values written into specific memory locations of the MCU. When CRP is not used the CRP memory locations are all 0xFF's (not protected). All FDI projects are written utilizing no CRP.

Memory section .crp1 and when needed .crp2 are each setup with 4 bytes, initialized to 0xFFFFFFFF (no CRP). We do not recommend changing the memory sections in our product demos as they are set up for the available memory of the uEZGUI. As an example, please review the Memory Usage Map and Memory placement section in uEZGUI-4357-70WVN.c.

Important Note – You should take particular care when modifying the value placed in the CRP word, as some CRP settings can disable some or all means of access to your MCU (including debug). Before making use of CRP, you are strongly advised to read NXP's documentation on this functionality. You can access the documentation for your MCU at Microcontrollers and Processors | NXP

See "LPC Code Read Protection Readme.pdf" for important details and examples.

# 15. Configuring Micro SD Card for High Clock Frequency

The LPC4357 can run the High-Speed SD Card Interface at a much higher speed, and the software is configured by default to do so. The cards should be of good quality and have at least a class 10 rating. Below are some examples of cards that FDI has tested and qualified.

- SanDisk Industrial 8GB+ U1 rated microSDHC
- Kingston SDC10G2/16GBSP
- Kingston SDCA10/16GBSP

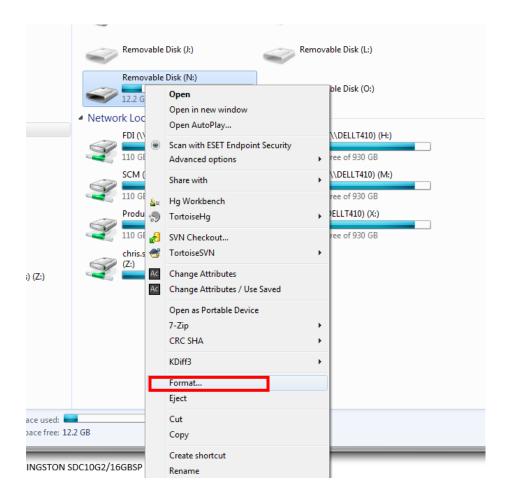
**Note:** At this time this uEZGUI does NOT support full-size SD cards rated for only 25MHz mode. All SDA compliant microSD cards should be compatible with the current fixed-speed implementation.



### **SD Card Formatting**

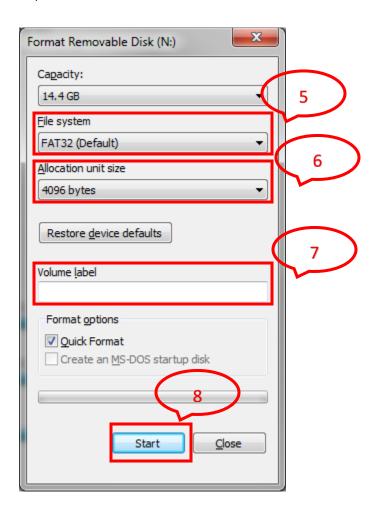
For the best results the SD card should be formatted in a particular way, this is especially important when playing videos from the unit. Follow the steps below on a Windows based PC to format the SD Card.

- 1. Insert the card into the PC.
- 2. Open My Computer.
- 3. Right click on the Removable Disk.
- 4. Select Format from the menu.



- 5. Change the File System to "Fat32" if not already selected.
- 6. Change Allocation unit size to "4096 bytes"
- 7. Enter a volume label if desired (optional)
- 8. Click <Start>.





9. Before Clicking <OK> in the warning dialog box be sure the card does not have importuning information on it.



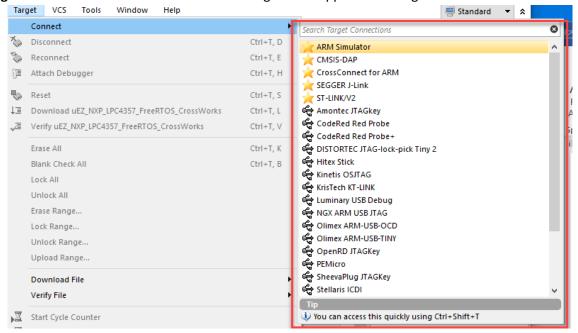
10. Click <OK> on the format complete dialog box.

The SD Card is now ready for use with the uEZGUI-4357-70WVN.



# 16. Configuring Rowley CrossWorks for ARM® for J-Link Flashing

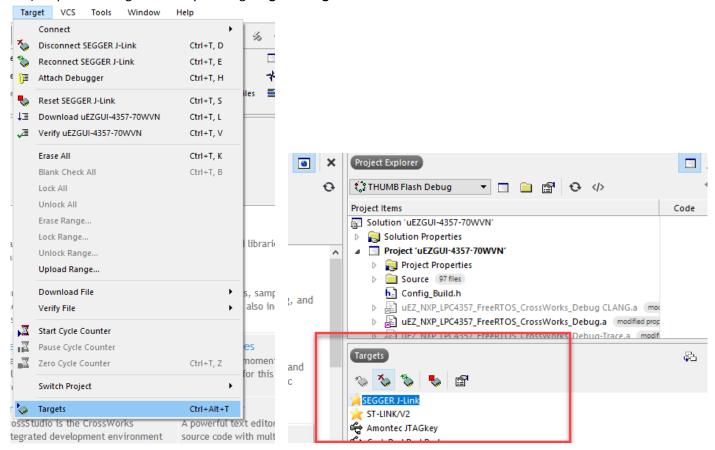
- 1) See the document " $\mu EZ^{\otimes}$  Software Quick Start Guide" for details on how to download the  $\mu EZ^{\otimes}$  source code and setup the Rowley CrossWorks compiler.
  - (https://www.teamfdi.com/products/uez/manual/MA00015%20uEZ%20Software%20Quickstart%20Guide.pdf) software quick start guide
- 2) Plug in the J-Link device into the PC and install any drivers as directed. The SEGGER J-Link drivers can be found at <a href="https://www.SEGGER.com/cms/jlink-software.html">https://www.SEGGER.com/cms/jlink-software.html</a> with additional information at <a href="https://www.SEGGER.com/cms/development-tools.html">https://www.SEGGER.com/cms/development-tools.html</a>.
- 3) Plug in the J-Link's JTAG connector to the μEZ GUI board at J1 with the JTAG adapter.
- 4) Select **Target** menu and choose **Connect**. The following list will appear to the right.



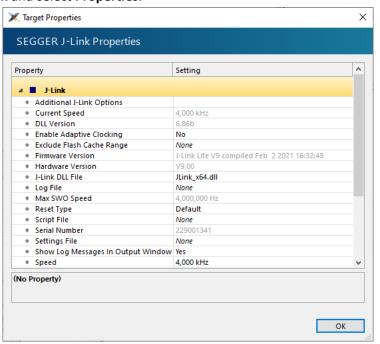
5) Click on "SEGGER J-Link".



6) Open the Targets view by clicking Target > Targets



7) Right click on SEGGER J-Link and select Properties.

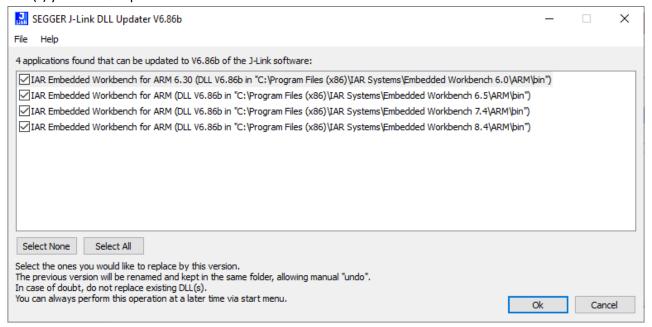




- 8) If programming a blank LPC4357 part, select a Speed of 100 kHz. If the part has already been programmed, select a Speed up to 50,000 kHz.
- 9) Press OK.

# 17. Configuring IAR EWARM v9.30.1 for J-Link Flashing

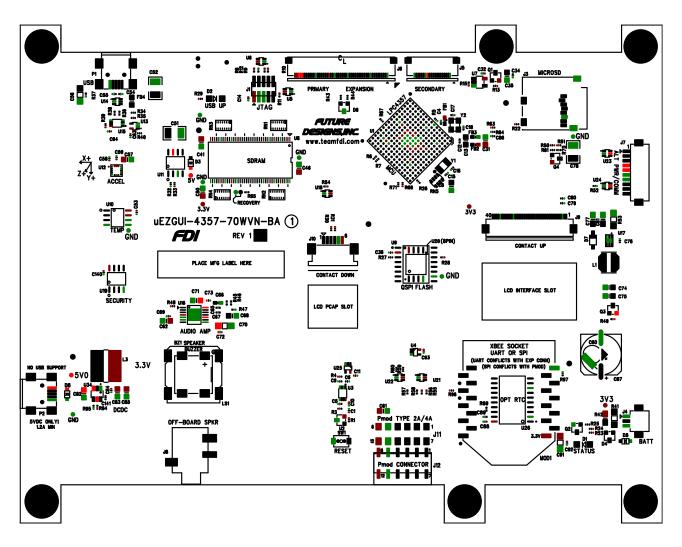
The IAR tools do not require any special configuration for configuring the J-Link tools. To update IAR's J-Link dll you just need to run SEGGER's J-Link DLL Updater (located in the SEGGER directory under Program Files) and select the IAR installation(s) you wish to update and click Ok.





# 18. Board Layout

The following figure illustrates the layout of the various components of the UEZGUI-4357-70WVN kit. They are for reference only and are subject to change.





#### Figure 2 – UEZGUI-4357-70WVN-BA Component View

# 19. I/O Connector Descriptions

#### J1 - JTAG Connector

The UEZGUI-4357-70WVN-BA uses a standard Cortex-M 9-pin 0.050" Header. This smaller connector provides 100% of the functionality of the standard 20-pin JTAG connector but utilizes 70% less board space. The connector is a standard Samtec FTSH part (defined by ARM) available from most major vendors.

Pin Number	Description	Pin Number	Description
1	3.3V	2	TMS
3	Ground	4	TCK
5	Ground	6	TDO
7	No Connect	8	TDI
9	No Connect	10	RESETn

The connector part number is PN: SAMTEC FTSH-105-01. Kits come with the SEGGER J-Link Lite Cortex-M shown below, and no adapter will be needed.



Figure 3 – SEGGER J-Link Lite Cortex-M 9-Pin JTAG with pinout (Rev 2.1+)

SEGGER and OLIMEX both provide adapters to convert the standard 20-pin ARM JTAG to the Cortex-M 9-pin JTAG. The SEGGER adapter also allows for connecting TRST using a solder bridge if needed. These adapters have female pins and are only compatible with 20-pin JTAG units that have male pins. Both adapters come with the required cable.

- SEGGER 9-pin adapter https://www.SEGGER.com/jlink-adapters.html#CM 9pin
- OLIMEX 9-pin adapter https://www.olimex.com/Products/ARM/JTAG/ARM-JTAG-20-10/
  - o Note: by default, pin 9 TRST is connected.



### J2 - Tag Connect

The UEZGUI-4357-70WVN-BA also includes the ability to JTAG and program using the Tag-Connect TC2050-ARM2010 ARM 20-pin to TC2050 Adapter.

- Adapter: <a href="https://www.tag-connect.com/TC2050-ARM2010">https://www.tag-connect.com/TC2050-ARM2010</a>
- Cable with legs: <a href="https://www.tag-connect.com/TC2050-IDC">https://www.tag-connect.com/TC2050-IDC</a> <- Note: This cable will NOT fit.
- Cable with no legs: <a href="https://www.tag-connect.com/TC2050-IDC-NL">https://www.tag-connect.com/TC2050-IDC-NL</a>
- Holding clip for no-legs cable version: https://www.tag-connect.com/TC2050-CLIP

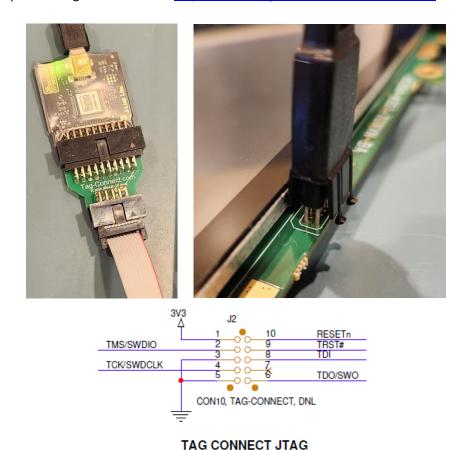


Figure 4 – Tag-Connect JTAG adapter, cables, and pinout for 10-pin TC2050



### J3 - MicroSD Socket

The unit uses a MicroSD Socket for flexible mass storage capability. µEZ™ supports MicroSDHC and SDHC Cards up to at least 64GB. This interface uses 4-wire SD mode at up to 51MHz.

Pin Number	Description
1	Micro SD DAT2
2	Micro SD DAT3
3	Micro SD CMD
4	3.3V
5	Micro SD SCLK
6	Ground
7	Micro SD DAT0
8	Micro SD DAT1
9	Micro SD CD
10	Micro SD CD
11	Ground
12	Ground

WARNING: The microSD card must only be removed using the spring loaded "push-pull" mechanism on the microSD socket. Improper forceful removal of the microSD card will result in permanent damage to the socket that is <u>not covered under warranty</u>. To insert the card, just push it into the socket until a "click" sound is heard.

Start with the microSD card in this position relative to the microSD slot with the text and "lip" facing up.



Use your figure to gently push the card into the socket until it clicks.



Next gently insert the card partially into the socket.



At this point the microSD card is fully inserted. It should not fall out, even if the unit is shaken vigorously.



To remove the microSD card, press the card back into the socket until another "click" sound is heard, then release pressure on the card. At this point, the card should be partially ejected from the socket. Finally grab the card and gently pull to remove it. See the following pictures for proper micro-SD removal:



To remove the microSD card, gently push it into the socket again until it "clicks", and then release your figure.



Carefully grab the edges or sides of the microSD card and gently pull it out.



At this point, the microSD card should partially stick out of the socket.



At this point the microSD card is fully removed from the socket.



### J7 - Alternate Power and communication



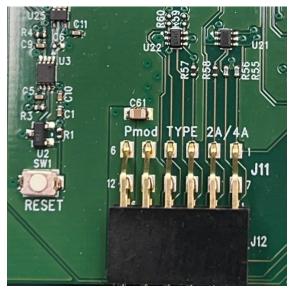
The UEZGUI-4357-70WVN-BA includes an alternate power and communication header. This header provides access to UART 3, I2C Channel 1, SPI Channel 0, GPIO, Counter/Timer Input/Output, as well as a 5V power input. This connector is a 1.25mm Hirose male, shrouded connector. The Hirose Part Number is: DF13A-10P-1.25H (20) (Digikey PN: H3375-ND). Refer to the schematic for specific connectivity.

Pin Number	Description	Pin Number	Description
1	GPIO4.13/MISO0	6	Ground
2	GPIO4.14/MOSI0	7	Ground
3	SSPOSCK (1)	8	GPIO5.4/RXD3/SCL1/CTIN0
4	5V Input	9	GPIO5.3/TXD3/SDA1/CTIN0
5	5V Input	10	GPIO2.4/DAC/CTOUT2

Note (1) – this signal is not GPIO capable



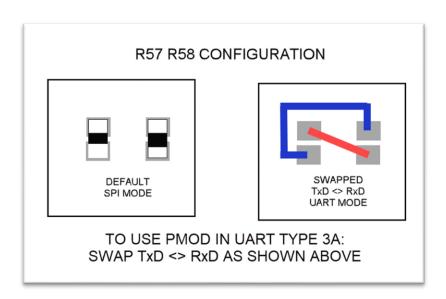
### J11 - PMOD Type 2A Connector



The UEZGUI-4357-70WVN-BA includes a PMOD Type 2A (SPI) connection to an expansion board. Refer to the schematic for specific connectivity.

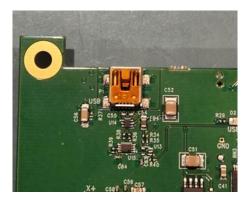
Pin Number	Description	Pin Number	Description
1	SPI Chip Select	7	Interrupt Request
2	SPI MOSI	8	Reset
3	SPI MISO	9	Optional I2C SCL (1)
4	SPI Clock	10	Optional I2C SDA (1)
5	Ground	11	Ground
6	3.3V	12	3.3V

- Note (1) These signals are not available in the standard load configuration. Two nearby resistor footprints must be populated with 0-ohm resistors to connect I2C (see schematic for details).
- Note (2) Although the initial version of the Digilent spec documented PMOD Type 4A, subsequent versions of the PMOD spec do not show Type 4A, but instead show Type 3A.
- Note (3) The uEZGUI default configuration is for PMOD Type 2A (SPI). To use PMOD Type 3A (UART), two resistors must be reconfigured to set TXD & RXD as shown below.





### P1 – USB mini-AB port



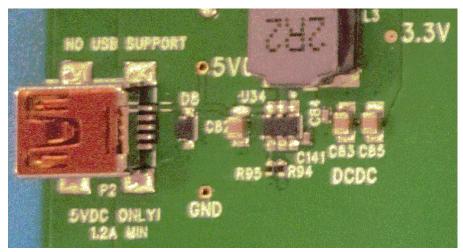
The UEZGUI-4357-70WVN-BA has a mini-AB USB connector for Host or Device mode. By using a USB OTG adapter (with a Mini-A plug) it will short the ID pin 4 to ground. This can be used for Host mode detection in the application.

Pin Number	Description
1	5V
2	D-
3	D+
4	ID
5	Ground

**NOTE:** Do not connect USB Device port to a powered host if the uEZGUI is not powered.



### P2 – USB Power Input 5VDC



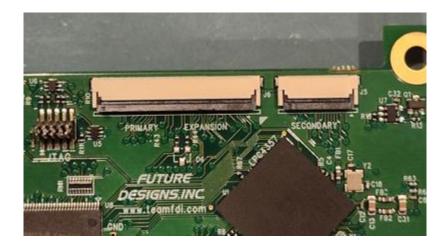
The UEZGUI-4357-70WVN-BA is normally powered via P2 (with the included 5V USB Wall Supply) or via J7. This power supply is only included in the development kit and is not included with the UEZGUI-4357-70WVN -BA.

Pin Number	Description
1	5V
2	NC
3	NC
4	NC
5	Ground

**NOTE:** Do not connect P2 to a data USB port. It is designed for 5V power input only.



### J5 & J6 - Expansion Connectors



The UEZGUI-4357-70WVN-BA includes two expansion connectors that provide a wide variety of capabilities for user expansion, ranging from 10/100 Ethernet to USB Host, etc. **Please Note:** When using I/O signals on the  $\mu$ EZ GUI Expansion Connectors (J5 & J6) to connect via the customers Expansion Board to external connectors or signals, *it is the customer's responsibility to provide adequate ESD protection and filtering to prevent damage to any pins that are not directly protected on the \muEZ GUI. Any damage caused by improper connectivity is not covered under warranty.* 

The tables below provide the pinout and signal names available on these connectors:

## **J6 Signal Details**

Pin	Pin Name	Pin Description	
1	Ground (GND)		Power
	GPIO5.4/I2C1_SCL/RXD3/CTIN0/	GPIO5.4 – General Purpose I/O	I/O
		I2C1_SCL – I2C Channel 1 Serial Clock	I/O
2		RXD3 – UART Channel 3 Receive Data	1
	T3_MAT1	CTIN0 – SCT Input 0 of timer 0, 1, 2, 3	1
		T3_MAT1 – Match Output 1 of timer 3	0
		GPIO5.3 – General Purpose I/O	I/O
	GPIO5.3/I2C1_SDA/TXD3/CTIN1/ T3_MAT0	I2C1_SDA – I2C Channel 1 Serial Data	I/O
3		TXD3 – UART Channel 3 Transmit Data	0
		CTIN1 – SCT Input 1 of timer 0, 1, 2, 3	1
		T3_MAT0 – Match Output 0 of timer 3	0
4	GPIO4.12/SSP0 SSEL	GPIO4.12 – General Purpose I/O	I/O
4	GF104.12/33F0_33EL	SSPO_SSEL – SPI Channel 0 Slave Select	I/O
5	CDIO2 9/CT OUT14	GPIO3.8 – General Purpose I/O	I/O
3	GPIO3.8/CT_OUT14	CT_OUT14 – SCT output 14, Match output 2 of timer 3	0
	GPIO6.2/RTS1/ADC1.0	GPIO6.2 – General Purpose I/O	I/O
6		RTS1 – UART Channel 1 Request to Send	0
		ADC1.0 – ADC Channel 1 input 0	1
7	CDIOS 1/CTS1	GPIO6.1 – General Purpose I/O	I/O
/	GPIO6.1/CTS1	CTS1 – UART Channel 1 Clear to Send Input	1
8	GPIO6.13/RXD1	GPIO6.13 – General Purpose I/O	I/O



Pin	Pin Name	Pin Description	
	riii Naiile	1	
		RXD1 – UART Channel 1 Receive Data	1
9	GPIO6.12/TXD1	GPIO6.12 – General Purpose I/O	I/O O
10	Ground (GND)	TXD1 – UART Channel 1 Transmit Data	
10	Ground (GND)		Power
11	USB1_DM	USB_D- 1 - USB port 1 bidirectional D- line.	I/O
12	USB1_DP	USB_D+1 - USB port 1 bidirectional D+ line.	I/O
		GPIO5.5 – General Purpose I/O	1/0
13	GPIO5.5/CTIN2/T3 MAT2/USB1 VBUS	CTIN2 – SCT input 2. Capture input 2 of timer 0	I
	0. 100.0, 012, 10_12, 00.0100	T3_MAT2 – Match output 2 of timer 3	0
		USB1_VBUS – USB Channel 1 VBus input	1
		GPIO4.11 – General Purpose I/O	1/0
14	GPIO4.11/MCOB1/USB1H_OVC/RXD0	MCOB1 – Motor Control PWM Channel 1 output B	0
	_ , , , , , , , , , , , , , , , , , , ,	USB1H_OVC – USB Channel 1 Power Fault (same as USB1_PWR_FAULT)	I
		RXD0 – UART Channel 0 Receive Data	ı
		GPIO5.18 – General Purpose I/O	1/0
15	GPIO5.18/MCOA1/USB1H_PPWR/TXD0	MCOA1 – Motor Control PWM Channel 1 output A	0
	_ , , , , , , , , , , , , , , , , , , ,	USB1H_PPWR – USB Channel 1 Host VBus Drive Signal	0
		TXD0 – UART Channel 0 Transmit Data	0
		GPIO4.14 – General Purpose I/O	1/0
16	GPIO4.14/MCOB2/I2STX_SDA/MOSI0	MCOB2 – Motor Control PWM Channel 2 output B	0
		I2STX_SDA – I2S Audio Transmit Serial Data	1/0
		MOSI – SPI Channel 0 Master Output, Slave Input Serial Data	1/0
		GPIO4.13 – General Purpose I/O	1/0
17	GPIO4.13/MCOA2/I2STX_WS/MISO0	MCOA2 – Motor Control PWM Channel 0 output A	0
		I2STX_WS – I2S Audio Transmit Word Select	I/O I/O
		MISO0 – SPI Channel 0 Master Input, Slave Output Serial Data	1/0
18	I2STX_SCK/SSP0SCK	Connected to pin F13 of LPC4357 BGA (P3_0)  I2STX_SCK — I2S Audio Transmit Clock	1/0
10	1231A_3CR/33F03CR	SSPOSCK – SPI Channel O Serial Clock	1/0
		GPIO5.9 – General Purpose I/O	1/0
19	GPIO5.9/I2SRX_SDA/CANOTD	12SRX_SDA – 12S Audio Receive Serial Data	1/0
13	01103.3/1231W_3BA/CAN01B	CANOTD – CAN Channel 0 Transmit Data	0
		GPIO5.8 – General Purpose I/O	1/0
20	GPIO5.8/I2SRX_WS/CANORD	I2SRX_WS – I2S Audio Receive Word Select	1
		CANORD – CAN Port 0 Receive Data	i
		Connected to pin M12 of LPC4357 BGA (P6_0)	
21	I2SRX_SCK	I2SRX_SCK – I2S Audio Receive Clock	1
22	Ground (GND)		Power
		External reset input: A LOW on this pin resets the device, causing I/O ports and peripherals to	
23	RESET_INn	take on their default states, and processor execution to begin at address 0.	I
		TTL with hysteresis, 5 V tolerant	
24	RESET_OUTn	RSTOUT - This is a 3.3 V pin. LOW on this pin indicates LPC4357 being in Reset state	0
		GPIO2.4 – General Purpose I/O	1/0
25	GPIO2.4/DAC/CTOUT2	DAC – DAC Output	0
		CTOUT2 – SCT Output 2, Match output 2 of timer 0	0
2.5	CD102 4 / 200 4 / 5-7:	GPIO2.1 – General Purpose I/O	1/0
26	GPIO2.1/ADC0.1/CTOUT1	ADC0.1 – ADC0 & ADC1, input channel 1	I
		CTOUT1 – SCT Output 1, Match output 3 of timer 3	0
	00100 40/511 11-15/11	GPIO0.12 – General Purpose I/O	1/0
27	GPIO0.12/ENET_MDIO/U2_UCLK/	ENET_MDIO – Ethernet MIIM data input and output	1/0
	T0_CAP3/CAN1_TD	U2_UCLK – UART Channel 2 Serial Clock input/output	1/0
		TO_CAP3 – Capture input 3 of timer 0	



Pin	Pin Name	Pin Description	·
		CAN1_TD – CAN Channel 1 Transmit Data	0
		GPIO6.0 – General Purpose I/O	I/O
		ENET_MDC – Ethernet MIIM Clock	0
28	GPIO6.0/ENET_MDC/T3_CAP0/U1_RI	T3 CAPO – Capture input 0 of timer 3	ı
		U1_RI – UART Channel 1 Ring Indicator	1
29	Ground (GND)		Power
		ENET_REFCLK – Ethernet RMII Reference Clock	1
30	ENET_REFCLK/SSP1_SCK	SSP1 SCK – SPI Channel 1 Serial Clock	I/O
		ENET RX ER – Ethernet Receive Error (MII Interface)	·
31	GPIO6.8/ENET_RX_ER/T3_MAT2	T3_MAT2 – Match Output 2 of Timer 3	0
32	3v3 volts	3.3 volts	Power
		GPIO0.0 – General Purpose I/O	I/O
33	GPIO0.0/ENET_RXD1/SSPI1_MISO	ENET RXD1 – Ethernet RMII Receive Data 1	1
		GPIO0.2 – General Purpose I/O	I/O
	GPIO0.2/ENET_RXD0/U2_TXD/	ENET RXD0 – Ethernet RMII Receive Data 0	1
34	T0_MAT1	U2_TXD – UART Channel 2 Transmit Data	0
		TO MAT1 – Match output 1 of timer 0	0
		GPIO0.3 – General Purpose I/O	1/0
	GPIO0.3/ENET_CRSDV/U2_RXD/	ENET CRSDV – Ethernet RMII Data Valid	1,0
35	T0 MAT0	U2 RXD – UART Channel 2 Receive Data	
	-	TO_MATO – Match output 0 of timer 0	0
		GPIO0.1 – General Purpose I/O	1/0
36	GPIO0.1/ENET_TXEN/SSP1_MOSI	ENET_TXEN — Ethernet RMII Transmit Enable	0
30	GI 100.1/ENET_TAEN/331 1_WOSI	SSP1_MOSI – SPI Channel 1 Master Output/Slave Input	1/0
		GPIO0.15 – General Purpose I/O	1/0
	37 GPIO0.15/ENET_TXD1/SSP1_SEL/ T0_CAP2	ENET TXD1 – Ethernet RMII Transmit Data 1	0
37		SSP1 SEL – SPI Channel 1 Slave Select	1/0
	10_CA12	TO_CAP2 – Capture input 2 of timer 0	1,0
		GPIO0.13 – General Purpose I/O	1/0
		ENET_TXD0 — Ethernet RMII Transmit Data 0	0
38	GPIO0.13/ENET_TXD0/U2_DIR/	U2_DIR – UART Channel 2 RS485 putout enable/direction control	1/0
30	T0_MAT3/CAN1_RD	TO_MAT3 – Match output 3 of timer 0	0
		CAN1_RD – CAN Channel 1 Receive Data	1
39	Ground (GND)	CANT_ND CAN Chailler I Neceive Data	Power
33	Ground (GND)	GPIO7.4 – General Purpose I/O	I/O
40	GPIO7.4/NMI	NMI – Non-maskable Interrupt	1,70
		GPIO7.25 – General Purpose I/O	1/0
41	GPIO7.25/RXD0	RXD0 – UART Channel 0 Receive Data	1,0
		GPIO7.24 – General Purpose I/O	1/0
42	GPIO7.24/TXD0	TXD0 – UART Channel 0 Transmit Data	0
43	USBD_DP	USB D+2 - USB port 2 bidirectional D+ line	1/0
44	USBD_DM	USB D-2 - USB port 2 bidirectional D- line	1/0
	3322_5//	VBUS - Monitors the presence of USB bus power.	1,70
45	USBD_VBUS	Note: This signal must be HIGH for USB reset to occur.	1
46	5volts (5VO)	5.0 Volts	Power
47	5volts (5vO)	5.0 Volts	Power
48	5volts (5vO)	5.0 Volts	Power
49	3v3 volts (3v3)	3.3 Volts	Power
50	3v3 volts (3v3) 3.3 volts 3.3 volts		Power
30	343 401(2 (242)	3.5 VUILS	rowei



### **J5 Signal Details**

Pin	Pin Name	Pin Description	
1	Ground (GND)	· ··· Description	Power
1	Ground (GND)	GPIO6.30 – General Purpose I/O	I/O
2	GPIO6.30/CTOUT12	CTOUT12 – SCT output 12, Match output 3 of timer 3	0
		GPIO4.10 – General Purpose I/O	1/0
3	GPIO4.10/QEI_PHA	Quadrature Encoder Interface Phase A Input	1/0
		GPIO6.28 – General Purpose I/O	1/0
4	GPIO6.28/CTOUT11	CTOUT11 - SCT output 11. Match output 3 of timer 2.	0
		GPIO6.27 – General Purpose I/O	-
5	GPIO6.27/CTOUT13		0
		CTOUT13 - SCT output 13. Match output 3 of timer 3.	-
6	GPIO6.26/CTOUT10	GPIO6.26 – General Purpose I/O	1/0
		CTOUT10 - SCT output 10. Match output 3 of timer 3.	0
7	GPIO6.25/CTOUT14	GPIO6.25 – General Purpose I/O	1/0
	·	CTOUT14 – SCT Output 14, Match output 2 of timer 3	0
8	GPIO6.24/CTIN1	GPIO6.24 – General Purpose I/O	1/0
	, ,	CTIN1 – SCT Input 1, Capture input 1 of timer 0, Capture input 1 of timer 2	I
		GPIO5.17 – General Purpose I/O	1/0
9	GPIO5.17/MCOB0/RXD3	MCOBO – Motor Control PWM Channel 0, output B	0
		RXD3 – UART Channel 3 Receive Data	1
		GPIO4.15 – General Purpose I/O	I/O
10	GPIO4.15/MCOA0/TXD3	MCOA0 – Motor Control PWM Channel 0, output A	0
		TXD3 – UART Channel 3 Transmit Data	0
		GPIO4.9 – General Purpose I/O	1/0
11	GPIO4.9/QEI_PHB/RXD2	Quadrature Encoder Interface Phase B Input	I
		RXD2 – UART Channel 2 Receive Data	I
		GPIO4.8 – General Purpose I/O	I/O
12	GPIO4.8/QEI_IDX/TXD2	Quadrature Encoder Interface INDEX Input	ı
		TXD2 – UART Channel 2 Transmit Data	0
13	5volts(5VO)	5.0 Volts	Power
14	Ground (GND)		Power
	, ,	GPIO3.13 – General Purpose I/O	1/0
15	GPIO3.13/CTOUT12/ADC0.3	CTOUT12 – SCT output 12, Match output 0 of timer 3	0
	, , , , , , , , , , , , , , , , , , , ,	ADC0.3 – ADC0 Channel 3 input	
		GPIO3.12 – General Purpose I/O	1/0
16	GPIO3.12/CTOUT13/ADC0.4	CTOUT13 – SCT output 13, Match output 1 of timer 3	0
	0.100.127, 0.00.129, 7.000.1	ADC0.4 – ADC0 Channel 4 input	1
		GPIO7.22 – General Purpose I/O	1/0
17	GPIO7 22/ADCO 2/HO CLK/CTIN2	ADC0.2 – ADC0 Channel 2 input	1/0
17	GPIO7.22/ADC0.2/U0_CLK/CTIN2	UO CLK – UART Channel O Synchronous Clock I/O	1/0
		GPIO7.20 – General Purpose I/O	1/0
		ADC1.3 – ADC1 Channel 3 input	1/0
18	GPIO7.20/ADC1.3/U3_DIR/SSP1_MISO	U3 DIR – UART Channel 3 RS485 output enable/direction	1/0
	, , , , , , , , , , , , , , , , , , , ,	<u> </u>	
		SSP1_MISO – SPI Channel 1 Master Input/Slave Output	1/0
		GPIO7.19 – General Purpose I/O	1/0
19	GPIO7.19/ADC1.4/U3_UCLK/SSP1_SSEL	ADC1.4 – ADC1 Channel 4 input	- !
		U3_CLK – UART Channel 3 Synchronous Clock I/O	1/0
		SSP1_SSEL – SPI Channel 1 Slave Select	1/0
20	Ground (GND)		Power

# J5 & J6 - Expansion Connector Cable Details

The maximum length for the expansion connector cables is as follows: General Purpose IO, TTL, Serial, 6" recommended maximum, 8" absolute maximum



Ethernet, high-speed IO, 3" recommended maximum, 4" absolute maximum

The following table provides example part numbers for the expansion cables:

Description	MFG	MFGPN	Digi-Key PN
3" 20-pin 0.5mm	Molex	21020-0209	WM10226-ND
6" 20-pin 0.5mm	Molex	21020-0215	WM10218-ND
3" 50-pin 0.5mm	Molex	21020-7650	WM10231-ND
6" 50-pin 0.5mm	Molex	21020-0548	WM10223-ND

Note: These lengths are only recommendations. The actual lengths utilized will be dependent on the expansion board circuitry, layouts and general environment of the application. It is up to the customer to test and validate the functional operation and use of the expansion connectors.



# 20. Schematics, Documentation, and Support

Please see the product information tabs at <a href="https://www.teamfdi.com/product-details/uezgui-4357-70wvn">https://www.teamfdi.com/product-details/uezgui-4357-70wvn</a> for support and documentation.

## 21. Temperature Range

UEZGUI-4357-70WVN-BA board w/o LCD: -30°C to +80°C

UEZGUI-4357-70WVN-BA with LCD: -20°C to +70°C

# 22. ESD Warning



The UEZGUI-4357-70WVN-BA kit is shipped in a protective anti-static package. The kit must not be subjected to high electrostatic potential. Damage may occur to the boards that will not be covered under warranty. General practice for working with static sensitive devices should be followed when working with the kit.

# 23. Real Time Clock Backup Time

The μEZ GUI's Real Time Clock is backed up with a Seiko Super Capacitor to allow the time to be preserved when external power is removed. The calculated backup time is shown below.

<b>Super Capacitor</b>	<b>Typical Voltage</b>	<b>Stop Voltage</b>	<b>Maximum Current</b>	Typical Backup Time
XH414HG	3.17 V	2.4 V	10μΑ	~3.5 hrs.

For a longer RTC backup time an off-board RTC located on an expansion board would be preferred.

# 24. Power Requirements

Power is supplied into the mini-USB connector (P2) via the USB cable and power supply provided in the kit. The power supply provides 5VDC output at 2A (min) and has input voltage range of 100-240VAC with standard U.S. 2-prong plug. The following typical power requirements were measured at room temperature with LPC4357 M0 and M4 cores both running, with a SanDisk Industrial microSD card installed:

Voltage	μEZ Demo Screen	<b>Observed Max</b>	Maximum Allowed
5.0V	675 mA	~0.800 A	Up to 1.5A through P2

#### μΕΖ GUI Input Power Requirements:

• +5VDC ±5% is the input power range specification. However, since the 5VDC input has reverse diode protection, it may be necessary to provide a higher input voltage level of 5VDC input to ensure that the μEZ GUI 5VDC output level retains the specified tolerance. If the μEZ GUI input level drops to 4.75VDC, then the μEZ GUI +5VDC output level to either the Expansion Board or the USB Host connector may be lower than 4.75VDC since the worst case drop from input to output is typically 0.25V.



- For reference on the USB output port from the μΕΖ GUI, the following are the specifications:
  - USB High Power Specifications are 500mA maximum, and 4.75V to 5.25V standard.
  - USB Low Power Specifications are 100mA maximum, and 4.4V to 5.25V standard.
  - The μEZ GUI can provide a maximum of 300mA of 3.3V power for "external use" over the expansion connectors. If more than 300mA of 3.3V is needed for an expansion board;
    - $\circ$  Then the primary power input (i.e. 5V) should be located on the expansion board rather than on the  $\mu EZ$  GUI.
    - The expansion board should be designed with a separate 3.3V voltage regulator.
    - Ensure the 3.3V voltage rails of the μΕΖ GUI & Expansion Board are not connected.
    - $\circ$  The  $\mu$ EZ GUI should be powered using 5V from the expansion board over the 70-pin breakout, instead of powering the expansion board from the  $\mu$ EZ GUI unit.
  - The UEZGUI-4357-70WVN may also optionally be powered via the following connectors;
    - Alternate Power/Communication Connector, J7, with a maximum of 2A 5V input
    - o Expansion Connector(s) J5 and/or J6 refer to the expansion connector section for details

Note: For running high current XBEE or Pmod modules a combination of J5, J6, and J7 should be used to power the uEZGUI.

### 25. Mechanical Details

The following illustrations show the mechanical details of the UEZGUI-4357-70WVN-BA PCB.



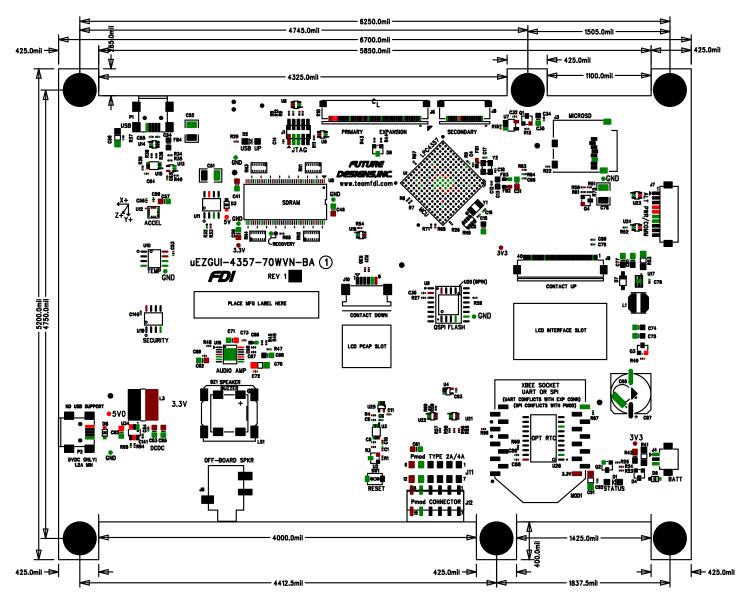


Figure 5 – Mechanical Dimensions (Component View)



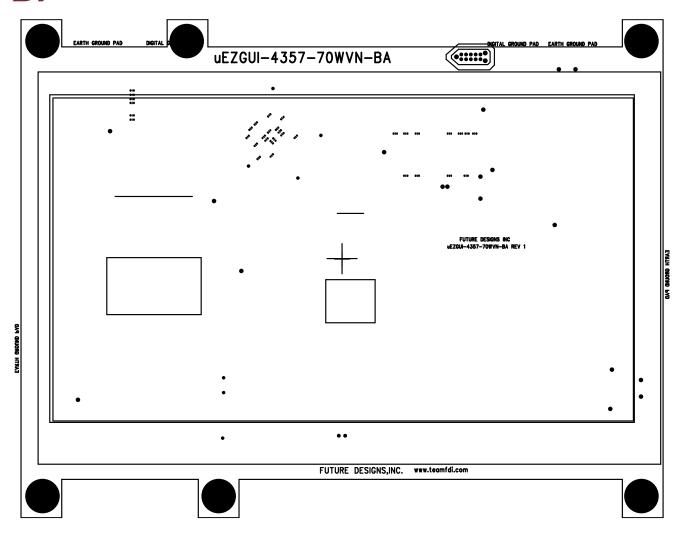


Figure 6 – Mechanical Dimensions (LCD Module View and Side View)

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