

DATASHEET

Technical Data Sheet Top Phototransistor PT67-21C/L606/TR8(DVP-2)

Features

- Fast response time
- High photo sensitivity
- Small junction capacitance
- Compatible with infrared and vapor phase reflow solder process.
- Pb free
- The product itself will remain within RoHS compliant version.



• PT67-21C/L606/TR8(DVP-2) is a high speed silicon NPN epitaxial planar phototransistor in a compact surface-mountable package. It's compatible with automatic placement equipment and can withstand IR reflow, vapor phase reflow, and wave solder processes.

Applications

- Miniature switch
- Counters and sorter
- Position sensor
- Infrared applied system
- Encoder

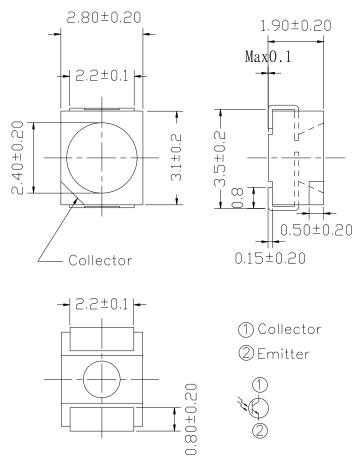
Device Selection Guide

| Device No. | Chip Material | Lens Color | |
|--------------------------|---------------|-------------|--|
| PT67-21C/L606/TR8(DVP-2) | Silicon | Water clear | |





Package Dimensions



Notes: 1.All dimensions are in millimeters 2.Tolerances unless dimensions ± 0.1 mm

Absolute Maximum Ratings (Ta=25°C)

| Parameter | Symbol | Rating | Unit |
|---|------------------|--------------------|------|
| Collector-Emitter Voltage | $V_{\sf CEO}$ | 30 | V |
| Emitter-Collector-Voltage | V _{ECO} | 4 | V |
| Collector Current | Ic | 20 | mA |
| Operating Temperature | T _{opr} | -25 ~ +85 | °C |
| Storage Temperature | T _{stg} | -40 ~ + 100 | °C |
| Soldering Temperature *1 | T _{sol} | 260 | °C |
| Power Dissipation at(or below) 25°CFree Air Temperature | Pc | 75 | mW |

Notes: *1:Soldering time \leq 5 seconds.

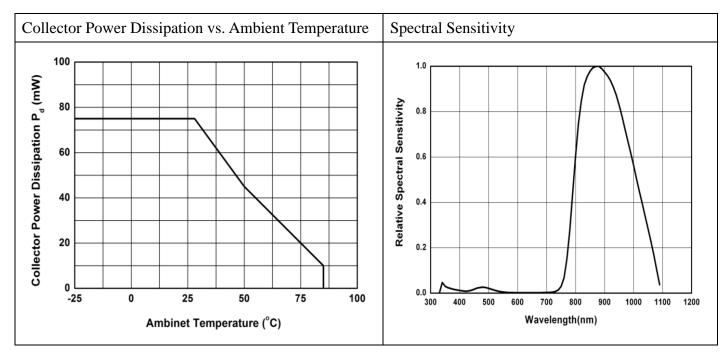


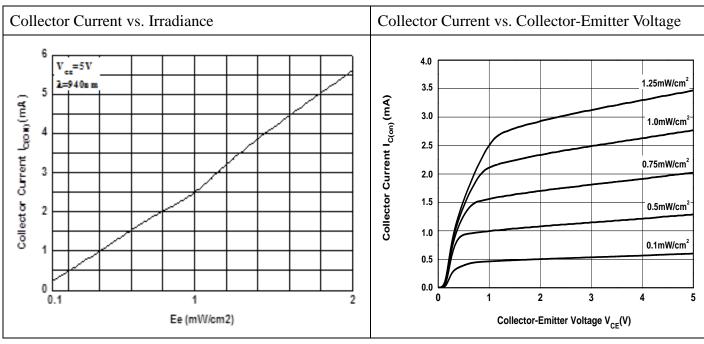
Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

| Parameter | Symbol | Min. | Typ.* | Max. | Unit | Condition |
|---|--------------------------|------|-------|------|------|---|
| Rang Of Spectral Bandwidth | λ _{0.1} | 400 | | 1100 | nm | |
| Wavelength Of Peak Sensitivity | λ _P | - | 940 | | nm | |
| Collector-Emitter Breakdown Voltage | BV _{CEO} | 30 | - | | V | I _C =100μA Ee=0mW/cm ² |
| Emitter-Collector Breakdown Voltage | BV _{ECO} | 5 | | | V | I _C =100μA Ee=0mW/cm ² |
| Collector-Emitter Saturation Voltage | V _{CE(sat)} | | | 0.4 | V | I _C =1mA Ee=1mW/cm ² |
| Collector Dark Current | I _{CEO} | | | 500 | nA | V _{CE} =20V Ee=0mW/cm ² |
| On State Collector Current | I _{C(ON)} | 2.00 | 2.30 | 10.0 | mA | $V_{CE}=5V$ $Ee=1mW/cm^2$ $\lambda_P=940nm$ |
| View Angle | 2θ _{1/2} | | 120 | | Deg. | V _{CE} =5V |
| Rise time | t _r | | 15 | | μS | V _{CE} =5V |
| Fall time | t _f | | 15 | | μS | I_{C} =1mA R_{L} =1000 Ω |

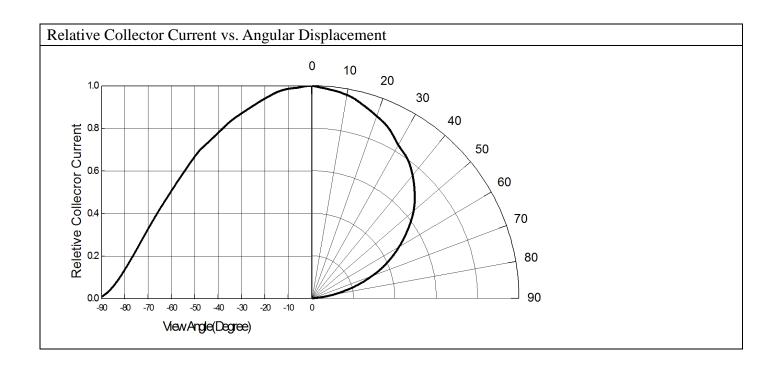


Typical Electrical/Optical/Characteristics Curves











Precautions For Use

1. Over-current-proof

Customer must apply resistors for protection, otherwise slight voltage shift will cause big current change (Burn out will happen).

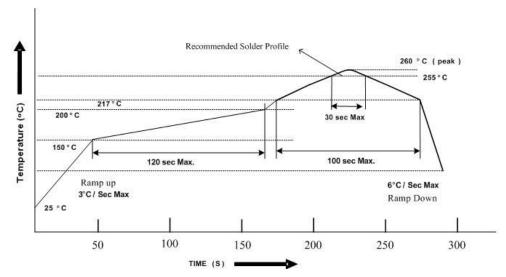
2. Storage

- 2.1 Do not open moisture proof bag before the products are ready to use.
- 2.2 Before opening the package, the Phototransistor should be kept at 30°C or less and 90%RH or less.
- 2.3 The Phototransistor should be used within a year.
- 2.4 After opening the package, the Phototransistor should be kept at 30° C or less and 60%RH or less.
- 2.5 The Phototransistor should be used within 72 hours (3 days) after opening the package
- 2.6 If the moisture absorbent material (silica gel) has faded away or the Phototransistor have exceeded the storage time, baking treatment should be performed using the following conditions.

Baking treatment: 60±5°C for Min. Min. 24 hours.

3. Soldering Condition

3.1 Pb-free solder temperature profile



- 3.2 Reflow soldering should not be done more than two times.
- 3.3 When soldering, do not put stress on the Phototransistor during heating.
- 3.4 After soldering, do not warp the circuit board.

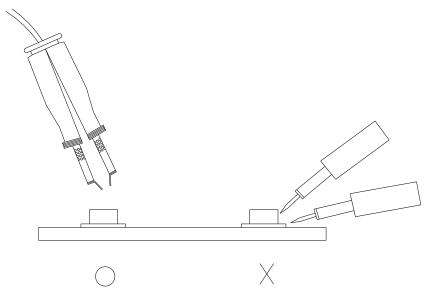


4. Soldering Iron

Each terminal is to go to the tip of soldering iron temperature less than 350°C for 3 seconds within once in less than the soldering iron capacity 25W. Leave two seconds and more intervals, and do soldering of each terminal. Be careful because the damage of the product is often started at the time of the hand solder.

5. Repairing

Repair should not be done after the Phototransistor have been soldered. When repairing is unavoidable, a double-head soldering iron should be used (as below figure). It should be confirmed beforehand whether the characteristics of the Phototransistor will or will not be damaged by repairing.

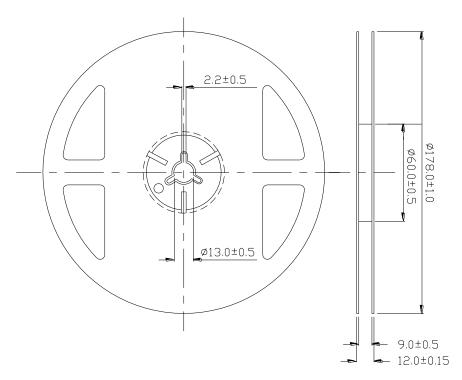


6. Sulfuration

Precautionary measures: Select and use quality guaranteed PCB board, solder substance and other related material. Avoid exposure to elemental sulfur substance. Never store LED with high oxidizing or reducing substances or other corrosive material. All the LED products can't be lighting in strong acid and strong alkali environment without special processing.

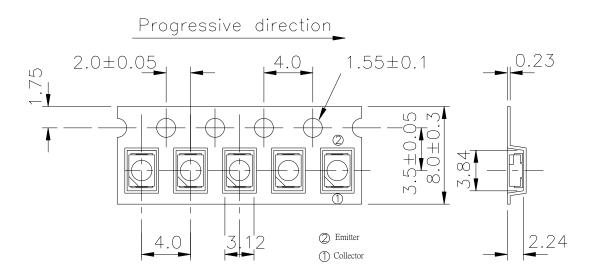


Package Dimensions



Note: The tolerances unless mentioned is ± 0.1 mm, Unit = mm

Carrier Tape Dimensions : (Quantity: 2000pcs/reel)

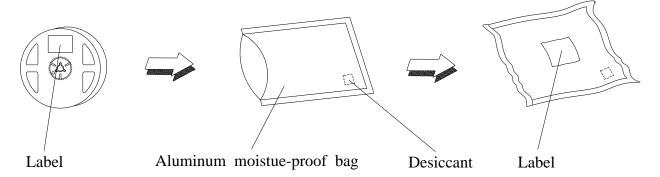


TOLERANCES UNLESS DIMENSION±0.1 ANGLE±0.5 UNIT:mm

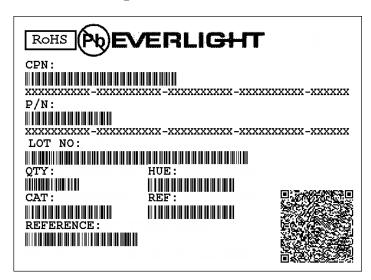
Note: The tolerances unless mentioned is ± 0.1 mm, Unit = mm



Packing Procedure



Label Form Specification



CPN: Customer's Production Number

P/N : Production Number QTY: Packing Quantity

CAT: Ranks

HUE: Peak Wavelength

REF: Reference

LOT No: Lot Number

MADE IN TAIWAN: Production Place

DISCLAIMER

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