



7N80

800V N-Channel Power MOSFET

Features

- $R_{DS(ON)}$, $V_{GS}@10V, I_D@3.3A < 1.9\Omega$
- Fast switching capability
- Low gate charge
- Lead free in compliance with EU RoHS directive.
- Green molding compound

Mechanical Data

- Case: TO-220, ITO-220, TO-262, TO-263 Package

Ordering Information

Part No.	Package	Packing
DMT7N80-TU	TO-220	50pcs / Tube
DMF7N80-TU	ITO-220	50pcs / Tube
DMK7N80-TU	TO-262	50pcs / Tube
DMG7N80-TU	TO-263	50pcs / Tube
DMG7N80-TR	TO-263	800pcs / 13" Reel

ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	800	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current	$T_C=25^\circ\text{C}$	I_D	7	A
	$T_C=100^\circ\text{C}$		4	
Pulsed Drain Current		I_{DM}	26	A
Single Pulse Avalanche Energy ^(Note 1)		E_{AS}	670	mJ
Power Dissipation	TO-220/TO-262/TO-263	P_D	142	W
	ITO-220		45	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Operating Temperature		T_{OPR}	-55 ~ +150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

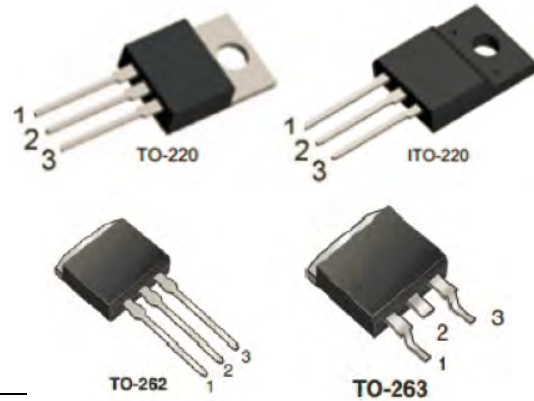
Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. $L=27.5\text{mH}$, $I_{AS}=7\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$

PRODUCT SUMMARY

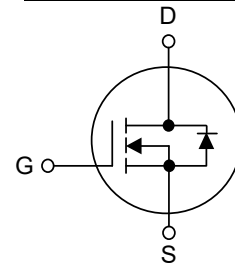
V_{DS} (V)	Current(A)	$R_{DS(on)}$ (Ω)
800	7	1.9 @ $V_{GS}=10\text{V}$



Pin Definition:

1. Gate
2. Drain
3. Source

Block Diagram





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THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-262/TO-263 ITO-220	θ_{JA}	62.5	$^{\circ}\text{C/W}$
Junction to Case	TO-220/TO-262/TO-263	θ_{JC}	0.9	$^{\circ}\text{C/W}$
	ITO-220		2.6	

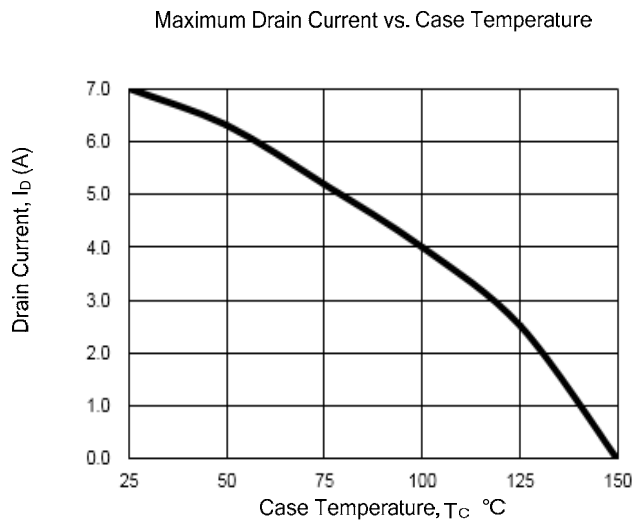
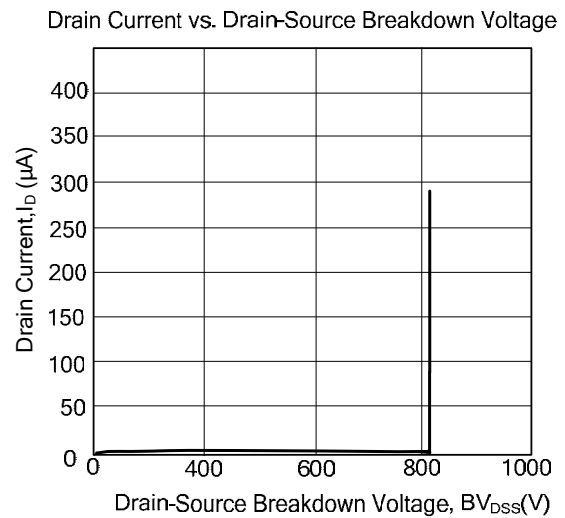
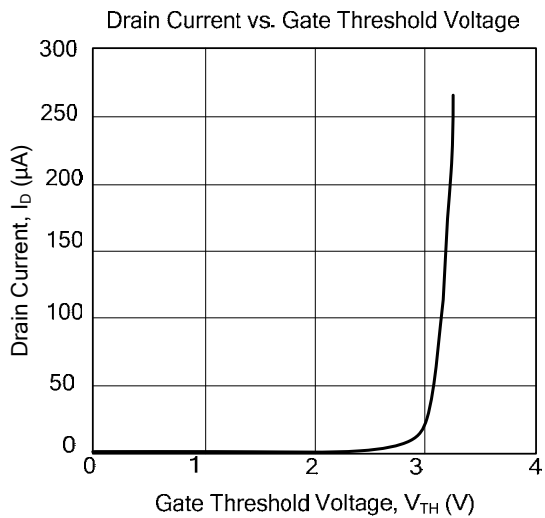
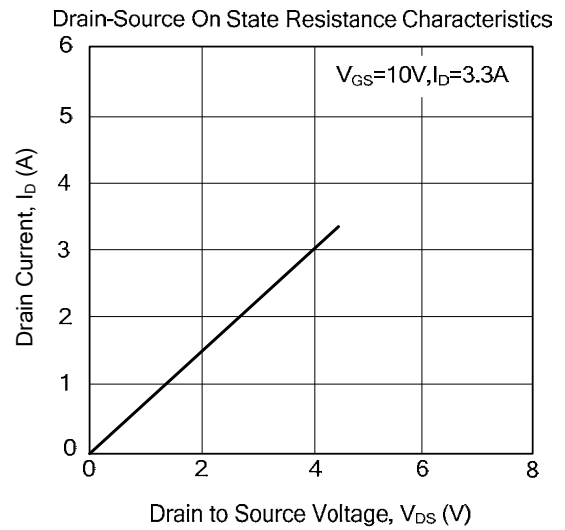
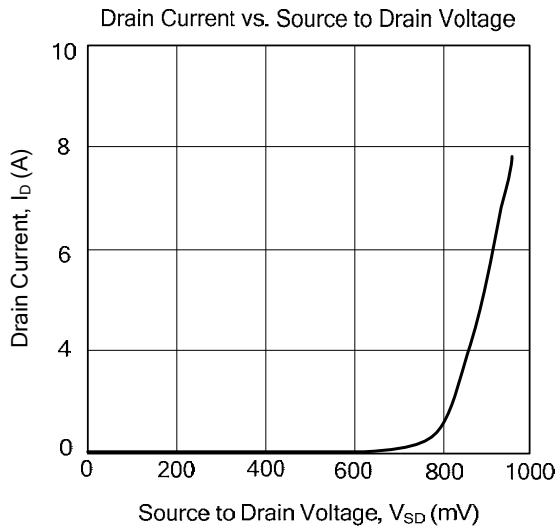
ELECTRICAL CHARACTERISTICS ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	800	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	3	-	5	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =3.3A	-	1.4	1.9	Ω
Gate -Source Drain Current	I _{DSS}	V _{DS} =800V, V _{GS} =0V	-	-	10	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Diode Forward Voltage	V _{SD}	I _S =6.6A, V _{GS} =0V	-	-	1.4	V
Dynamic (Note 4)						
Total Gate Charge	Q _g	V _{DS} =120V, V _{GS} =10V, I _D =6.6A I _G =3.3mA (Note 1,2)	-	155	160	nC
Gate-Source Charge	Q _{gs}		-	11	-	
Gate-Drain Charge	Q _{gd}		-	23	-	
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHZ	-	1200	1600	pF
Output Capacitance	C _{oss}		-	120	160	
Reverse Transfer Capacitance	C _{rss}		-	19	30	
Turn-On Delay Time	t _{d(on)}	V _{DD} =400V, I _D =6.6A, R _G =25Ω (Note 1,2)	-	60	80	ns
Turn-On Rise Time	t _r		-	100	140	
Turn-Off Delay Time	t _{d(off)}		-	300	330	
Turn-Off Fall Time	t _f		-	125	150	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	6.6	A
Maximum Pulsed Drain-Source Diode Forward Current	I _{SM}	---	-	-	26	A
Reverse Recovery Time	trr	V _{GS} =0V, I _S =6.6A, dI _F /dt=100A/μs (Note 1)	-	650	-	ns
Reverse Recovery Charge	Qrr		-	7.0	-	uC

Notes: 1. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature

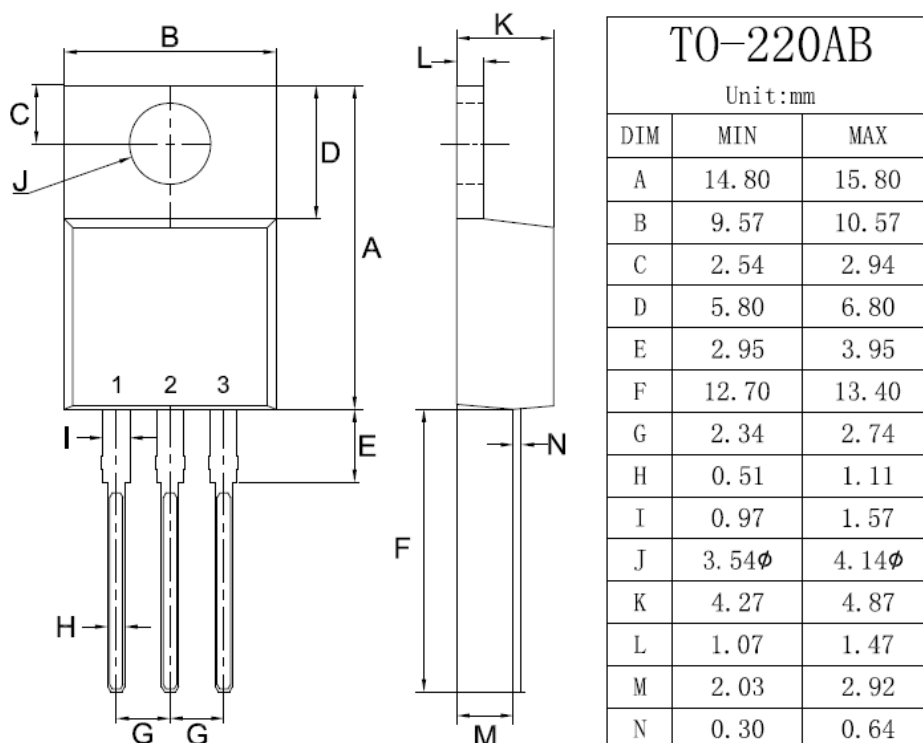


TYPICAL CHARACTERISTICS

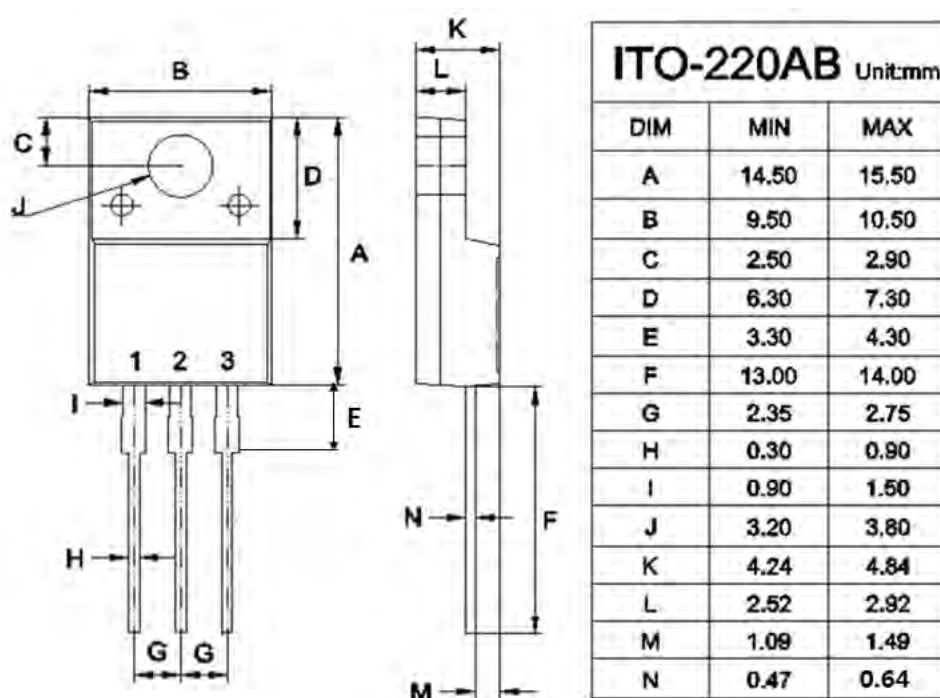




TO-220 Mechanical Drawing



ITO-220 Mechanical Drawing

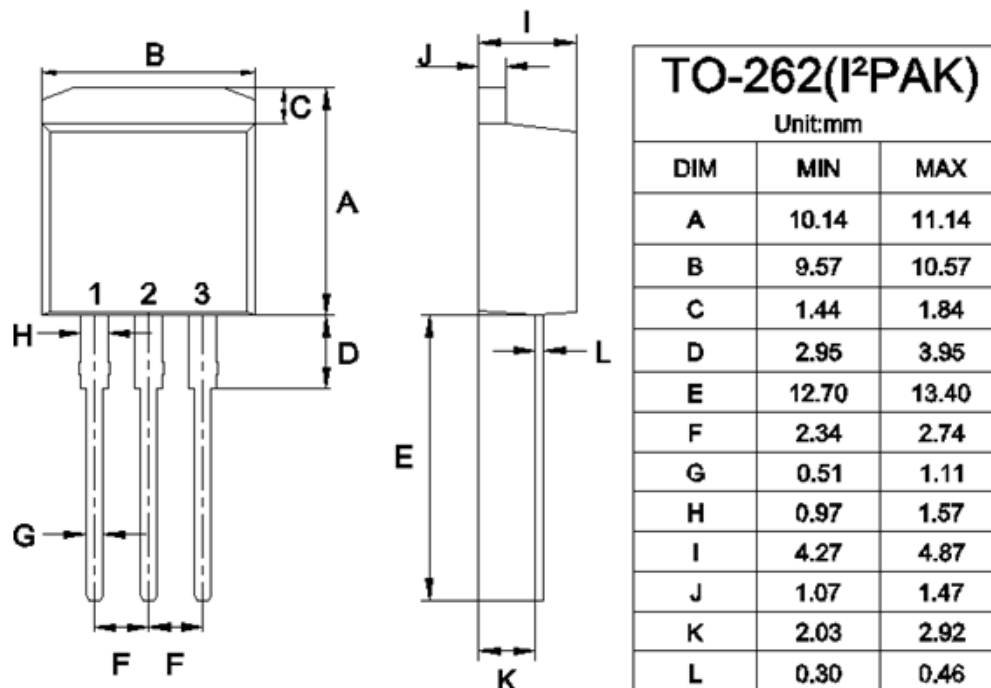




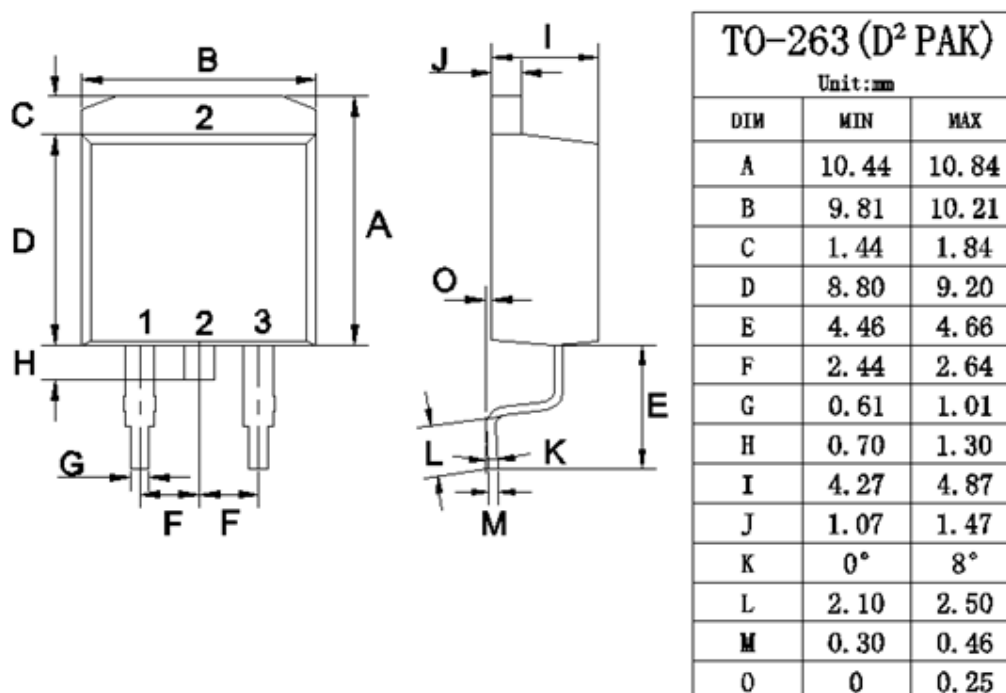
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TO-262 Mechanical Drawing



TO-263 Mechanical Drawing



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