



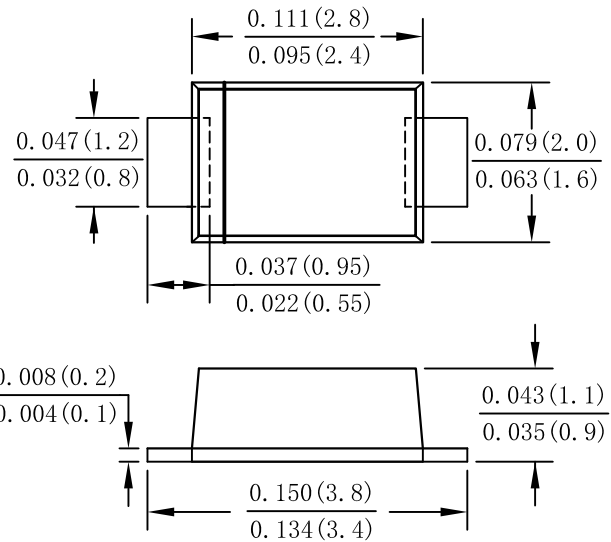
DSS12U THRU DSS120U

Single Phase 1.0AMP Surface Mount Schottky Barrier Rectifier

Features

- The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- Metal silicon junction, majority carrier conduction
- Low power loss, high efficiency
- High temperature soldering guaranteed: 260 °C/10 seconds, 0.375"(9.5mm) lead length, 5 lbs. (2.3kg) tension

Case: SOD-123FL



Dimensions in inches and (millimeters)

Mechanical Data

- Case: SOD-123FL, molded plastic
- Terminals: plated leads solderable per MIL-STD-750, Method 2026
- Polarity: Color band denotes cathode end
- Mounting position: Any

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	DSS12U	DSS13U	DSS14U	DSS15U	DSS16U	DSS18U	DSS110U	DSS115U	DSS120U	UNITS	
	Code	D12U	D13U	D14U	D15U	D16U	D18U	D110U	D115U	D120U		
Peak Repetitive Reverse Voltage	V_{RRM}										V	
Working Peak Reverse Voltage	V_{RWM}	20	30	40	50	60	80	100	150	200		
DC Blocking Voltage	V_{DC}											
RMS Reverse Voltage	V_{RMS}	14	21	28	35	42	56	70	105	140	V	
Average Rectified Output Current @ $T_L=90^\circ C$	$I_{F(AV)}$	1.0									A	
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave @ $T_j=125^\circ C$	I_{FSM}						30					A
Superimposed On Rated Load (JEDEC Method)							24					
Non-Repetitive Peak Forward Surge Current 1.0ms Single half sine-wave @ $T_j=125^\circ C$	I_{FSM}						60					A
Superimposed On Rated Load (JEDEC Method)							48					
10000 times of the wave surge current (time width 1ms, time interval 3s)	I_{FSM}						22.5					A
I^2t Rating for Fusing ($t < 8.3ms$)	I^2t	3.735									A^2s	
Forward Voltage per element @ $I_F=1.0A$	V_{FM}	0.50		0.67		0.82		0.90		V		
	T_{yp}	0.48		0.55		0.78		0.83				
Peak Reverse Current @ $T_A=25^\circ C$ At Rated DC Blocking Voltage @ $T_A=100^\circ C$	I_R	0.1					0.05					mA
		10					5					
Typical Junction Capacitance (Note 1)	C_J	50					35					pF
Typical Thermal Resistance	$R_{\theta JA}$	115									$^\circ C/W$	
	$R_{\theta JL}$	18										
Operating and Storage Temperature Range	T_J, T_{STG}	-55to+150									$^\circ C$	

Note: 1. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C



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Fig. 1 Typical Forward Current Derating Curve

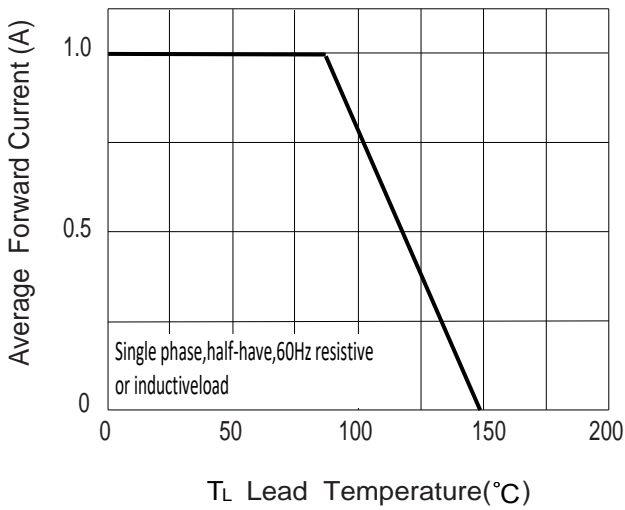


Fig. 2 Typical Instantaneous Forward Characteristics

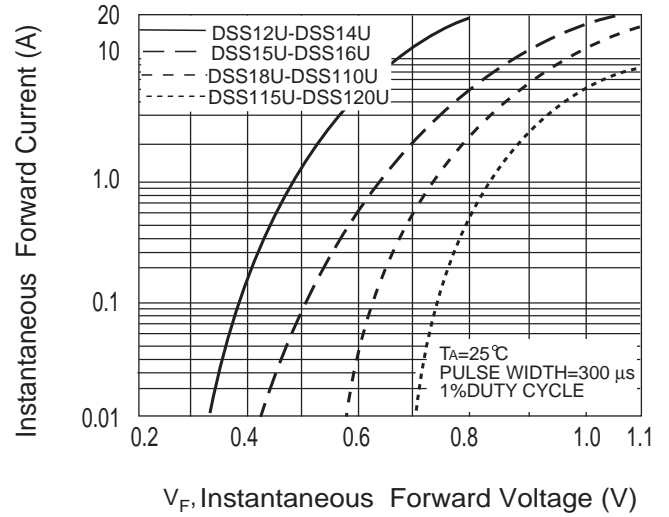


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

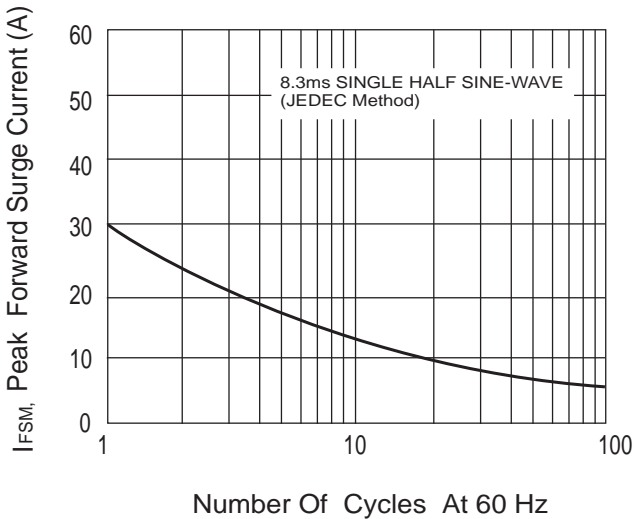


Fig.4 Typical Reverse Characteristics

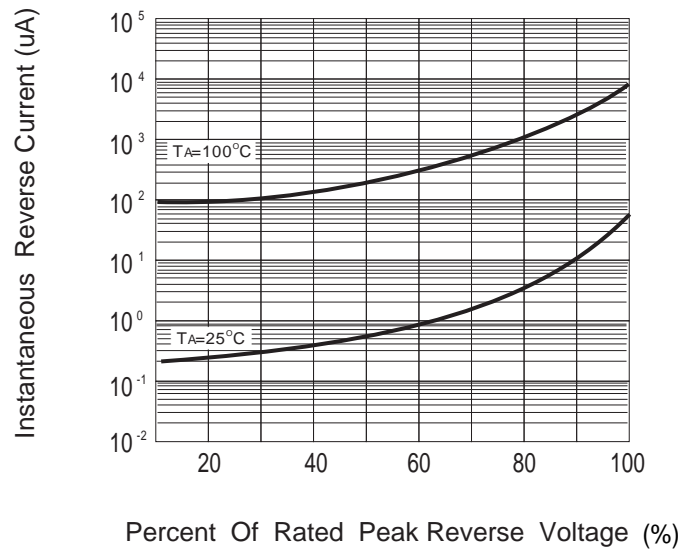
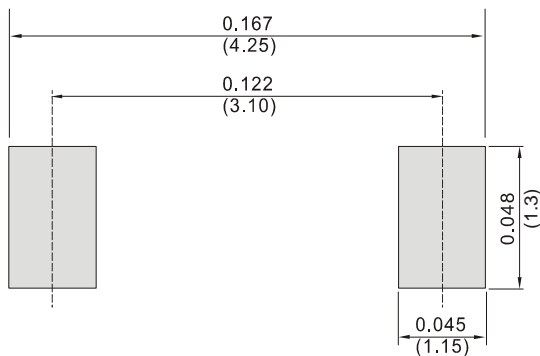


Fig.5 Typical Capacitance





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