



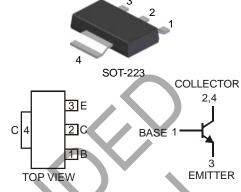
# NPN SURFACE MOUNT TRANSISTOR

#### **Features**

- Epitaxial Planar Die Construction
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

#### **Mechanical Data**

- Case: SOT-223
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.115 grams



#### Schematic and Pin Configuration

#### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	400	V
Collector-Emitter Voltage	V <sub>CEO</sub>	400	V
Emitter-Base Voltage	$V_{EBO}$	5	V
Continuous Collector Current	lc	0.5	A
Peak Pulse Current	I <sub>CM</sub>	1	A

#### Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation @T <sub>A</sub> = 25°C (Note 3)	P <sub>D</sub>	1	W
Thermal Resistance, Junction to Ambient Air (Note 3) @T <sub>A</sub> = 25°C	$R_{ hetaJA}$	125	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Off Characteristics		~				
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	400		_	V	$I_C = 100 \mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	400		_	V	$I_C = 10mA, I_B = 0$
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	5			V	$I_E = 100 \mu A, I_C = 0$
Collector Cutoff Current	I <sub>CBO</sub>			100	nA	$V_{CB} = 320V, I_{E} = 0$
Emitter Cutoff Current	I <sub>EBO</sub>			100	nA	$V_{EB} = 4V$ , $I_C = 0$
On Characteristics (Note 4)						
		_	0.075	0.3	V	$I_C = 20\text{mA}$ , $I_B = 1\text{mA}$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	0.06	0.25	V	$I_C = 50 \text{mA}$ , $I_B = 5 \text{mA}$
			0.08	0.5	V	$I_C = 100 \text{mA}, I_B = 10 \text{mA}$
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>			0.9	V	$I_C = 100 \text{mA}, I_B = 10 \text{mA}$
Base-Emitter Turn-On Voltage	V <sub>BE(ON)</sub>			1	V	$V_{CE} = 5V, I_{C} = 100mA$
		50	110	_		$V_{CE} = 5V$ , $I_C = 1mA$
DC Current Gain	h <sub>FE</sub>	50	100	_	_	$V_{CE} = 5V, I_{C} = 100mA$
		40	85	_		V <sub>CE</sub> = 10V, I <sub>C</sub> = 200mA
AC Characteristics						
Transition Frequency	f⊤	50		_	MHz	V <sub>CE</sub> = 20V, I <sub>C</sub> = 30mA, f = 30MHz
Output Capacitance	C <sub>obo</sub>			10	pF	V <sub>CB</sub> = 20V, f = 1MHz
Switching Times	t <sub>on</sub>	_	138	_	ns	V <sub>CC</sub> = 100V, I <sub>C</sub> = 100mA
Switching Times	t <sub>off</sub>	_	175	_	ns	$I_{B1} = 10 \text{mA}, I_{B2} = -20 \text{mA}$

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- Device mounted on FR-4 PCB, pad layout as shown on page 3 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Pulse Test: Pulse width ≤300μs. Duty cycle ≤2.0%.



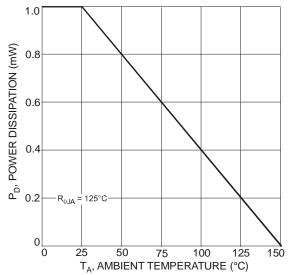


Fig. 1 Max Power Dissipation vs. Ambient Temperature

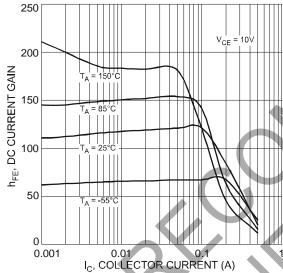


Fig. 3 Typical DC Current Gain vs. Collector Current

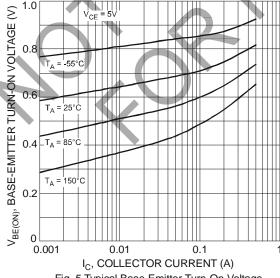


Fig. 5 Typical Base-Emitter Turn-On Voltage vs. Collector Current

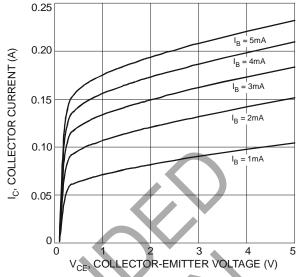


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

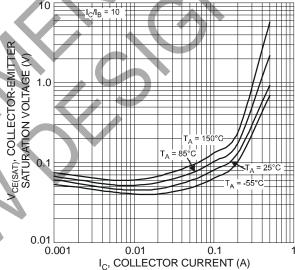


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

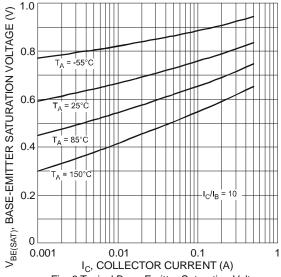


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current

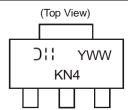
#### NOT RECOMMENDED FOR NEW DESIGN **USE FZT658**

#### **Ordering Information** (Note 5)

Device	Packaging	Shipping
DZT658-13	SOT-223	2500/Tape & Reel

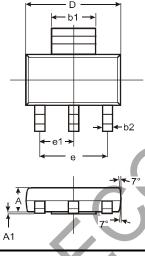
Notes: 5. For packaging details, go to our website at http://www.diodes.com/ap2007.pdf.

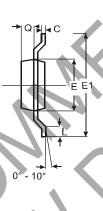
#### Marking Information



KN4 = Product Type Marking Code YWW = Date Code Marking Y = Last digit of year ex: 7 = 2007 WW = Week code 01 - 52

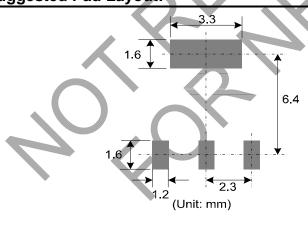
#### **Package Outline Dimensions**





	SOT-223					
Dim	Min	Max	Тур			
Α	1.55	1.65	1.60			
A1	0.010	0.15	0.05			
<b>b</b> 1	2.90	3.10	3.00			
b2	0.60	0.80	0.70			
C	0.20	0.30	0.25			
	6.45	6.55	6.50			
E	3.45	3.55	3.50			
E1	6.90	7.10	7.00			
е	_	_	4.60			
e1	_	_	2.30			
L	0.85	1.05	0.95			
Q	0.84	0.94	0.89			
All Dimensions in mm						

### Suggested Pad Layout:



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