

## Product Summary

BV <sub>DSS</sub>	R <sub>DSON</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
-60V	60mΩ @ V <sub>GS</sub> = -10V	-18.8A
	80mΩ @ V <sub>GS</sub> = -4.5V	-16.8A

## Features and Benefits

- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low R<sub>DSON</sub> — Ensures Minimal On-State Losses
- Wettable Flank for Improved Optical Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMP6051SFVWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

<https://www.diodes.com/quality/product-definitions/>

## Description and Applications

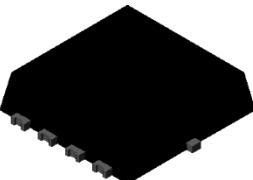
This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Motor controls
- Power-management functions
- DC-DC converters

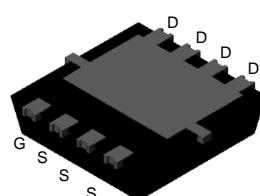
## Mechanical Data

- Package: PowerDI®3333-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.03 grams (Approximate)

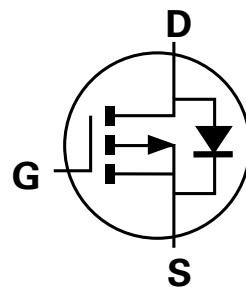
PowerDI3333-8/SWP (Type UX)



Top View



Bottom View



Equivalent Circuit

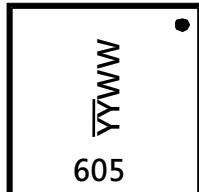
## Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
DMP6051SFVWQ-7	PowerDI3333-8/SWP (Type UX)	2000	Tape & Reel
DMP6051SFVWQ-13	PowerDI3333-8/SWP (Type UX)	3000	Tape & Reel

Notes:

- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



605 = Product Type Marking Code

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 24 = 2024)

WW = Week Code (01 to 53)

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	-60	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current (Note 6) $V_{GS} = -10\text{V}$	$I_D$	-18.8 -15.1	A
Pulsed Drain Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)	$I_{DM}$	-75	A
Maximum Continuous Body Diode Forward Current (Note 6)	$I_S$	-20.6	A
Pulsed Source Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)	$I_{SM}$	-75	A
Avalanche Current, $L = 0.1\text{mH}$	$I_{AS}$	-27	A
Avalanche Energy, $L = 0.1\text{mH}$	$E_{AS}$	36	$\text{mJ}$

**Thermal Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$P_D$	2.7	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	47	$^\circ\text{C/W}$
Total Power Dissipation (Note 6)	$P_D$	43	W
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	2.9	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	-60	—	—	V	$V_{GS} = 0, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	-1	$\mu\text{A}$	$V_{DS} = -60\text{V}, V_{GS} = 0$
Gate-Source Leakage	$I_{GSS}$	—	—	$\pm 100$	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0$
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	-1	—	-3	V	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	48	60	$\text{m}\Omega$	$V_{GS} = -10\text{V}, I_D = -7\text{A}$
		—	62	80		$V_{GS} = -4.5\text{V}, I_D = -7\text{A}$
Diode Forward Voltage	$V_{SD}$	—	-0.8	-1.2	V	$V_{GS} = 0, I_S = -1\text{A}$
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	$C_{iss}$	—	2087	—	pF	$V_{DS} = -30\text{V}, V_{GS} = 0, f = 1\text{MHz}$
Output Capacitance	$C_{oss}$	—	94	—	pF	
Reverse Transfer Capacitance	$C_{rss}$	—	78	—	pF	
Gate Resistance	$R_g$	—	3.5	—	$\Omega$	$V_{DS} = 0, V_{GS} = 0, f = 1\text{MHz}$
Total Gate Charge ( $V_{GS} = -4.5\text{V}$ )	$Q_g$	—	17	—	nC	
Total Gate Charge ( $V_{GS} = -10\text{V}$ )	$Q_g$	—	36	—	nC	
Gate-Source Charge	$Q_{gs}$	—	3.7	—	nC	$V_{DS} = -30\text{V}, I_D = -5\text{A}$
Gate-Drain Charge	$Q_{gd}$	—	5.6	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	6.5	—	ns	
Turn-On Rise Time	$t_R$	—	19	—	ns	$V_{DD} = -30\text{V}, V_{GS} = -10\text{V}, R_G = 3\Omega, I_D = -5\text{A}$
Turn-Off Delay Time	$t_{D(OFF)}$	—	40	—	ns	
Turn-Off Fall Time	$t_F$	—	24	—	ns	
Body Diode Reverse-Recovery Time	$t_{RR}$	—	23	—	ns	$I_F = -5\text{A}, di/dt = 100\text{A}/\mu\text{s}$
Body Diode Reverse-Recovery Charge	$Q_{RR}$	—	21	—	nC	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

6. Thermal resistance from junction to soldering point (on the exposed drain pad).

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

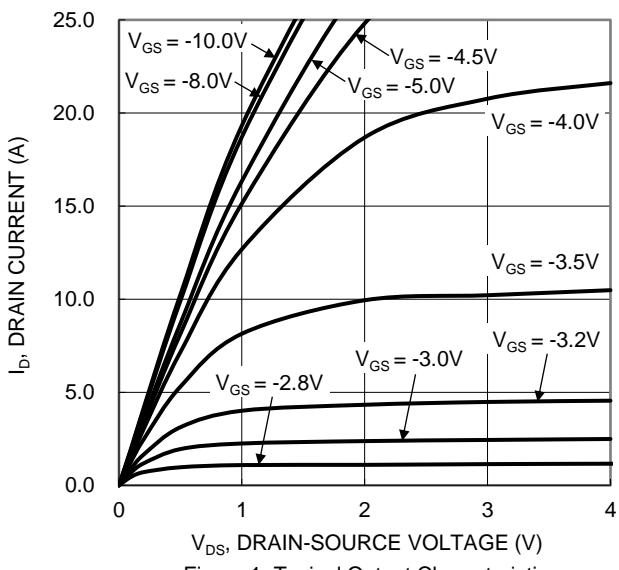


Figure 1. Typical Output Characteristic

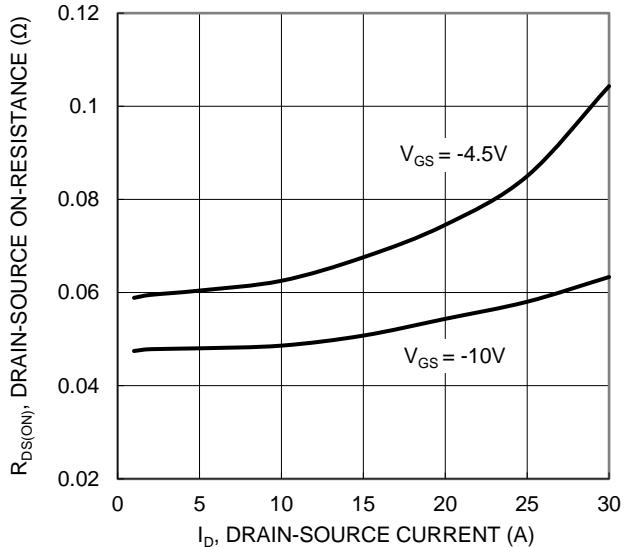


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

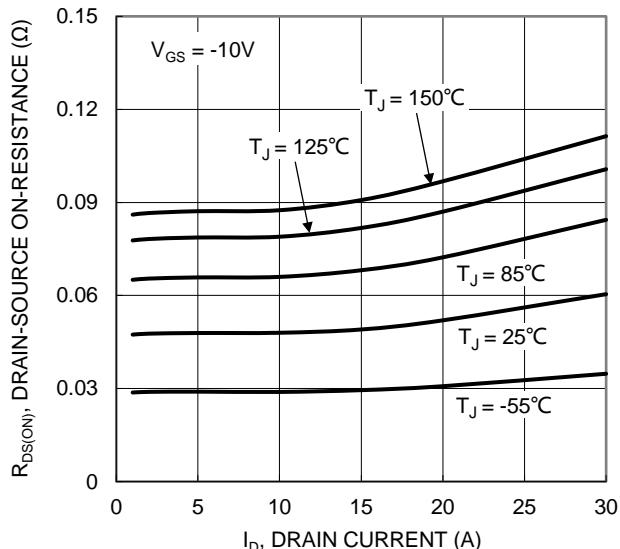


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

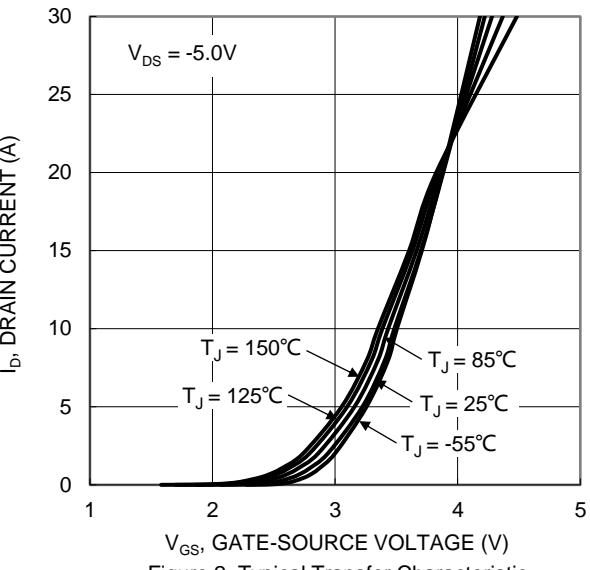


Figure 2. Typical Transfer Characteristic

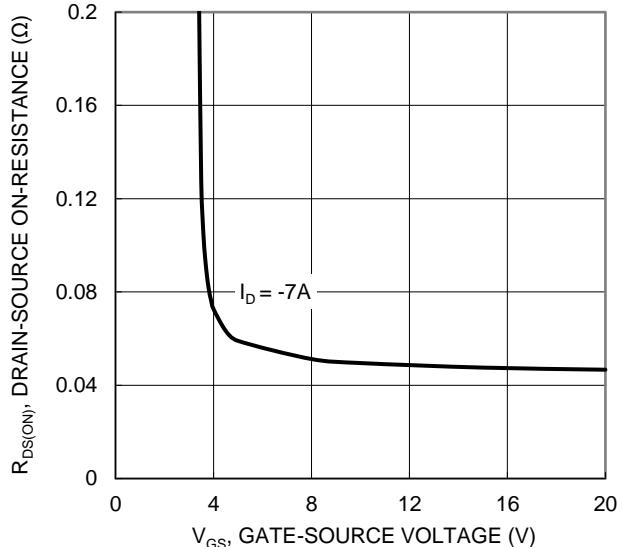


Figure 4. Typical Transfer Characteristic

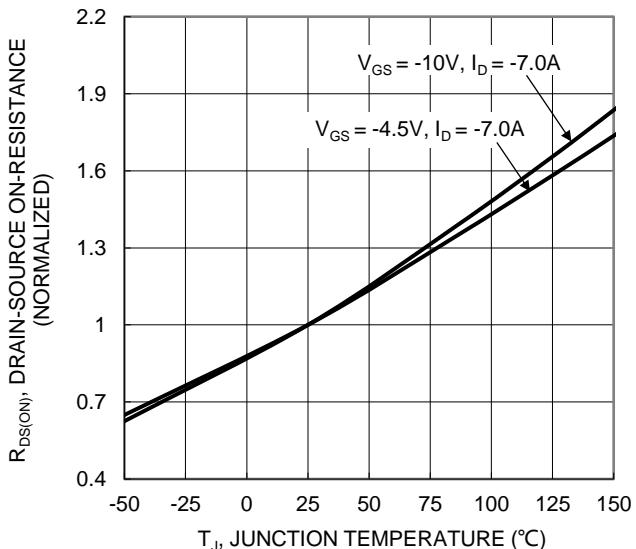
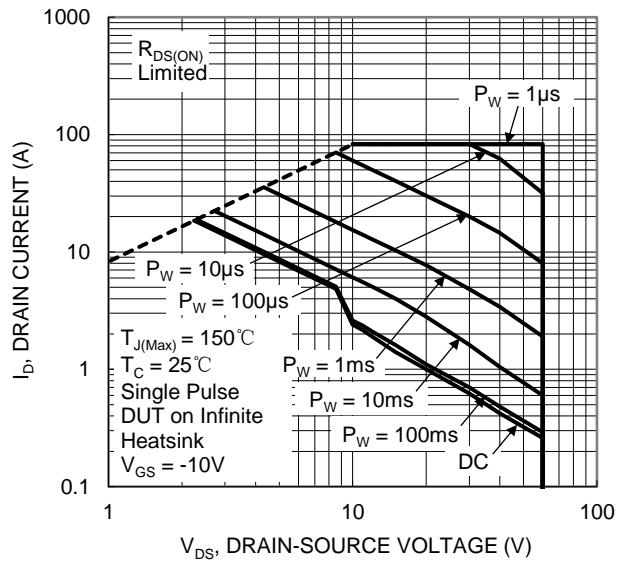
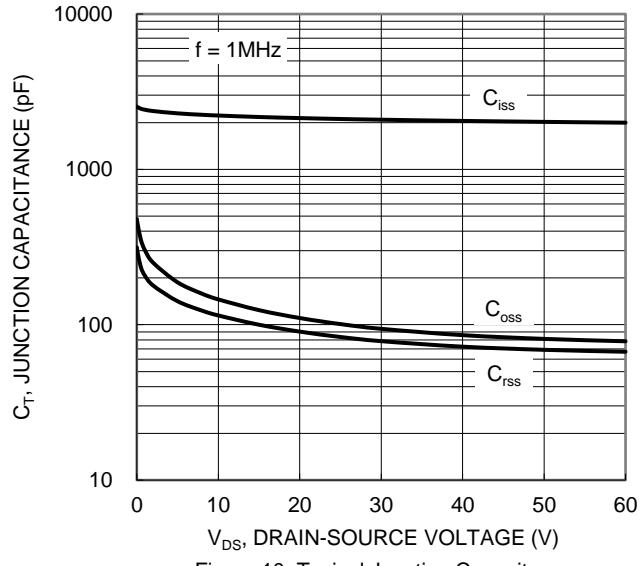
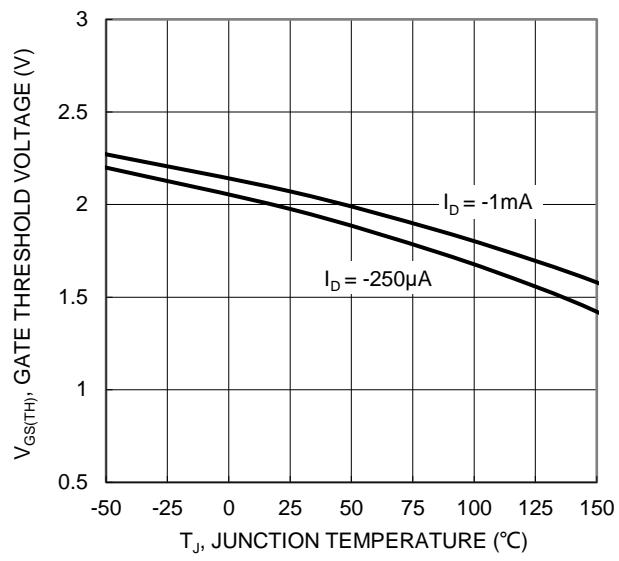
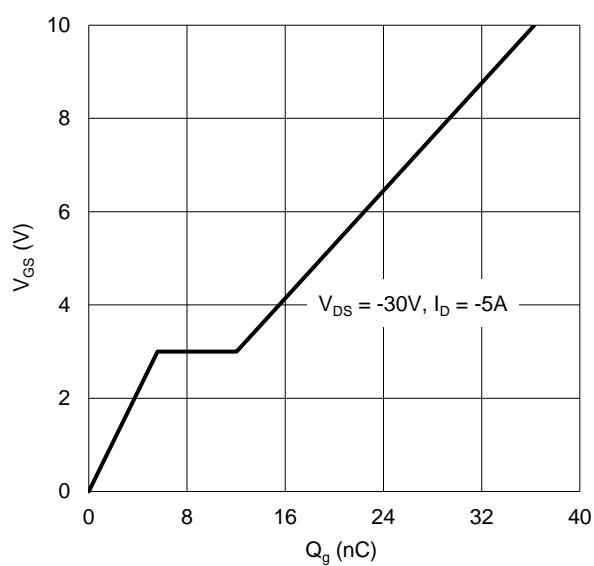
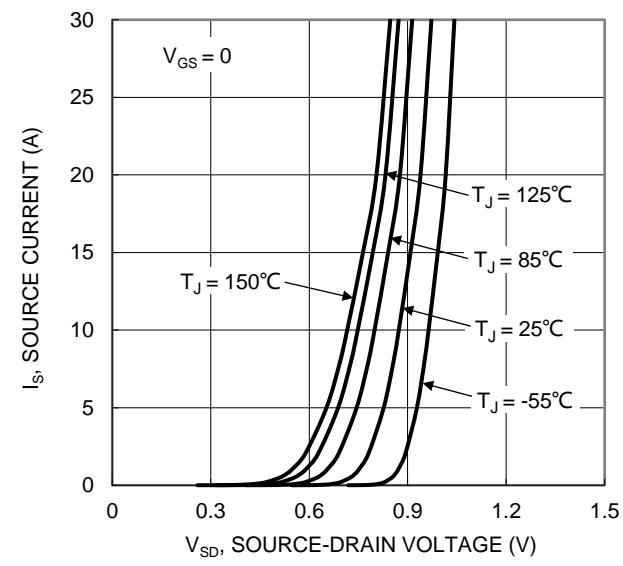
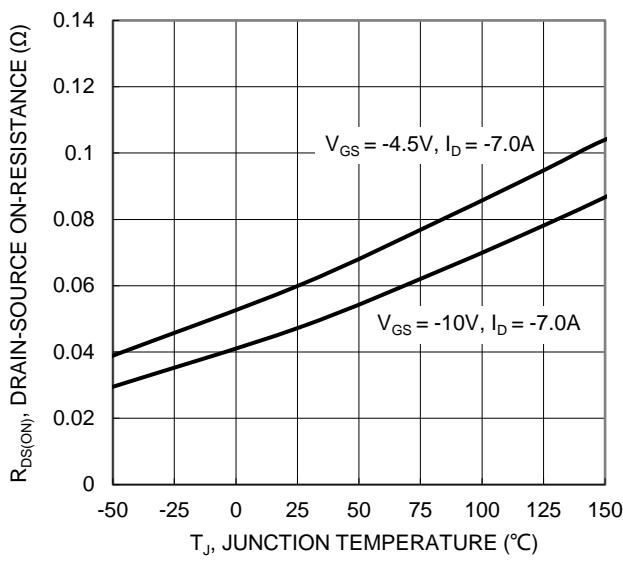


Figure 6. On-Resistance Variation with Junction Temperature



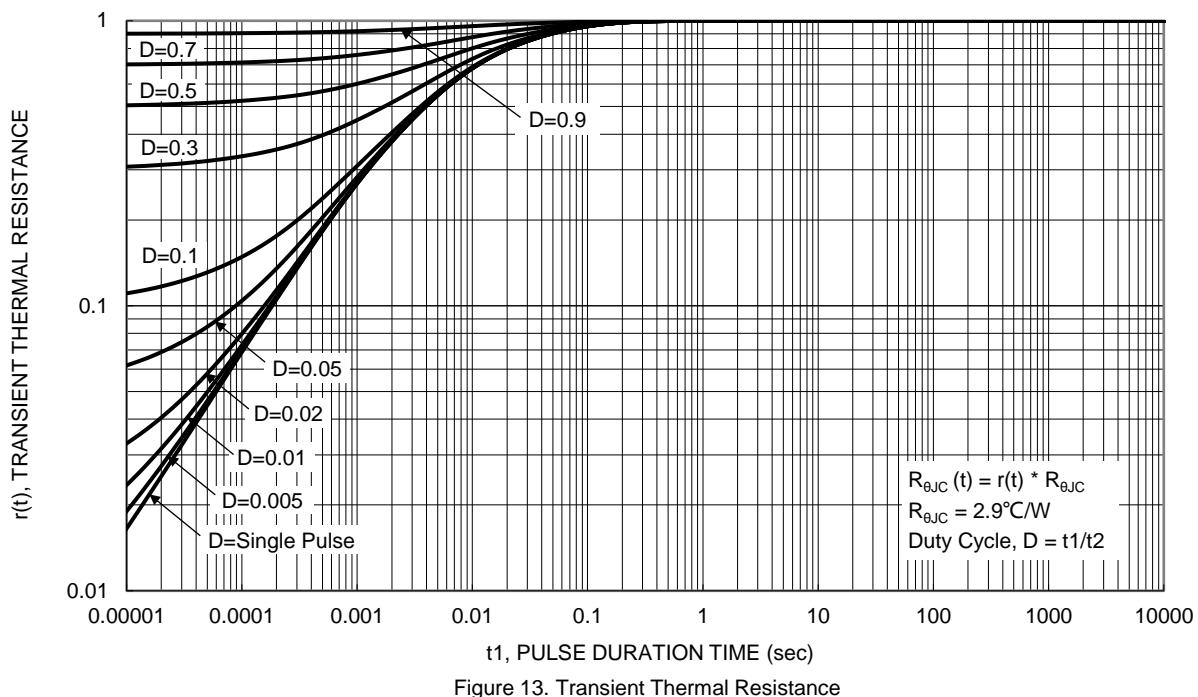
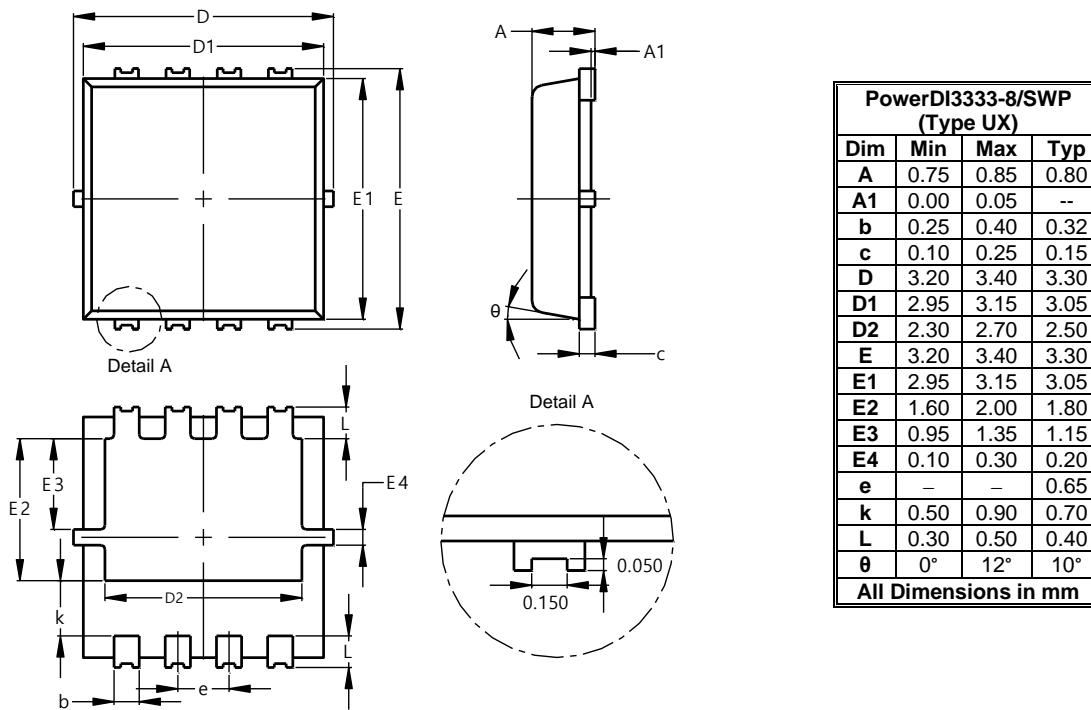


Figure 13. Transient Thermal Resistance

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

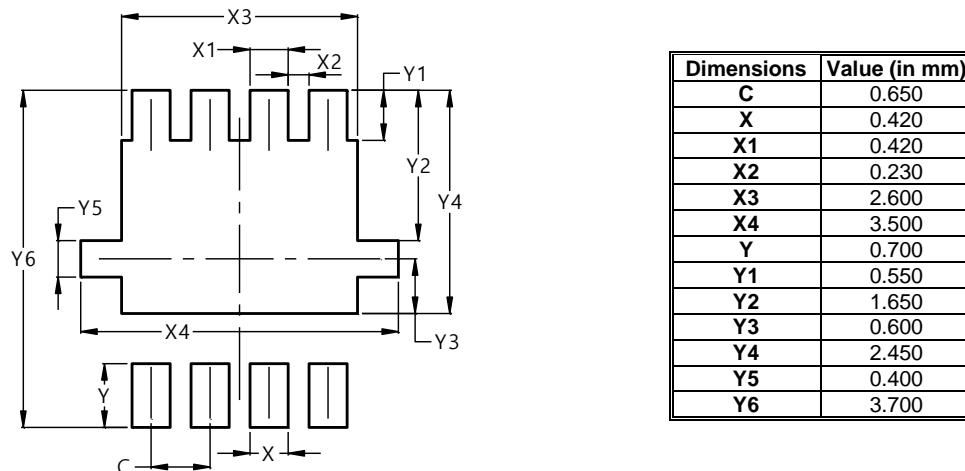
**PowerDI3333-8/SWP (Type UX)**



## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI3333-8/SWP (Type UX)**



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