

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
30V	0.4Ω @ V _{GS} = 10V	0.89A
	0.7Ω @ V _{GS} = 4.5V	0.67A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

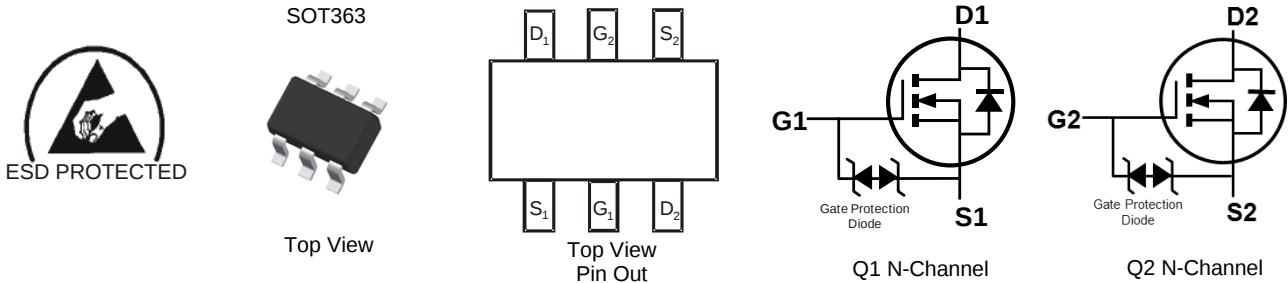
- Motor controls
- Power-management functions
- DC-DC converters

Features and Benefits

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative.
<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: SOT363
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.027 grams (Approximate)



Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMN3350LDW-7	SOT363	3000	Tape & Reel
DMN3350LDW-13	SOT363	10000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



BU2 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: L = 2024)
 M = Month (ex: 9 = September)

Date Code Key

Year	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	K	L	M	N	P	R	S	T	U	V	W	X
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	I _D	0.89 0.7	A
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	0.48	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I _{DM}	3.2	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Total Power Dissipation (Note 5)			P _D	0.34	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		R _{θJA}	364	°C/W
Total Power Dissipation (Note 6)			P _D	0.48	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		R _{θJA}	260	°C/W
Operating and Storage Temperature Range			T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1.0	μA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.8	—	1.6	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	0.24	0.4	Ω	V _{GS} = 10V, I _D = 0.59A
		—	0.3	0.7		V _{GS} = 4.5V, I _D = 0.2A
Diode Forward Voltage	V _{SD}	—	0.6	1.2	V	V _{GS} = 0V, I _S = 10mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	38.4	—	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	10.5	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	6.4	—	pF	
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	0.5	—	nC	V _{DS} = 10V, V _{GS} = 10V I _D = 250mA
Total Gate Charge (V _{GS} = 10V)	Q _g	—	1.1	—	nC	
Gate-Source Charge	Q _{gs}	—	0.2	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.1	—	nC	V _{GS} = 10V, V _{DS} = 30V, I _D = 100mA, R _G = 25Ω
Turn-On Delay Time	t _{D(ON)}	—	3.2	—	ns	
Turn-On Rise Time	t _r	—	12	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	82	—	ns	
Turn-Off Fall Time	t _f	—	51	—	ns	

- Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.

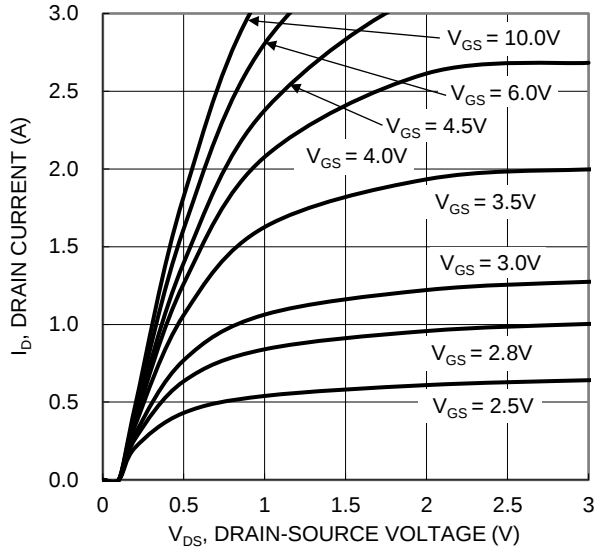


Figure 1. Typical Output Characteristic

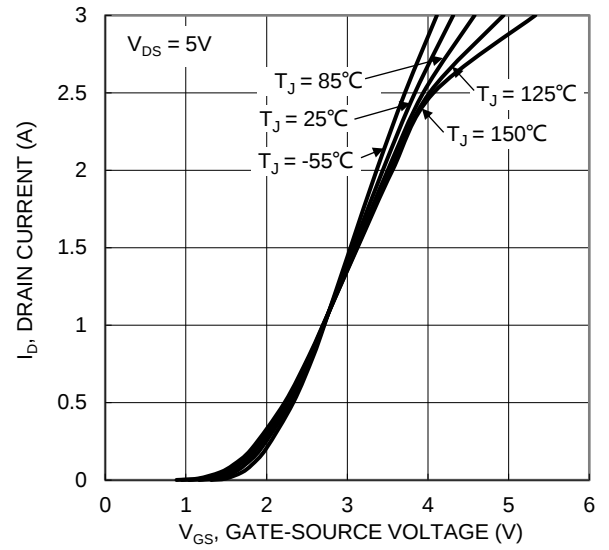


Figure 2. Typical Transfer Characteristic

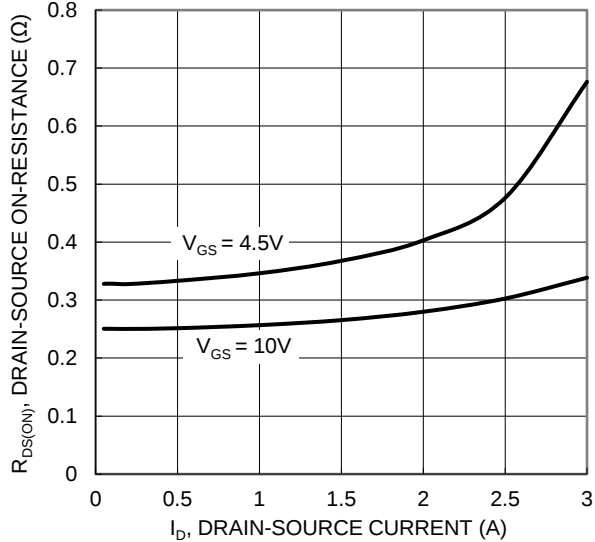


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

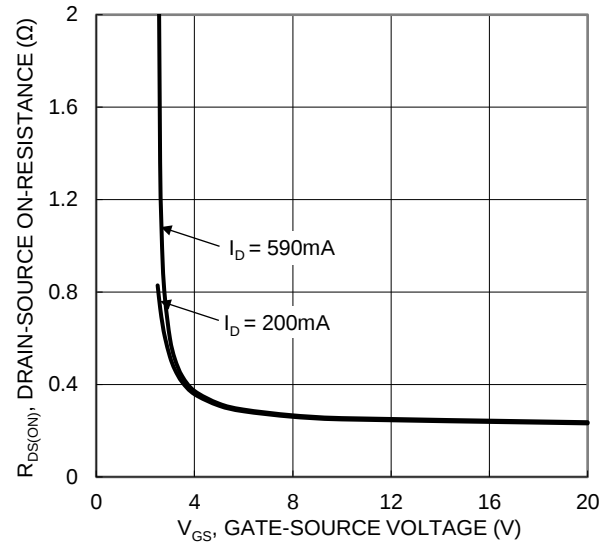


Figure 4. Typical Transfer Characteristic

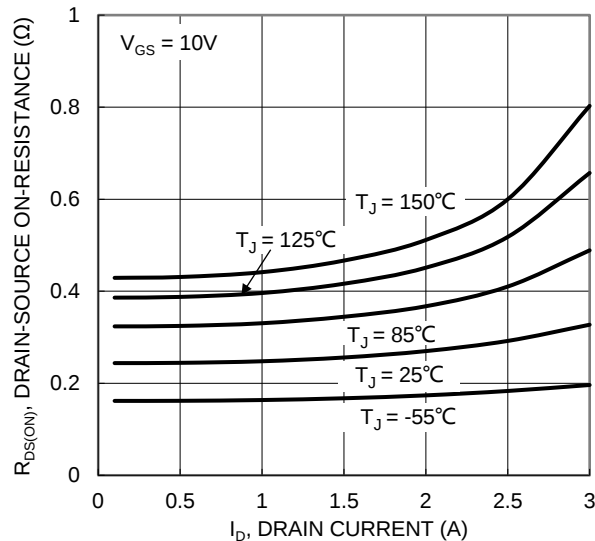


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

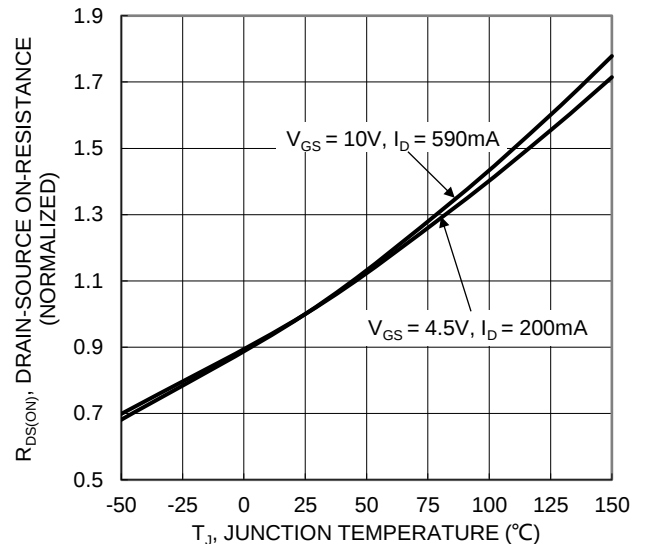
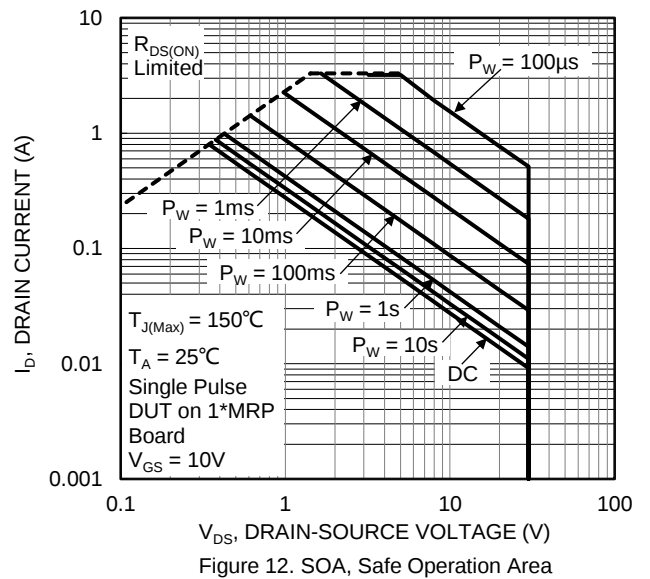
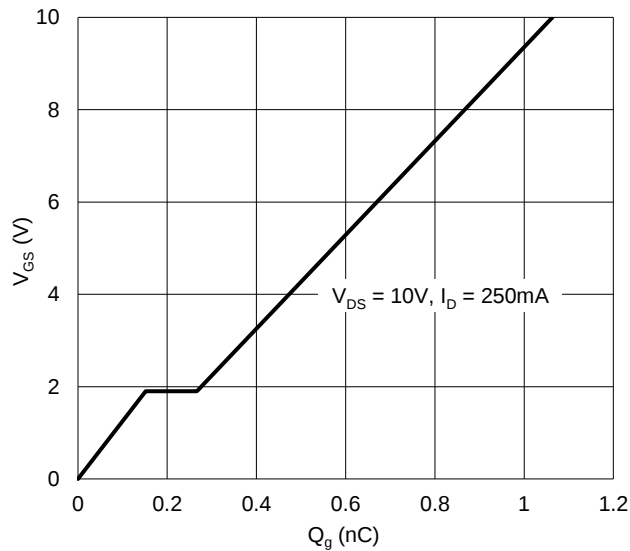
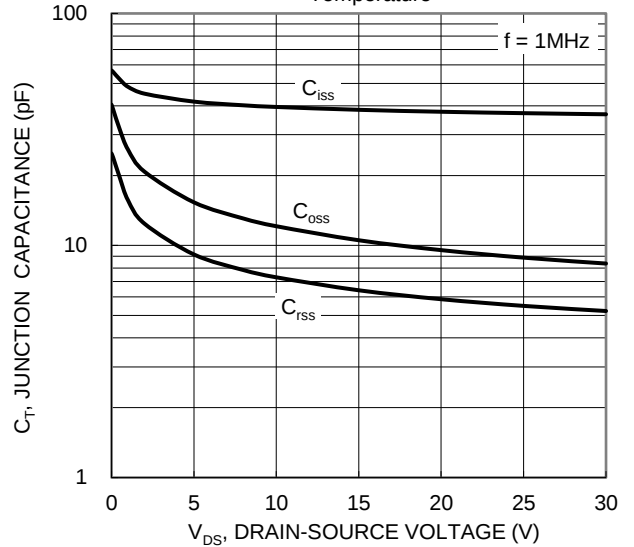
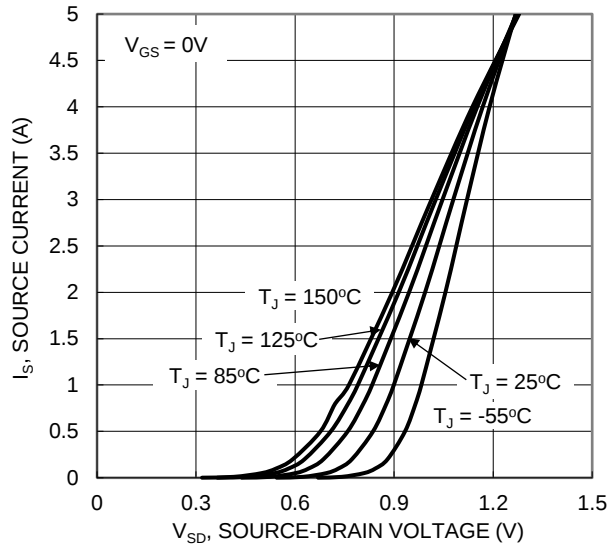
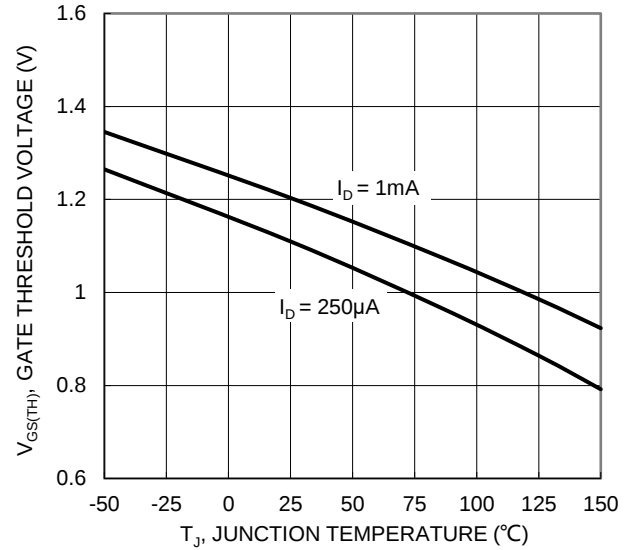
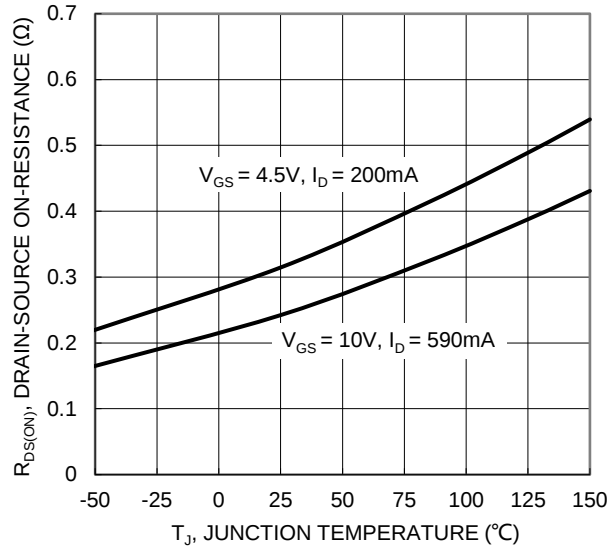


Figure 6. On-Resistance Variation with Junction Temperature



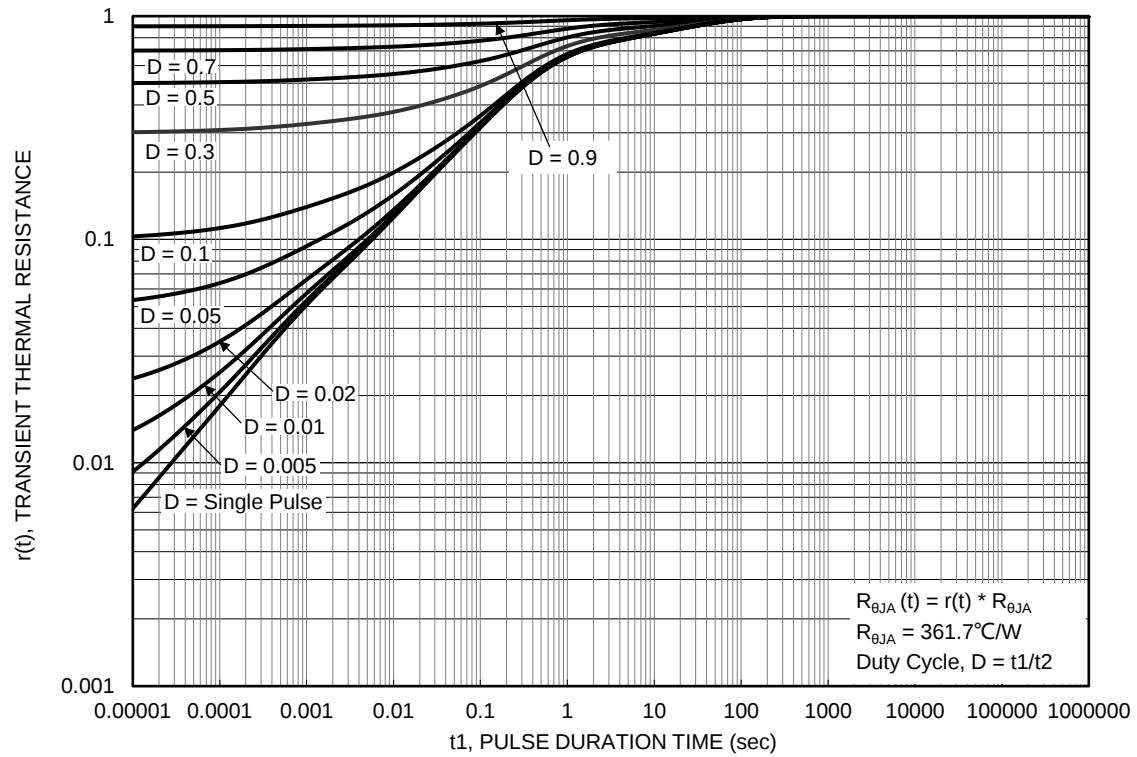
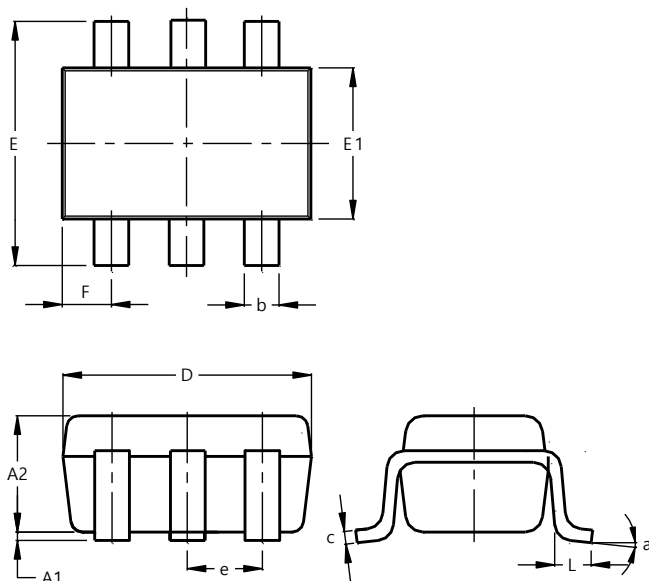


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT363

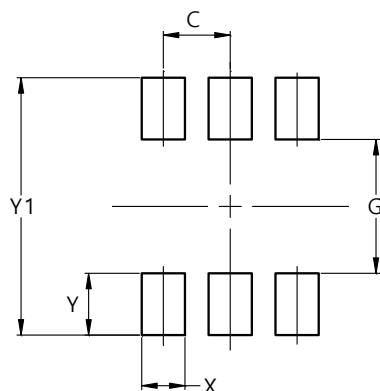


SOT363			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.10	0.30	0.25
c	0.10	0.22	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
F	0.40	0.45	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT363



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.420
Y	0.600
Y1	2.500

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