

Description

The DML3017LDC load switch provides a component and area-reducing solution for efficient power domain switching with inrush current limit via soft-start. In addition to integrated control functionality with ultra-low on-resistance, this device offers system safeguards and monitoring via fault protection and power-good signaling. This cost-effective solution is ideal for power management and hot-swap applications requiring low power consumption in a small footprint.

Features and Benefits

- Advanced Controller with Charge Pump
- Integrated N-Channel MOSFET with Ultra-Low R_{ON}
- Input Voltage Range 0.5V to 20V
- Soft-Start via Controlled Slew Rate
- Adjustable Slew Rate Control
- Power-Good Signal
- Thermal Shutdown
- V_{CC} Undervoltage Lockout
- Short-Circuit Protection
- Extremely Low Standby Current
- Load Bleed (Quick Discharge)
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative.**
<https://www.diodes.com/quality/product-definitions/>

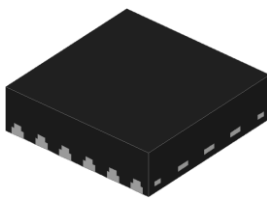
Applications

- Infotainment
- Body control modules
- Instrument clusters
- ADAS

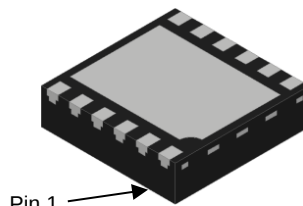
Mechanical Data

- Package: V-DFN3030-12
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 
- Weight: 0.024 grams (Approximate)

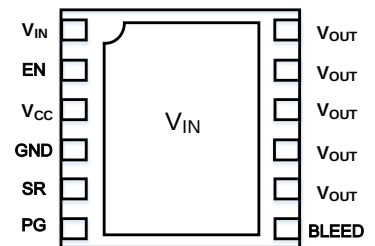
V-DFN3030-12 (Type B)



Top View



Bottom View



Top View

Ordering Information (Note 4)

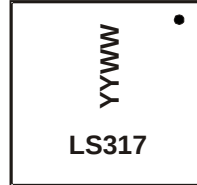
Orderable Part Number	Package	Tape Width	Tape Pitch	Packing	
				Qty.	Carrier
DML3017LDC-7A	V-DFN3030-12 (Type B)	12mm	8mm	1,500	Tape & Reel
DML3017LDC-7	V-DFN3030-12 (Type B)	8mm	4mm	3,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

Site 1

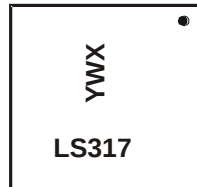
V-DFN3030-12 (Type B)



LS317 = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 24 = 2024)
WW = Week Code (01 to 53)

Site 2

V-DFN3030-12 (Type B)



LS317 = Product Type Marking Code
YWX = Date Code Marking
Y = Year (ex: 4 = 2024)
W = Week (ex: a = Week 27; z Represents Week 52 and 53)
X = Internal Code (ex: U = Monday)

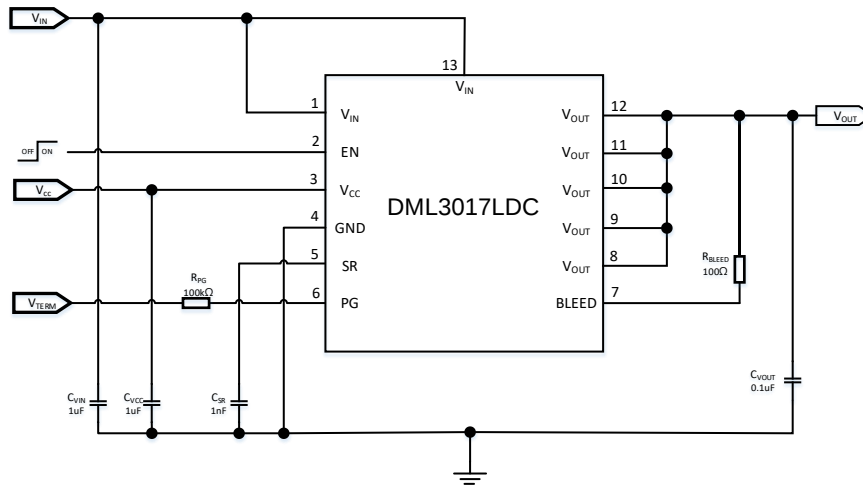
Date Code Key

Year	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035
Code	4	5	6	7	8	9	0	1	2	3	4	5

Week	1-26	27-52	53
Code	A-Z	a-z	z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	T	U	V	W	X	Y	Z

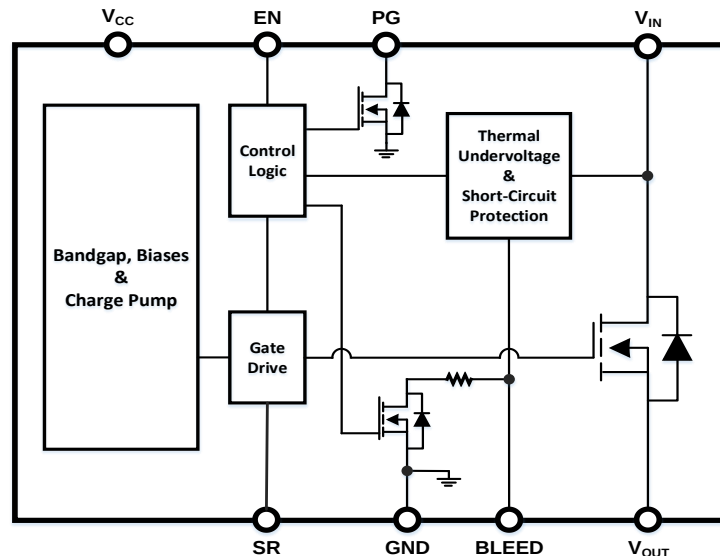
Typical Application Circuit



Pin Description

Pin Number	Pin Name	Pin Function
1, 13	V _{IN}	Drain of internal MOSFET, pin 1 must connect to pin 13.
2	EN	Active-high digital input used to turn on the MOSFET, pin has an internal pulldown resistor to GND.
3	V _{CC}	Supply voltage to controller (3.0V to 5.5V).
4	GND	Controller ground.
5	SR	Slew rate adjustment. Please refer to C _{SR} vs. V _{OUT} rising time in Table 1.
6	PG	Active-high, open-drain output that indicates when the gate of the MOSFET is fully charged, external pullup resistor ≥ 1kΩ to an external voltage source required; tie to GND if not used.
7	BLEED	Load bleed connection, must be tied to V _{OUT} either directly or through a resistor ≤ 1kΩ.
8 to 12	V _{OUT}	Source of internal MOSFET connected to load.

Function Block Diagram



Absolute Maximum Rating

Parameter	Rating
V _{IN} , BLEED, V _{OUT} to GND	-0.3V to 24V
EN, V _{CC} , SR, PG to GND	-0.3V to 6V
I _{MAX_DC} (Note 5)	15A
Junction Temperature (T _J)	-40°C to +125°C
Storage Temperature (T _S)	-65°C to +150°C

Note: 5. I_{MAX_DC} defined as the maximum steady state current the load switch can pass at room ambient temperature without entering thermal lockout.

Recommended Operating Ranges

Parameter	Rating
Supply Voltage (V _{VCC})	3V to 5.5V
Input Voltage (V _{VIN})	0.5V to 20V
Ambient Temperature (T _A)	-40°C to +85°C
Package Thermal Resistance (Θ _{JC})	3.5°C/W
Package Thermal Resistance (Θ _{JA})	30°C/W

Electrical Characteristics (T_A = +25°C, V_{VCC} = 3.3V, V_{VIN} = 5V = V_{TERM}, C_{VIN} = 1μF, C_{VOUT} = 0.1μF, C_{VCC} = 1μF, C_{SR} = 1nF, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{VIN}	Input Voltage	—	0.5	—	20	V
V _{VCC}	Supply Voltage	—	3.0	—	5.5	V
I _{DYN}	V _{CC} Dynamic Supply Current	V _{EN} = V _{VCC} = 3V, V _{VIN} = 20V	—	150	250	μA
		V _{EN} = V _{VCC} = 5.5V, V _{VIN} = 1.8V	—	190	350	μA
I _{STBY}	V _{CC} Shutdown Supply Current	V _{VCC} = 3V, V _{EN} = 0V	—	0.1	1	μA
		V _{VCC} = 5.5V, V _{EN} = 0V	—	0.1	2	μA
V _{ENH}	EN High-Level Voltage	V _{VCC} = 3V to 5.5V	2.0	—	—	V
V _{ENL}	EN Low-Level Voltage	V _{VCC} = 3V to 5.5V	—	—	0.8	V
R _{BLEED}	Bleed Resistance	V _{VCC} = 3V, V _{EN} = 0V	90	120	150	Ω
		V _{VCC} = 5.5V, V _{EN} = 0V	70	100	130	Ω
I _{BLEED}	Bleed Pin Leakage Current	V _{VCC} = V _{EN} = 3V, V _{VIN} = 1.8V	—	3	—	μA
		V _{VCC} = V _{EN} = 3V, V _{VIN} = 20V	—	32	—	μA
V _{PGL}	PG Output Low Voltage	V _{VCC} = 3V, I _{SINK} = 5mA	—	—	0.2	V
I _{PG}	PG Output Leakage Current	V _{VCC} = 3V, V _{TERM} = 3.3V	—	—	100	nA
Switching Device						
R _{ON}	Switch On-State Resistance	V _{VCC} = 3.3V, V _{VIN} = 1.8V	—	6.1	9	mΩ
		V _{VCC} = 3.3V, V _{VIN} = 5V	—	5.9	9	mΩ
		V _{VCC} = 3.3V, V _{VIN} = 12V	—	5.8	9	mΩ
		V _{VCC} = 5V, V _{VIN} = 1.8V	—	4.8	7	mΩ
		V _{VCC} = 5V, V _{VIN} = 5V	—	4.8	7	mΩ
		V _{VCC} = 5V, V _{VIN} = 12V	—	4.8	7	mΩ
I _{LEAK}	Input Shutdown Supply Current	V _{EN} = 0V, V _{VIN} = 20V	—	—	10	μA
R _{PDEN}	EN Pulldown Resistance	—	70	100	130	kΩ
Fault Protection						
T _{OTP}	Thermal Shutdown Threshold	V _{VCC} = 3V to 5.5V	—	+145	—	°C
T _{OTPHYS}	Thermal Shutdown Hysteresis	V _{VCC} = 3V to 5.5V	—	+20	—	°C
V _{UVLO}	V _{CC} Lockout Threshold	V _{CC} Falling	2.3	2.55	2.8	V
V _{UVLOHYS}	V _{CC} Lockout Hysteresis	V _{CC} Rising	—	200	—	mV
V _{SCP}	Short-Circuit Protection Threshold	V _{VCC} = 3.3V, V _{VIN} = 0.5V	140	240	350	mV
		V _{VCC} = 3.3V, V _{VIN} = 1.2V to 12V	120	240	500	mV
		V _{VCC} = 3.3V, V _{VIN} = 20V	100	250	500	mV

Switching Characteristics ($T_A = +25^\circ\text{C}$, $V_{\text{TERM}} = V_{\text{VCC}} = 5\text{V}$, $R_{\text{PG}} = 100\text{k}\Omega$, $R_{\text{VOUT}} = 10\Omega$, $C_{\text{VIN}} = 1\mu\text{F}$, $C_{\text{VOUT}} = 0.1\mu\text{F}$, $C_{\text{VCC}} = 1\mu\text{F}$, $C_{\text{SR}} = 1\text{nF}$, unless otherwise specified.)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
V _{VIN} = 1.8V						
t _{ON}	Output Turn-On Delay Time	V _{VCC} = 3.3V	300	660	900	μs
		V _{VCC} = 5V	100	350	600	
t _{OFF}	Output Turn-Off Delay Time	V _{VCC} = 3.3V	—	1	2	
		V _{VCC} = 5V	—	1	2	
t _{PGON}	Power-Good Turn-On Time	V _{VCC} = 3.3V	0.8	1.8	2.6	ms
		V _{VCC} = 5V	0.7	1.45	2	
t _{PGOFF}	Power-Good Turn-Off Time	V _{VCC} = 3.3V	—	20	100	ns
		V _{VCC} = 5V	—	10	100	
SR	Output Slew Rate	V _{VCC} = 3.3V	1	3.3	7	kV/s
		V _{VCC} = 5V	3	5.6	9	
V _{VIN} = 12V						
t _{ON}	Output Turn-On Delay Time	V _{VCC} = 3.3V	250	600	850	μs
		V _{VCC} = 5V	50	440	550	
t _{OFF}	Output Turn-Off Delay Time	V _{VCC} = 3.3V	—	1	2	
		V _{VCC} = 5V	—	1	2	
t _{PGON}	Power-Good Turn-On Time	V _{VCC} = 3.3V	1.2	3.1	3.5	ms
		V _{VCC} = 5V	0.9	3	3.1	
t _{PGOFF}	Power-Good Turn-Off Time	V _{VCC} = 3.3V	—	20	100	ns
		V _{VCC} = 5V	—	10	100	
SR	Output Slew Rate	V _{VCC} = 3.3V	3	9.5	16	kV/s
		V _{VCC} = 5V	3	11.5	20	

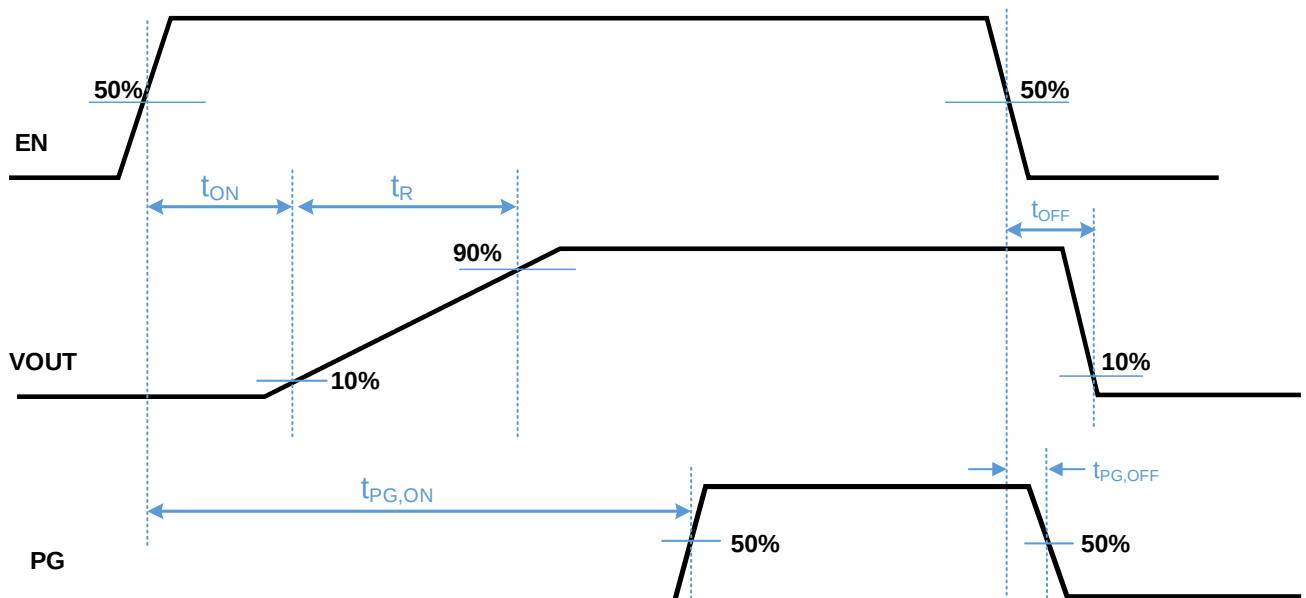
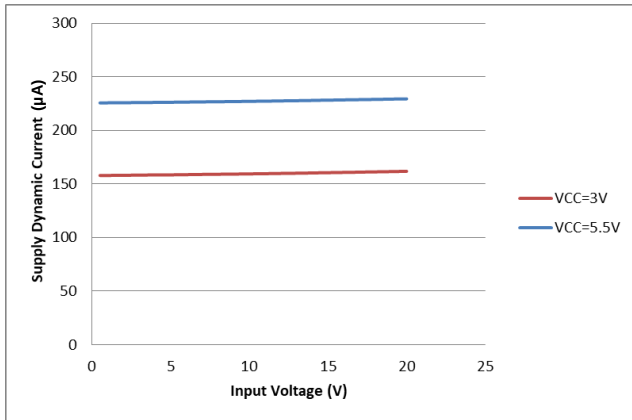


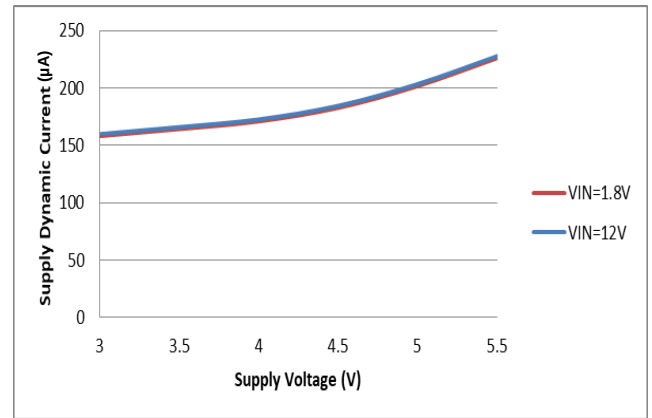
Figure 1. Timing Diagram

Performance Characteristics (@T_A = +25°C, unless otherwise specified.)

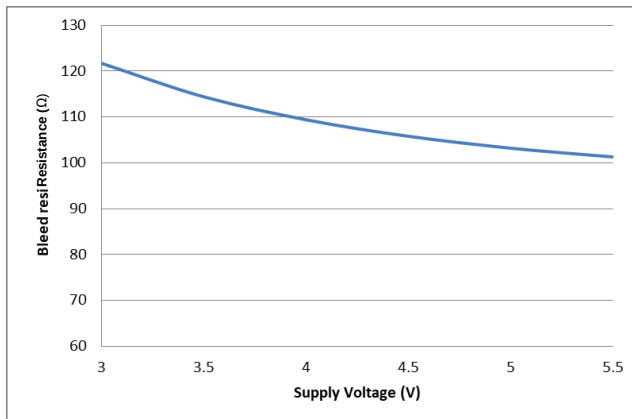
Supply Dynamic Current vs. Input Voltage



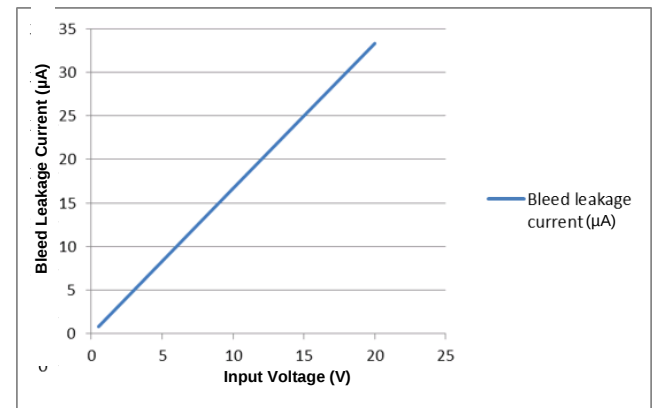
Supply Dynamic Current vs. Supply Voltage



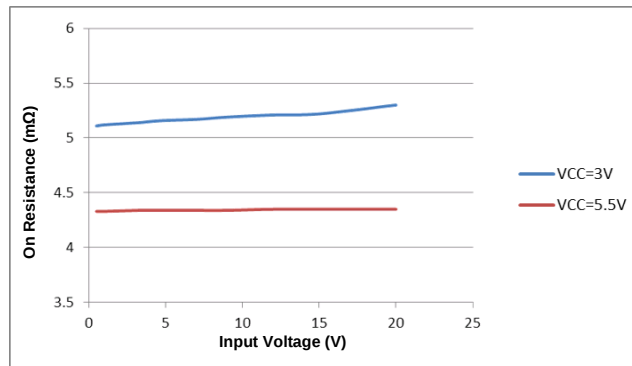
Bleed Resistance vs. Supply Voltage



Bleed Leakage Current vs. Input Voltage



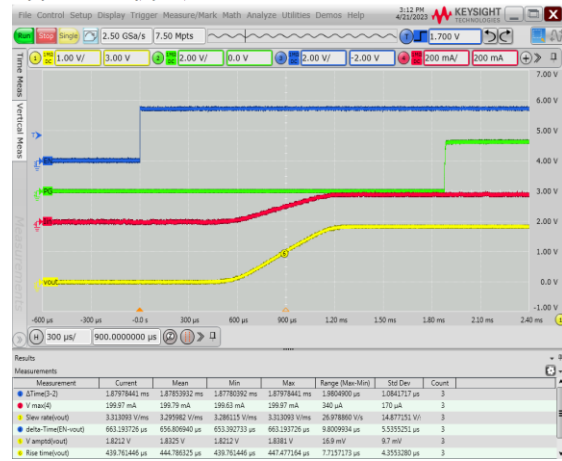
ON Resistance vs. Input Voltage



Performance Characteristics (@T_A = +25°C, unless otherwise specified.) (continued)

Turn-ON Response

V_{IN}=1.8V, V_{CC}=3.3V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



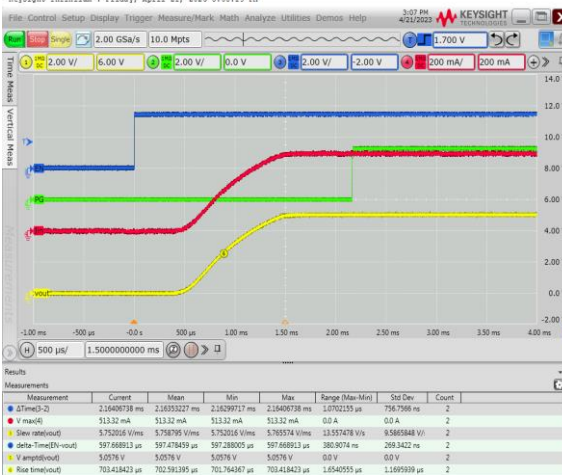
Turn-OFF Response

V_{IN}=1.8V, V_{CC}=3.3V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



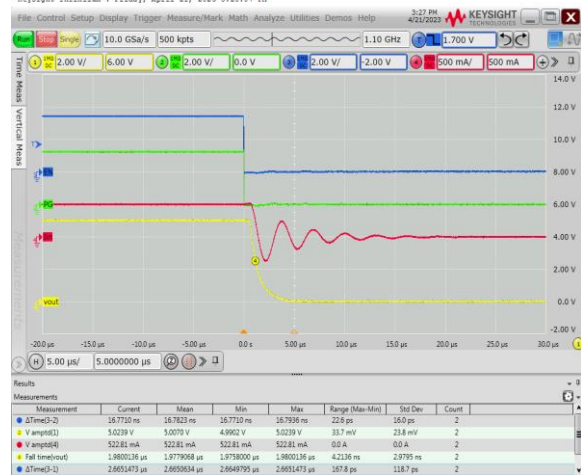
Turn-ON Response

V_{IN}=5V, V_{CC}=3.3V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



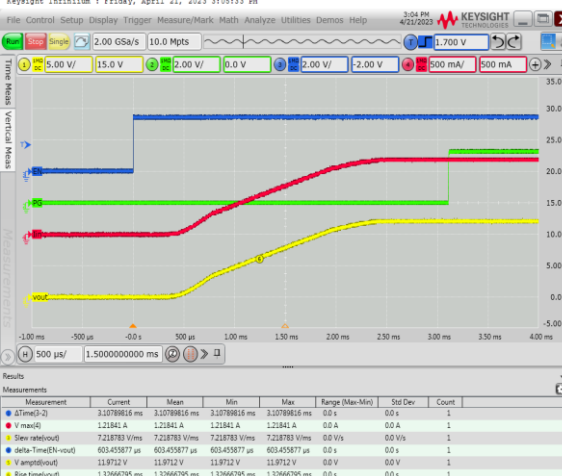
Turn-OFF Response

V_{IN}=5V, V_{CC}=3.3V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



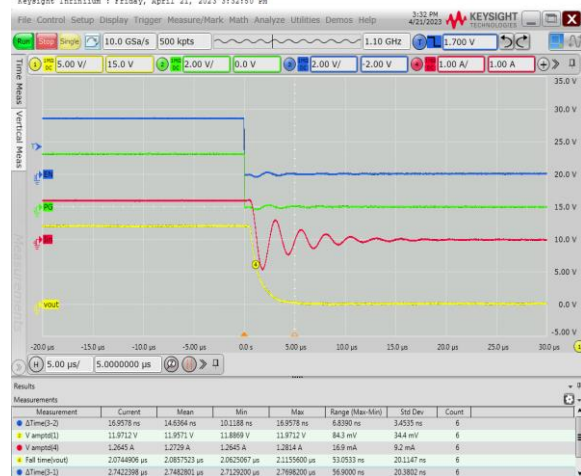
Turn-ON Response

V_{IN}=12V, V_{CC}=3.3V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



Turn-OFF Response

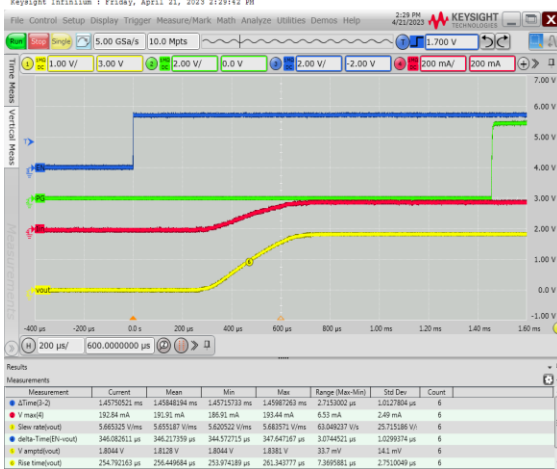
V_{IN}=12V, V_{CC}=3.3V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



Performance Characteristics (@T_A = +25°C, unless otherwise specified.) (continued)

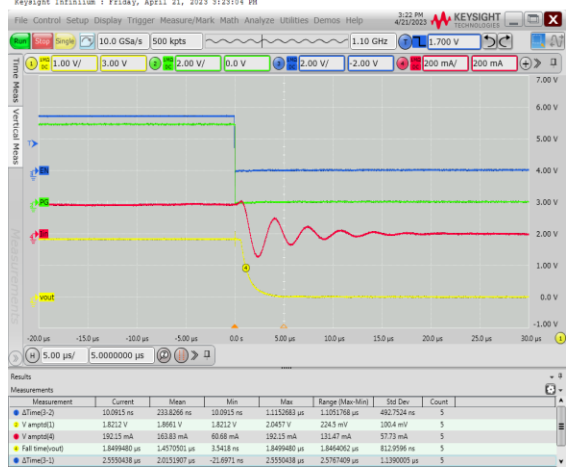
Turn-ON Response

V_{IN}=1.8V, V_{CC}=5V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



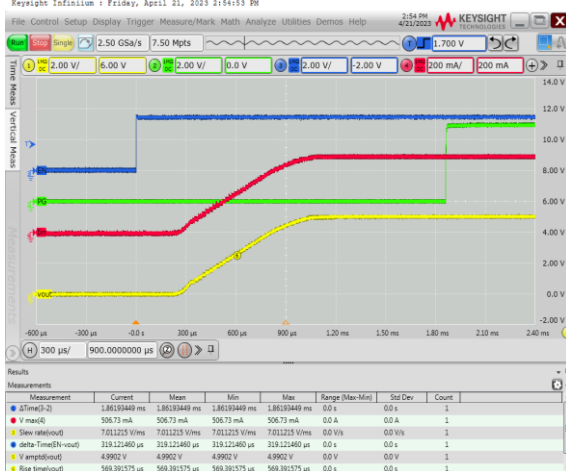
Turn-OFF Response

V_{IN}=1.8V, V_{CC}=5V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



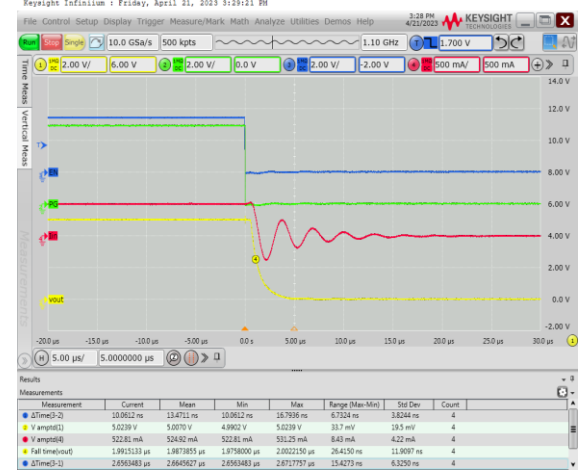
Turn-ON Response

V_{IN}=5V, V_{CC}=5V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



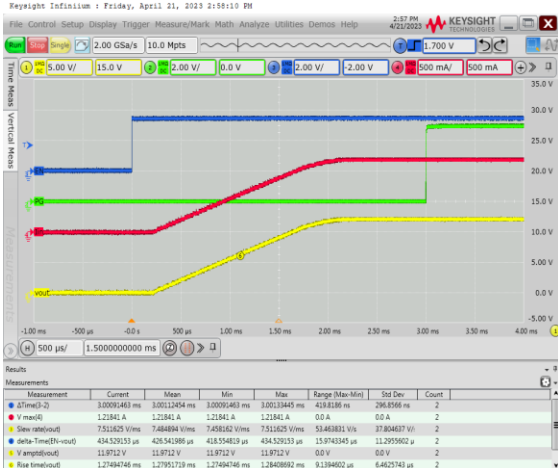
Turn-OFF Response

V_{IN}=5V, V_{CC}=5V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



Turn-ON Response

V_{IN}=12V, V_{CC}=5V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



Turn-OFF Response

V_{IN}=12V, V_{CC}=5V, V_{EN}=0V to 3.3V, C_{SR}=1nF, R_L=10Ω



Application Information

General Description

The DML3017LDC is a single-channel load switch with a controlled adjustable turn-on and integrated PG indicator in a 12-pin V-DFN3030-12 (Type B) package. The device contains an n-channel MOSFET that can operate over an input voltage range of 0.5V to 20V and can support a maximum continuous current of 15A. The wide-input voltage range and high-current capability enable the device to be used across multiple designs and end equipment. 5mΩ on-resistance minimizes the voltage drop across the load switch and power loss from the load switch.

The controlled rise time for the device greatly reduces inrush current by large bulk load capacitances thereby reducing or eliminating power supply droop. The adjustable slew rate through SR provides the design flexibility to trade off the inrush current and power-up timing requirements. Integrated PG indicator notifies the system about the status of the load switch to facilitate seamless power sequencing. During shutdown, the device has very low leakage current thereby reducing unnecessary leakages for downstream modules during standby. The DML3017LDC also has an embedded 100Ω on-chip resistor on BLEED pin for quick discharge of the output when switch is disabled.

Enable Control

The DML3017LDC device allows for enabling the MOSFET in an active-high configuration. When the V_{CC} supply pin has an adequate voltage applied, and the EN pin is at logic-high level, the MOSFET is enabled. Similarly, when the EN pin is at logic-low level, the MOSFET is disabled. An internal pulldown resistor to ground on the EN pin ensures that the MOSFET is disabled when not being driven.

Power Sequencing

The DML3017LDC device functions with fixed power sequence. The performance of output turn-on delay may vary from what specified. To achieve the specified performance, recommended power sequences are:

- 1.) V_{CC} → V_{IN} → V_{EN}
- 2.) V_{IN} → V_{CC} → V_{EN}

Load Bleed (Quick Discharge)

The DML3017LDC device has an internal bleed discharge device, which is used to bleed the charge off from the load to ground after the MOSFET is disabled. The bleed discharge device is enabled whenever the MOSFET is disabled. The MOSFET and the bleed device are never concurrently active.

The BLEED pin must connect to V_{OUT} either directly or through an external resistor, R_{EXT}. R_{EXT} must not exceed 1kΩ and can be used to increase the total bleed resistance.

To ensure that the power dissipated across R_{BLEED} is kept at safe level, dissipated power of R_{BLEED} needs to be detailedly calculated. The maximum continuous power that dissipates across R_{BLEED} is 0.4W. R_{EXT} can be used to decrease the amount of power dissipated across R_{BLEED}.

Adjustable Rise Time (Slew Rate Control)

The DML3017LDC device has controlled rise time for inrush current control. A capacitor to ground on the SR pin adjusts the rise time. Without a capacitor on SR, the rise time is at its minimum for fastest timing. An approximate equation for the relationship between C_{SR}, V_{VIN}, and rise time when V_{CC} is set to 5V is shown in Equation 1. As shown in Figure 1, rise time defined as from 10% to 90% measurement on V_{OUT}.

$$t_R = K1 \times ((C_{SR} + K2) \times V_{VIN}) / I_{SR}$$

Where: K1 = 0.058 and K2 = 0.04

C_{SR} is the ramp-up control setting capacitor in nF.

I_{SR} = 0.5μA is SR pin output current.

t_R is the total ramp time in ms.

Application Information (continued)

Table 1 contains rise time values measured on a typical device. Rise times shown below are only valid for the power-up sequence 1.

Table 1. Rise Time vs. SR Capacitor

CSR	Rise Time (ms)				
	V _{CC} = 5V, C _L = 0.1μF, R _L = 10Ω, +25°C; Measure V _{OUT} rising time from 10% to 90% V _{VIN}				
	V _{VIN} = 20V	V _{VIN} = 12V	V _{VIN} = 5V	V _{VIN} = 3.3V	V _{VIN} = 1.8V
0 (floating)	0.8	0.6	0.44	0.37	0.26
0.22nF	0.8	0.6	0.44	0.37	0.26
0.47nF	1.04	0.6	0.44	0.37	0.26
1nF	2.23	1.27	0.57	0.38	0.26
2.2nF	5.25	3	1.26	0.87	0.47
4.7nF	10.95	6.31	2.72	1.83	1.01
10nF	22.08	12.91	5.46	3.6	2.09

Note: 6. An SR capacitor less than 47nF for system success startup recommended.

Power-Good

The DML3017LDC device has a power-good output (PG) that can be used to indicate when the gate of the MOSFET is driven high and the switch is on with the on-resistance close to its final value (full load ready). The PG pin is an active-high, open-drain output that requires an external pullup resistor, R_{PG}, greater than or equal to 1kΩ to an external voltage source, V_{TERM}, compatible with input levels of those devices connected to this pin. Equation 2 approximately shows the relationship between CSR, V_{VIN}, and PG turn-on time, t_{PG_ON}.

$$t_{PG_{ON}} = 1.2 + K3 \times t_R + K4 \times C_{SR}$$

Where:

t_{PG_ON} is the PG turn-on time (ms)

K3 / K4 is constant, which is K3 = 1.28 and K4 = 0.42

Table 2 contains PG turn-on time values measured on a typical device. PG turn-on times shown below are valid for the power-up sequence 1.

Table 2. PG Turn-On Time vs. SR Capacitor

CSR	PG Turn-On Time (ms)				
	V _{CC} = 5V, C _L = 0.1μF, R _L = 10Ω, R _{PG} = 100kΩ, +25°C				
	V _{VIN} = 20V	V _{VIN} = 12V	V _{VIN} = 5V	V _{VIN} = 3.3V	V _{VIN} = 1.8V
0 (floating)	2.87	2.2	1.7	1.58	1.45
0.22nF	2.87	2.2	1.7	1.58	1.45
0.47nF	3.12	2.2	1.7	1.58	1.45
1nF	4.51	3	1.86	1.6	1.45
2.2nF	8.5	5.42	2.99	2.43	1.94
4.7nF	16.35	10.43	5.51	4.35	3.33
10nF	32.5	20.76	10.77	8.45	6.44

The power-good output can be used as the enable signal for other active-high devices in the system. This allows for guaranteed by design power sequencing and reduces the number of enable signals required from the system controller. If the power-good feature is not used in the application, the PG pin must tie to GND.

Short-Circuit Protection

The DML3017LDC device is equipped with short-circuit protection that is used to help protect the part and the system from a sudden high-current event, such as the output, V_{OUT}, being shorted to ground. This circuitry is only active when the gate of MOSFET is fully charged.

Once active, the circuitry monitors the difference in the voltage on the V_{IN} pin and the voltage on the BLEED pin. In order for the V_{OUT} voltage to be monitored through the BLEED pin, it is required that BLEED pin be connected to V_{OUT} either directly or through a resistor, R_{EXT}, which should not exceed 1kΩ. With the BLEED pin connected to V_{OUT}, the short-circuit protection is able to monitor the voltage drop across the MOSFET.

If the voltage drop across the MOSFET is greater than or equal to the short-circuit protection threshold voltage, the MOSFET is immediately turned off, and the load bleed is activated. The part remains latched in this off state until EN toggled or V_{CC} supply voltage cycled at which point the MOSFET turns on delay and slew rate. The current through the MOSFET that causes a short-circuit event can be calculated by dividing the short-circuit protection threshold by expected on-resistance of the MOSFET.

Application Information (continued)

Thermal Shutdown

The DML3017LDC device is equipped with thermal shutdown protection for internally or externally generated excessive temperatures. This circuitry is disabled when EN is not active to reduce standby current. When an overtemperature condition is detected, the MOSFET immediately turns off, and the load bleed is active.

The part comes out of thermal shutdown when the junction temperature decreases to a safe operating temperature as dictated by the thermal hysteresis. Upon exiting a thermal shutdown state and if EN remains active, the MOSFET turns on in a controlled fashion with the normal output turn-on delay and slew rate.

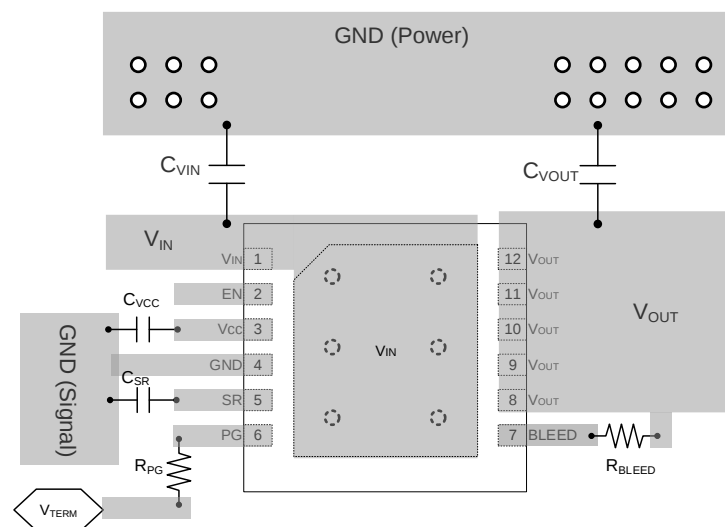
Undervoltage Lockout

The DML3017LDC device is equipped with undervoltage lockout protection. DML3017LDC turns the MOSFET off and activates the load bleed when the input voltage, V_{IN} , is less than or equal to the undervoltage lockout threshold. This circuitry is disabled when EN is not active to reduce standby current.

If the V_{CC} voltage rises above the undervoltage lockout threshold and EN remains active, the MOSFET turns on in a controlled fashion with the normal output turn-on delay and slew rate.

PCB Layout Consideration

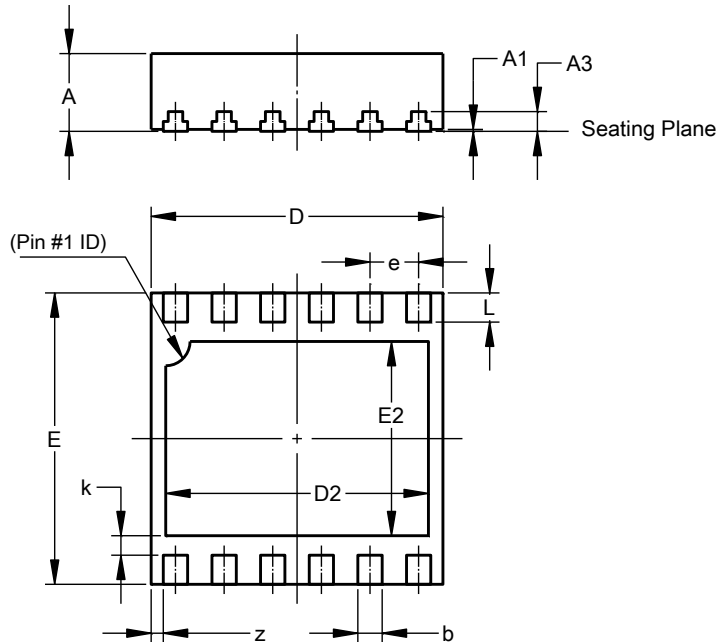
1. Place the input/output capacitors C_{VIN} and C_{VOUT} as close as possible to the V_{IN} and V_{OUT} pins.
2. The power traces, which are V_{IN} trace, V_{OUT} trace, and GND trace, should be short, wide, and direct for minimize parasitic inductance.
3. Place feedback resistance R_{BLEED} as close as possible to BLEED pin.
4. The SR trace must be as short as possible to reduce parasitic capacitance.
5. Place C_{VCC} capacitor near the device pin.
6. Connect the signal ground to the GND pin, and keep a single connection from GND pin to the power ground behind the input or output capacitors.
7. For better power dissipation, via holes are recommended to connect the exposed pad's landing area to a large copper polygon on the other side of the PCB. The copper polygons and exposed pad shall connect to V_{IN} pin on the printed circuit board.



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

V-DFN3030-12 (Type B)

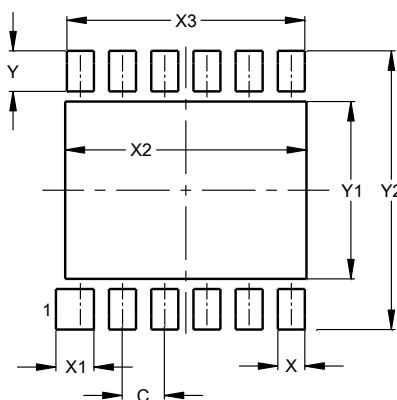


V-DFN3030-12 (Type B)			
Dim	Min	Max	Typ
A	0.77	0.85	0.80
A1	0.00	0.05	0.02
A3	--	--	0.203
b	0.20	0.30	0.25
D	2.95	3.05	3.00
D2	2.60	2.80	2.70
E	2.95	3.05	3.00
E2	1.90	2.10	2.00
e	0.50BSC		
k	--	--	0.20
L	0.25	0.35	0.30
z	--	--	0.125
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

V-DFN3030-12 (Type B)



Dimensions	Value (in mm)
C	0.50
X	0.32
X1	0.45
X2	2.86
X3	2.82
Y	0.48
Y1	2.10
Y2	3.30

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