

Pin Assignments

BS

1



8 SS

(Top View)

Description

The AP3512E is a 500kHz fixed frequency, current mode, PWM synchronous buck (step-down) DC-DC converter, capable of driving a 2A load with high efficiency, excellent line and load regulation. The AP3512E exhibits high efficiency at light load. The device integrates N-channel power MOSFET switch with low on-resistance. Current mode control provides fast transient response and cycle-by-cycle current limit.

The AP3512E employs complete protection to ensure system security, including output Over Voltage Protection, input Under Voltage Lock Out, programmable Soft-start, Over Temperature Protection and hiccup mode Short Circuit Protection.

This IC is available in SO-8 and SO-8EP packages.

Features

- Input Voltage Range: 4.5V to 18V
- Fixed 500kHz Frequency
- High Efficiency at Light Load
- Output Current: 2A
- Current Mode Control
- **Built-In Over Current Protection**
- **Built-In Thermal Shutdown Function**
- **Built-In UVLO Function**
- **Built-In Over Voltage Protection**
- Programmable Soft-Start
- Hiccup Mode SCP
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- 2 IN 7 ΕN SW 3 6 COMP FB GND 4 5 **SO-8** (Top View) BS 1 (己 8 SS IN 2 7 ΕN FF COMP SW 3 6 GND FB 4 5

SO-8EP

Applications

- Monitor
- TV
- STB
- Datacom

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. Notes:

See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and 2 Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Typical Applications Circuit



Pin Descriptions

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Pin Number	Pin Name	Function
1	BS	Bootstrap pin. A bootstrap capacitor is connected between the BS pin and SW pin. The voltage across the bootstrap capacitor drives the internal high-side NMOS switch.
2	IN	Supply input pin. A capacitor should be connected between the IN pin and GND pin to keep the DC input voltage constant.
3	sw	Power switch output pin. This pin is connected to the inductor and bootstrap capacitor.
4	GND	Ground pin
5	FB	Feedback pin. This pin is connected to an external resistor divider to program the system output voltage. When the FB pin voltage exceeds 1.1V, the over voltage protection is triggered. When the FB pin voltage is below 0.3V, the oscillator frequency is lowered to realize short circuit protection.
6	COMP	Compensation pin. This pin is the output of the transconductance error amplifier and the input to the current comparator. This pin is used to compensate the control loop. Connect a series RC network from this pin to GND pin. In some cases, an additional capacitor from this pin to GND pin is required.
7	EN	Enable Input. EN is a digital input that turns the regulator on or off. Drive EN high to turn on the regulator, drive it low to turn off. Pull up with $100k\Omega$ resistor for automatic startup.
8	SS	Soft-start control input pin. SS controls the soft start period. Connect a capacitor from SS to GND to set the soft-start period. A 0.1μ F capacitor sets the soft-start period to 15ms. To disable the soft-start feature, leave SS unconnected.
_	EP	Exposed pad. It should be connected to GND in PCB layout.



AP3512E

Functional Block Diagram





Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Value	Unit
V _{IN}	IN Pin Voltage	-0.3 to 20	V
V _{EN}	EN Pin Voltage	-0.3 to V _{IN}	V
Vsw	SW Pin Voltage	21	V
V _{BS}	BS Pin Voltage	-0.3 to V _{SW} +6	v
V _{FB}	FB Pin Voltage	-0.3 to 6	v
VCOMP	COMP Pin Voltage	-0.3 to 6	V
V _{SS}	SS Pin Voltage	-0.3 to 6	V
TJ	Operating Junction Temperature	+150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
T _{LEAD}	Lead Temperature (Soldering, 10s)	+260	°C
θ_{JA}	Thermal Resistance (Junction to Ambient)	SO-8 105 SO-8EP 60	°C/W
V _{HBM}	ESD (Human Body Model)	2000	V
V _{MM}	ESD (Machine Model)	200	V

Note 4: Stresses greater than those listed under "*Absolute Maximum Ratings*" can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "*Recommended Operating Conditions*" is not implied. Exposure to "*Absolute Maximum Ratings*" for extended periods can affect device reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Мах	Unit
V _{IN}	Input Voltage	4.5	18	V
TA	Operating Ambient Temperature	-40	+85	°C
7	<u> </u>			



Electrical Characteristics (V_{IN}=V_{EN}=12V, V_{OUT}=3.3V, T_A=+25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
SUPPLY VOLTA	AGE (IN PIN)					
V _{IN}	Input Voltage	_	4.5	_	18	V
l _Q	Quiescent Current	V _{FB} =1V, V _{EN} =3.3V	_	1.2	1.4	mA
I _{SHDN}	Shutdown Supply Current	V _{EN} =0V	_	0.1	1.0	μA
UNDER VOLTA	GE LOCKOUT					
Vuvlo	Input UVLO Threshold	VIN Rising	3.65	4.0	4.25	V
V _{HYS}	Input UVLO Hysteresis	_		0.2	-	V
ENABLE (EN PI	N)					
_	EN Shutdown Threshold Voltage	-	1.1	1.5	2	V
_	EN Shutdown Threshold Voltage Hysteresis (Note 5)	-	-	350	- 1	mV
_	EN Lockout Threshold Voltage	-	2.2	2.5	2.7	V
_	EN Lockout Hysteresis	A M	-	210	—	mV
VOLTAGE REFI	ERENCE (FB PIN)					
V _{FB}	Feedback Voltage	+	0.907	0.925	0.943	V
V _{FBOV}	Feedback Over Voltage Threshold	-	_	1.1	—	V
I _{FB}	Feedback Bias Current	V _{FB} =1V	-0.1	—	0.1	μA
MOSFET						
RDSONH	High-Side Switch On-resistance (Note 6)	I _{SW} =0.2A and 0.7A	_	100	—	mΩ
R _{DSONL}	Low-Side Switch On-resistance (Note 6)	I _{SW} =-0.2A and -0.7A	_	100	—	mΩ
CURRENT LIMI		•				
ILEAKH	High-Side Switch Leakage Current	V _{IN} =18V, V _{EN} =0V, V _{SW} =0V	-	0.1	10	μA
Іцімн	High-Side Switch Current Limit	_	4.3	5.6	_	А
ILIML	Low-Side Switch Current Limit	From drain to source	_	50	_	mA
SWITCHING RE	GULATOR					
f _{OSC1}	Oscillator Frequency	_	410	500	590	kHz
fosc2	Short Circuit Oscillator Frequency	_	_	180	_	kHz
D _{MAX}	Maximum Duty Cycle	V _{FB} =0.85V	_	90	_	%
D _{MIN}	Minimum Duty Cycle	V _{FB} =1V	_	_	0	%
ERROR AMPLIE	FIER					
A _{EA}	Error Amplifier Voltage Gain (Note 5)	_	_	400	_	V/V
G _{EA}	Error Amplifier Transconductance	_	_	800	_	μA/V
G _{CS}	COMP to Current Sense Transconductance	_	_	5.2	_	A/V



Electrical Characteristics (continued) (VIN=VEN=12V, VOUT=3.3V, TA=+25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
THERMAL SHUTDOWN						
T _{OTSD}	Thermal Shutdown (Note 5)	_	_	+160		°C
T _{HYS}	Thermal Shutdown Hysteresis (Note 5)	_	_	+30		°C
SOFT START (SS PIN)						
tss	Soft-Start Time (Note 5)	C _{SS} =0.1µF	-	15	_	ms
_	Soft-Start Current	_	7	5	_	μA

Notes: 5. Not tested, guaranteed by design.

 $\text{6. } \mathsf{R}_{\mathsf{DS(ON)}}\text{=}\frac{V_{SW1}\text{-}V_{SW2}}{I_{SW1}\text{-}I_{SW2}}$

AP3512E Document number: DS38502 Rev. 4 - 3



Performance Characteristics (V_{IN}=12V, V_{OUT}=3.3V, L=4.7µH, T_A=+25°C, unless otherwise noted.)





Performance Characteristics (continued) (V_{IN}=12V, V_{OUT}=3.3V, L=4.7µH, T_A=+25°C, unless otherwise noted.)





Ordering Information



All rube versions are End of Life with replacement in rape & Reel versions.
For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





Package Outline Dimensions (All dimensions in mm(inch).)

Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SO-8



Note: Eject hole, oriented hole and mold mark is optional.



Package Outline Dimensions (continued. All dimensions in mm (inch).)

Please see http://www.diodes.com/package-outlines.html for the latest version.

(2) Package Type: SO-8EP



Note: Eject hole, oriented hole and mold mark is optional.





Suggested Pad Layout Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SO-8



Dimensions	Z	G	X	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050



Suggested Pad Layout (continued) Please see http://www.diodes.com/package-outlines.html for the latest version.

(2) Package Type: SO-8EP



Dimensions	Z	G	X	Y	X1	Y1	E
	(mm)/(inch)						
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	3.600/0.142	2.700/0.106	1.270/0.050





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