

CY8C4247AZA-M483

Status: In Production



Product Specs	Pricing & Inventory	Packaging/Ordering	Quality and RoHS	Technical Documents
CY8C4247AZA-M483				DESIGN SUPPORT
Automotive Qualified	Y			Development Kits
CPU Core		ARM Cortex-M0		Forums
CapSense	Y			Knowledge Base
Dedicated ADC (#Max Resolution @ Sample Rate)		SAR (1, 12-bit @ 1 msp/s)		Packaging
EEPROM (KB)	0			Product Brochures
Flash (KB)	128			Quality & Reliability
JTAG and SI ID		0x11A5		Samples
LCD Direct Drive	Y			Technical Support
Max. Operating Frequency (MHz)	48			Technical Training
Max. Operating Temp. (°C)	85			Third Party Network (CYPros)
Max. Operating Voltage (V)	5.50			
Min. Operating Temp. (°C)	-40			
Min. Operating Voltage (V)	1.71			
No. of CAN Controllers	2			
No. of Continuous Time Blocks	2			
No. of DMA Channels	8			
No. of Dedicated Comparators	2			
No. of Dedicated Timer/Counter/PWM Blocks	8			
No. of GPIOs	38			
No. of Op Amps	4			
No. of SIO	0			
No. of Serial Communication Blocks	4			
No. of USB IO	0			
No. of Universal Analog Blocks	0			
No. of Universal Digital Blocks	4			
PLL	N			
SRAM (KB)	16			
Series		PSoC 4200M		
Tape & Reel	N			
USB (Type)	None			

PRICING & INVENTORY AVAILABILITY					
1-9 unit Price*	10-24 unit Price*	25-99 unit Price*	100-249 unit Price*	250-999 unit Price*	1000+ unit Price*
Contact Sales					

Availability	Quantity	Ships In	Buy from Cypress	Buy from Distributors
Out of Stock	0	Please click here to check lead times	Contact Sales	Contact Sales

* Prices are shown in United States dollars and are for budgetary use only for volume of 1ku

PACKAGING/ORDERING	
Package	TQFP
No. of Pins	44
Package Dimensions	275 L x 1.4 H x 275 W (Mils)
Package Cross Section Drawing	Download
Package Carrier	TRAY
Standard Pack Quantity	250
Minimum Order Quantity (MOQ)	250
Order Increment	250
Estimated Lead Time (days)	91
HTS Code	8542.31.0001
ECCN	(A3)
ECCN Suball	3A991.A.3

QUALITY AND ROHS	
Moisture Sensitivity Level (MSL)	3
Peak Reflow Temp. (°C)	260 (Cypress Reflow Profile)
RoHS Compliant	Y Print RoHS Certificate of Compliance
Pb Free	Y
Lead/Ball Finish	Ni/Pd/Au
Marking	Cypress Marking Format

Device Qualification Reports
 FIT/MTBF, ESD (HBM/CDM) and Latch-up data available in the Device Qualification Report.
[QTP#162707: AUTOMOTIVE PSoC 4100M and PSoC 4200M DEVICE FAMILY SBPFHD-10P, FAB25 \(AL BEOL INTERCONNECT\).pdf](#) Last Update: Mar 06, 2019

TECHNICAL DOCUMENTS	
Application Notes (17)	
AN84858 - PSoC@4 Programming Using an External Microcontroller (HSSP) Jan 19, 2020	
AN93637 - PSoC@ 4 Sensorless Field-Oriented Control (FOC) Dec 30, 2019	
AN82156 - PSoC@ 3, PSoC 4, and PSoC SLP - Designing PSoC Creator™ Components With UDB Datapaths Oct 02, 2019	
AN82250 - PSoC Creator - Implementing Programmable Logic Designs with Verilog Sep 22, 2019	
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CORPORATE HEADQUARTERS Cypress Semiconductor Corp. 198 Champion Court San Jose, CA 95134 USA Tel: +1-408-943-2600	CUSTOMER SERVICE SUPPORT +1-800-541-4736 Hours: 8:00AM - 5:00PM (local time) Create a MyCase Cypress Developer Community	INTERNATIONAL SUPPORT +1-408-943-2600 United States +1-800-541-4736 Hours: 4:30AM - 1:30PM (pacific time) 7:30PM - 4:30AM (standard time)	OTHER REQUESTS Contact Us Report a Website Problem Report a Security Vulnerability
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[MB96F001YBPMC1-GSE1](#) [MB9BF121KPMC-G-JNE2](#) [VA10800-D000003PCA](#) [CP8547AT](#) [CY9AF156NPMC-G-JNE2](#)
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[MB9BF306RBPMC-G-JNE2](#) [MB9BF618TBGL-GE1](#) [ATSAMS70N21A-CN](#) [LPC1754FBD80](#) [STM32F030K6T6TR](#) [AT91M42800A-33AU](#)
[ATSAM3N0AA-MU](#) [ATSAM3N0CA-CU](#) [ATSAM3SD8BA-MU](#) [ATSAM4LC2BA-UUR](#) [ATSAM4LS2AA-MU](#) [ADuC7023BCPZ62I-R7](#)
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