

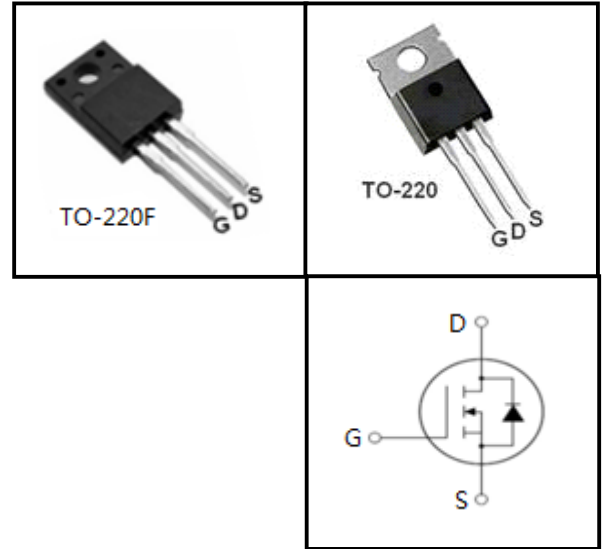
800V N-Channel MOSFET

FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information		
Device	Package	Marking
CS7N80F	TO-220F	CS7N80F
CS7N80P	TO-220	CS7N80P

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted				
Parameter	Symbol	Value		Unit
		TO-220F	TO-220	
Drain-Source Voltage ($V_{GS} = 0V$)	V_{DSS}	800		V
Continuous Drain Current	I_D	7		A
Pulsed Drain Current (note1)	I_{DM}	28		A
Gate-Source Voltage	V_{GSS}	± 30		V
Single Pulse Avalanche Energy (note2)	E_{AS}	245		mJ
Avalanche Current (note1)	I_{AS}	7		A
Repetitive Avalanche Energy (note1)	E_{AR}	147		mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	25	70	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	$-55 \sim +150$		$^\circ\text{C}$

Thermal Resistance				
Parameter	Symbol	Value		Unit
		TO-220F	TO-220	
Thermal Resistance, Junction-to-Case	R_{thJC}	5	1.78	K/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5	60	

Specifications $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	800	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 800V, V _{GS} = 0V, T _J = 25°C	--	--	1	μA
		V _{DS} = 640V, V _{GS} = 0V, T _J = 125°C	--	--	100	
Gate-Source Leakage	I _{GSS}	V _{GS} = ±30V	--	--	±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	3.0	--	4.0	V
Drain-Source On-Resistance (Note3)	R _{DS(on)}	V _{GS} = 10V, I _D = 3.5A	--	1.35	1.6	Ω
Dynamic						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1.0MHz	--	1178	--	pF
Output Capacitance	C _{oss}		--	128	--	
Reverse Transfer Capacitance	C _{rss}		--	27	--	
Total Gate Charge	Q _g	V _{DD} = 640V, I _D = 7A, V _{GS} = 10V	--	49	--	nC
Gate-Source Charge	Q _{gs}		--	6	--	
Gate-Drain Charge	Q _{gd}		--	26	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} = 400V, I _D = 7A, R _G = 25 Ω	--	43	--	ns
Turn-on Rise Time	t _r		--	28	--	
Turn-off Delay Time	t _{d(off)}		--	244	--	
Turn-off Fall Time	t _f		--	54	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I _S	T _C = 25 °C	--	--	7	A
Pulsed Diode Forward Current	I _{SM}		--	--	28	
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 3.5A, V _{GS} = 0V	--	--	1.4	V
Reverse Recovery Time	t _{rr}	V _{GS} = 0V, I _S = 7A, di _F /dt = 100A /μs	--	295	--	ns
Reverse Recovery Charge	Q _{rr}		--	1.7	--	μC

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $L=10mH, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^{\circ}\text{C}$
3. Pulse Test: Pulse width $\leq 300\mu s$, Duty Cycle $\leq 1\%$

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^\circ\text{C}$)

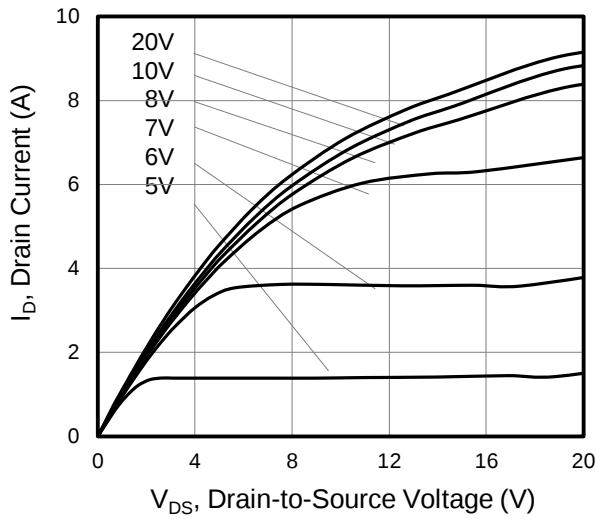


Figure 2. Body Diode Forward Voltage

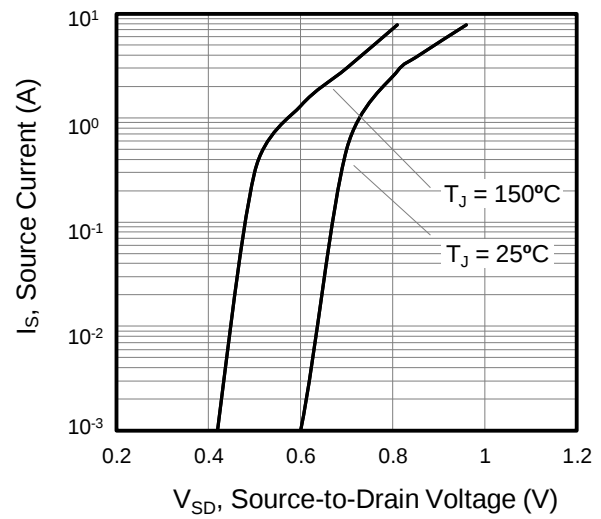


Figure 3. Drain Current vs. Temperature

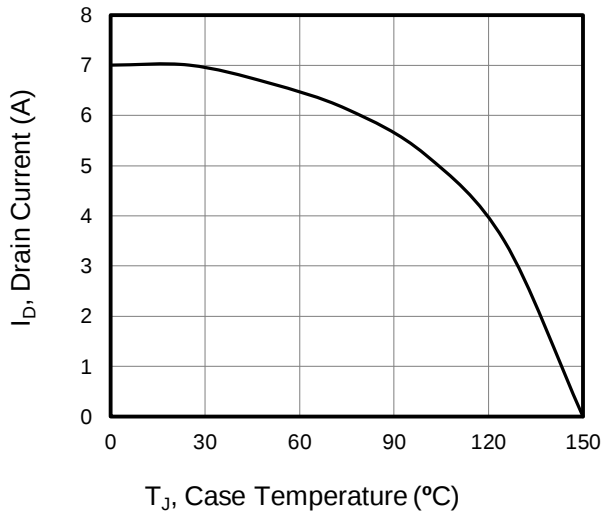


Figure 4. BV_{DSS} Variation vs. Temperature

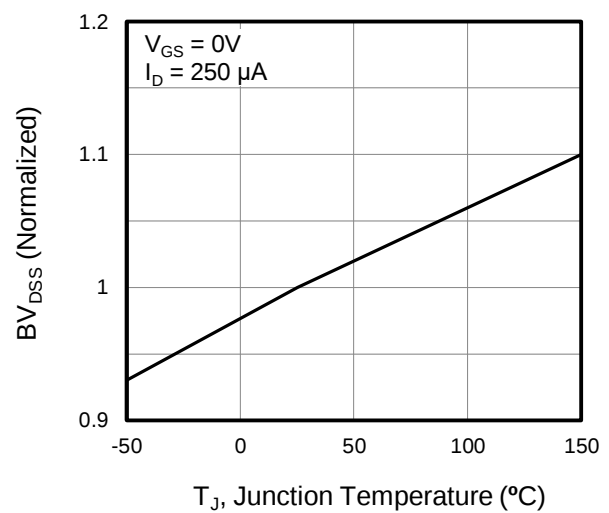


Figure 5. Transfer Characteristics

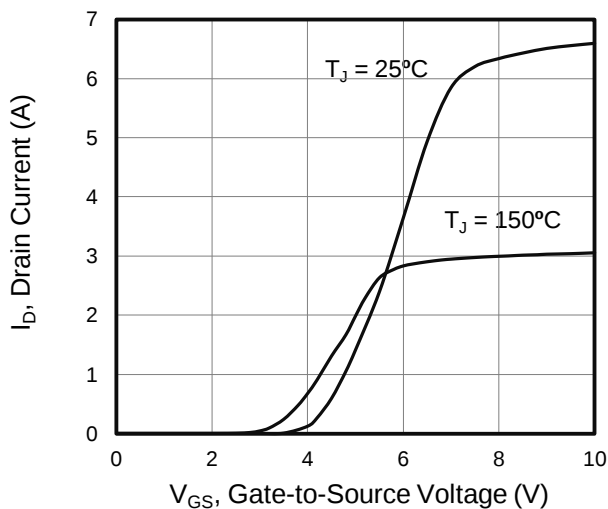
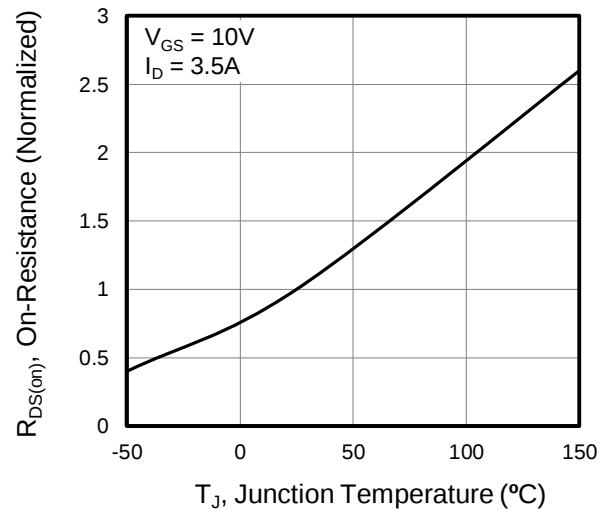


Figure 6. On-Resistance vs. Temperature



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Capacitance

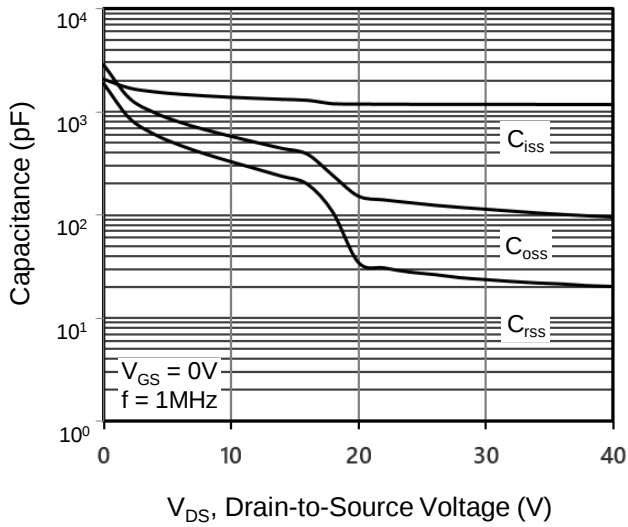


Figure 8. Gate Charge

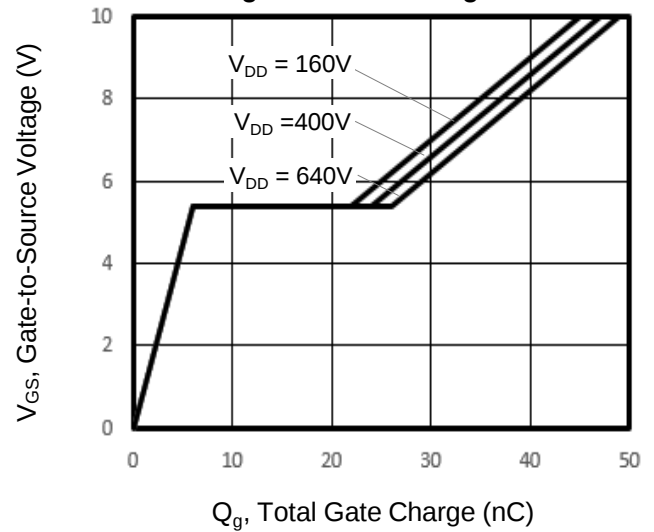


Figure 9. Transient Thermal Impedance

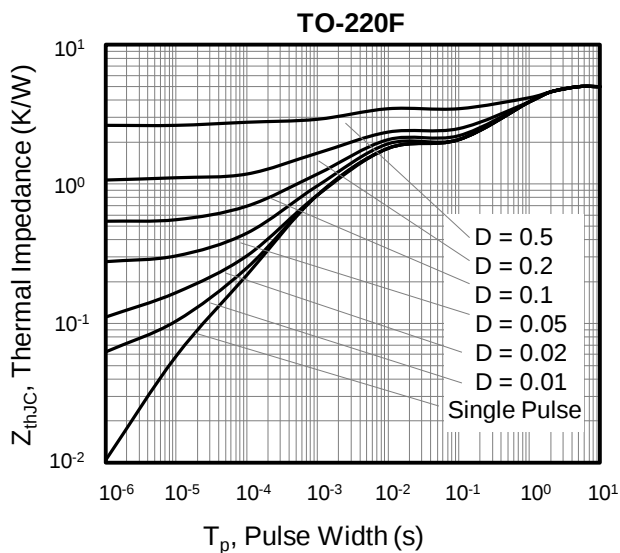


Figure 10. Transient Thermal Impedance

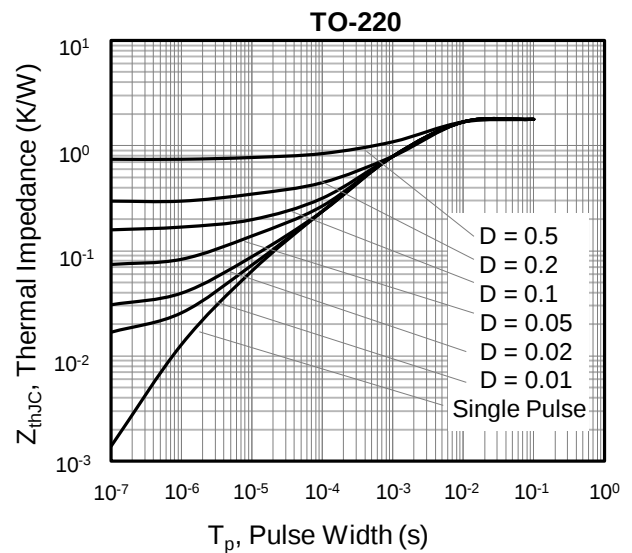
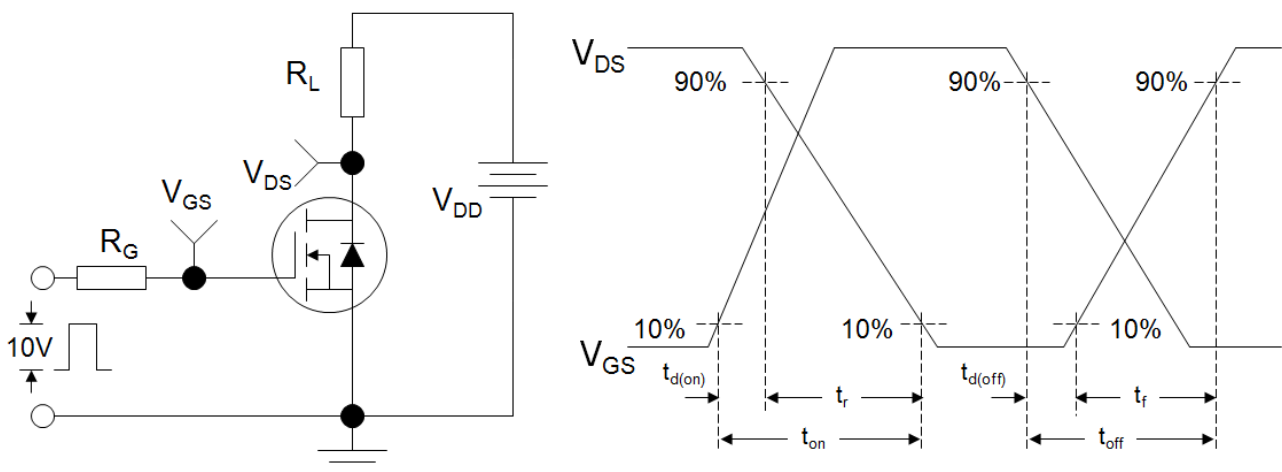
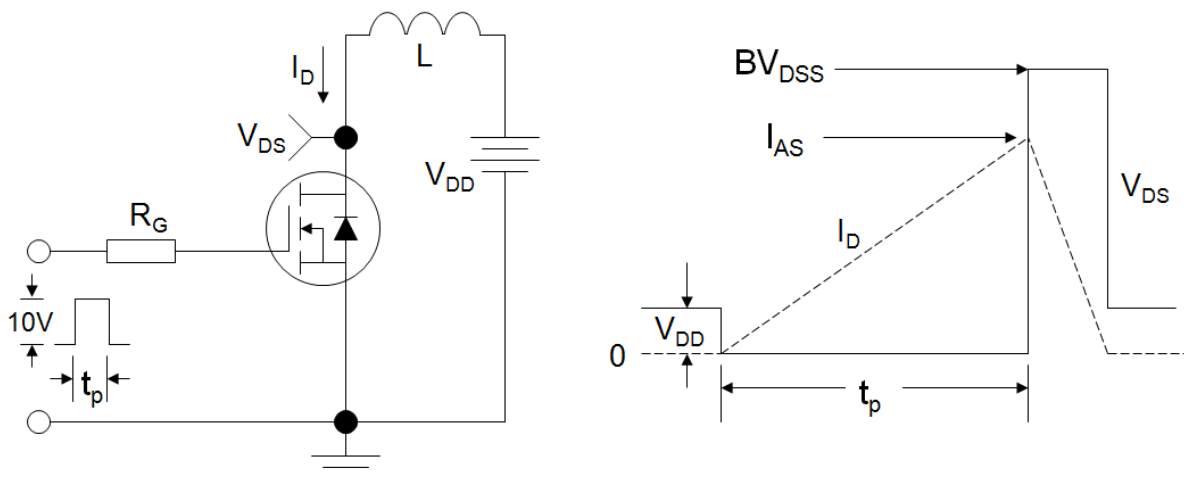
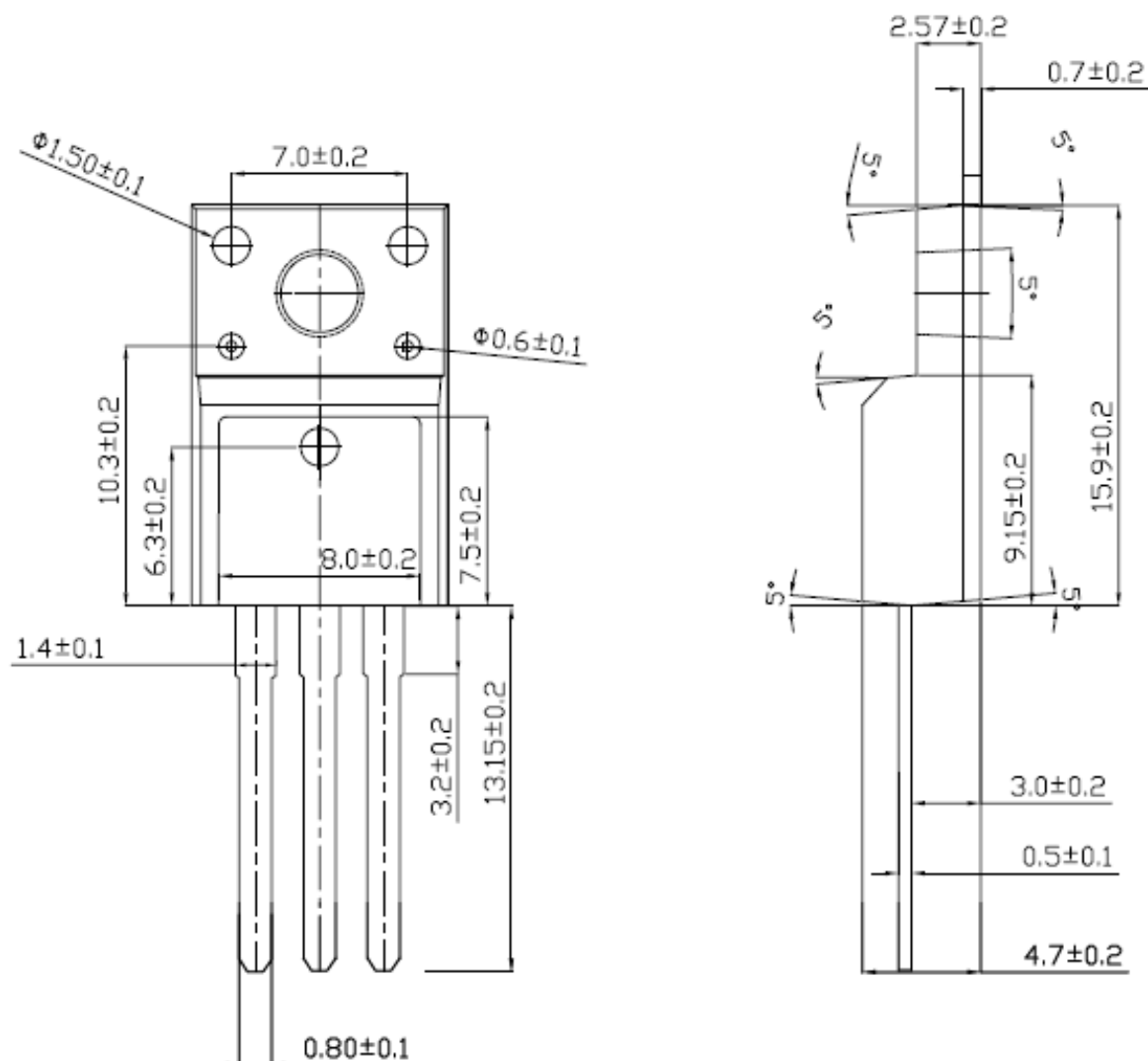


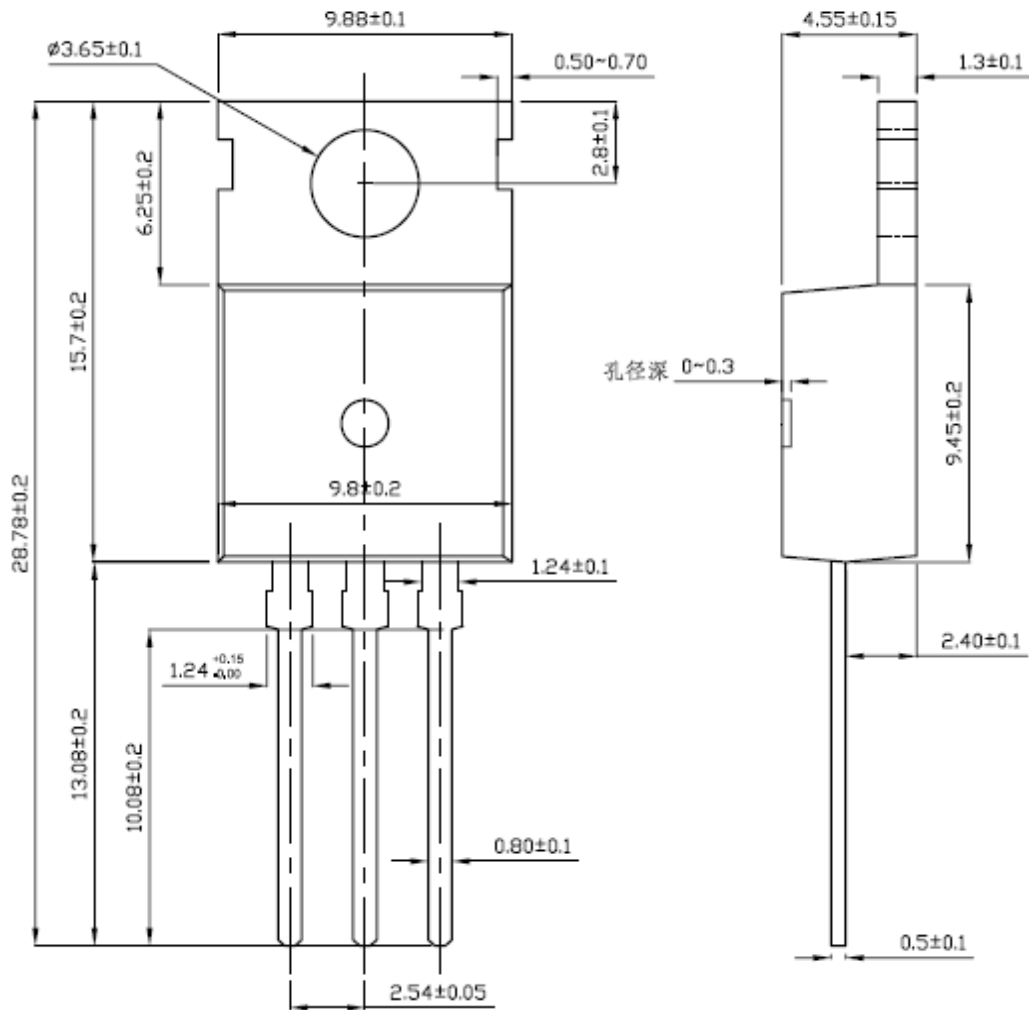
Figure A: Gate Charge Test Circuit and Waveform

Figure B: Resistive Switching Test Circuit and Waveform

Figure C: Unclamped Inductive Switching Test Circuit and Waveform


TO-220F



TO-220



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