

# **800V N-Channel MOSFET**

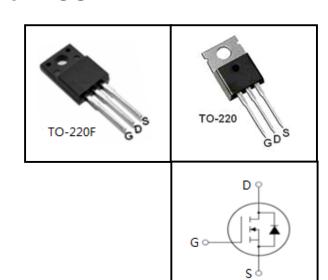
### **FEATURES**

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information			
Device	Package Marking		
CS5N80F	TO-220F	CS5N80F	
CS5N80P	TO-220	CS5N80P	



<b>Absolute Maximum Ratings</b> T <sub>C</sub> = 25°C, unless otherwise noted						
Parameter	Symbol -	Val	l lmit			
Parameter		TO-220F	TO-220	Unit		
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>	800		V		
Continuous Drain Current	I <sub>D</sub>	5		А		
Pulsed Drain Current (note1)	I <sub>DM</sub>	20		А		
Gate-Source Voltage	V <sub>GSS</sub>	±30		V		
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	151		mJ		
Avalanche Current (note1)	I <sub>AS</sub>	5.5		А		
Repetitive Avalanche Energy (note1)	E <sub>AR</sub>	90		mJ		
Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>D</sub>	25	70	W		
Operating Junction and Storage Temperature Range	$T_J, T_stg$	-55~+150		°C		

Thermal Resistance					
Bergmater	Symbol	Va	l lmit		
Parameter		TO-220F	TO-220	Unit	
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	5	1.78	12/\\\	
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	62.5	60	K/W	



<b>Specifications</b> $T_J = 25^{\circ}C$ , unless otherwise noted								
Parameter	Symbol	T . O . IIV	Value					
		Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250\mu A$	800			V		
Zero Gate Voltage Drain Current		$V_{DS} = 800V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ		
Zero Gate voltage Drain Gunerit	I <sub>DSS</sub>	$V_{DS} = 640V, V_{GS} = 0V, T_{J} = 125^{\circ}C$	ŀ		100			
Gate-Source Leakage	$I_{\rm GSS}$	$V_{GS} = \pm 30V$			±100	nA		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	R <sub>DS(on)</sub>	$V_{GS} = 10V, I_D = 2.5A$		2.3	2.8	Ω		
Dynamic								
Input Capacitance	$C_{iss}$	V - 0V		667		pF		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 25V,$		77				
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		14				
Total Gate Charge	$Q_g$			27		nC		
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 640V, I_{D} = 5A,$ $V_{GS} = 10V$		3.5				
Gate-Drain Charge	$Q_{gd}$			13				
Turn-on Delay Time	t <sub>d(on)</sub>			37		ns		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 400V, I_{D} = 5A,$		15				
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25 \Omega$		144				
Turn-off Fall Time	t <sub>f</sub>			41				
Drain-Source Body Diode Character	istics							
Continuous Body Diode Current	I <sub>S</sub>				5	A		
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25 °C			20			
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}\text{C}, I_{SD} = 2.5\text{A}, V_{GS} = 0\text{V}$			1.4	V		
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0V, I_{S} = 5A,$		1099		ns		
Reverse Recovery Charge	Q <sub>rr</sub>	di <sub>F</sub> /dt =100A /μs		3.2		μC		

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.  $I_{AS} = 3A$ ,  $V_{DD} = 50V$ ,  $R_G = 25 \Omega$ , Starting  $T_J = 25 \, ^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



# **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted

Figure 1. Output Characteristics ( $T_J = 25^{\circ}C$ )

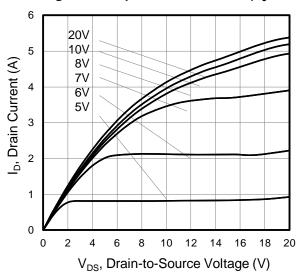


Figure 2. Body Diode Forward Voltage

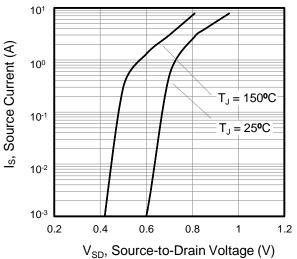


Figure 3. Drain Current vs. Temperature

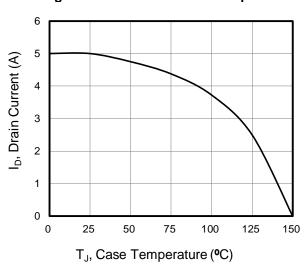


Figure 4. BV<sub>DSS</sub> Variation vs. Temperature

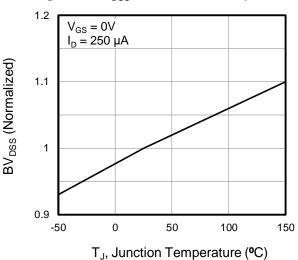


Figure 5. Transfer Characteristics

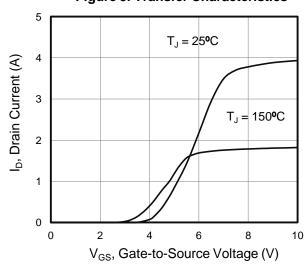
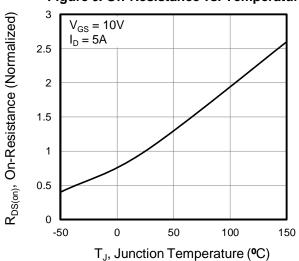


Figure 6. On-Resistance vs. Temperature





### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

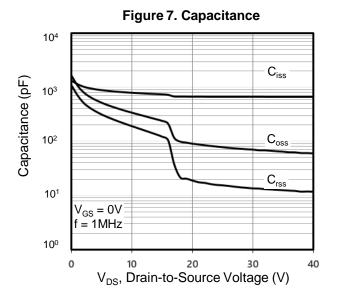
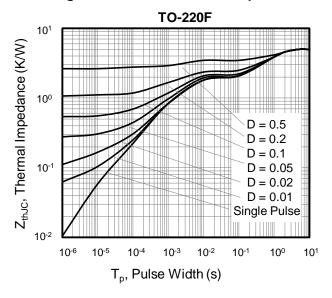


Figure 9. Transient Thermal Impedance



(V) Source Voltage (V) Source Vo

10

0

0

Figure 8. Gate Charge

Figure 10. Transient Thermal Impedance

Q<sub>q</sub>, Total Gate Charge (nC)

20

30

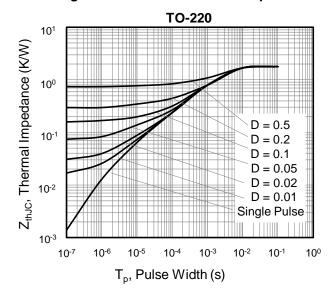




Figure A: Gate Charge Test Circuit and Waveform

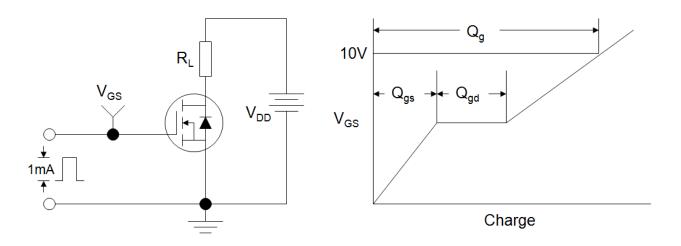


Figure B: Resistive Switching Test Circuit and Waveform

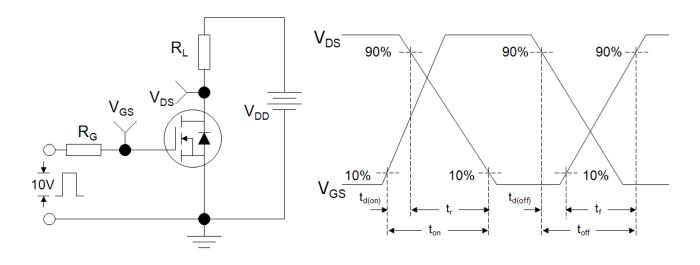
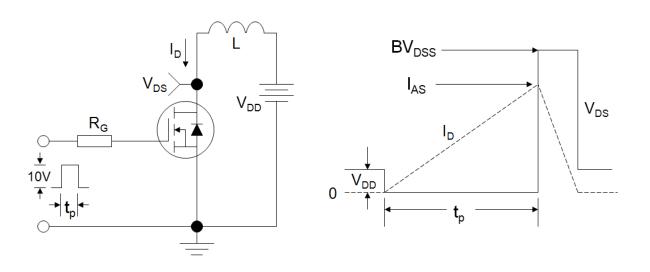
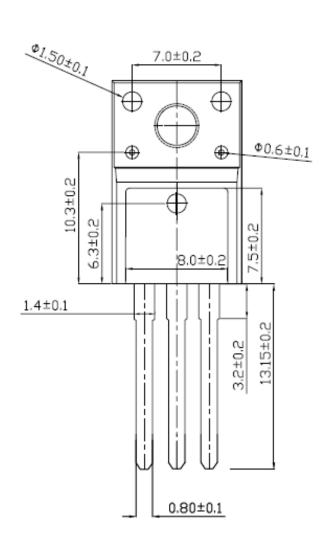


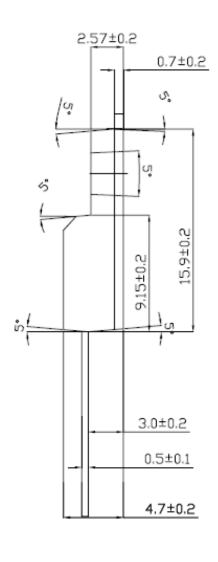
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





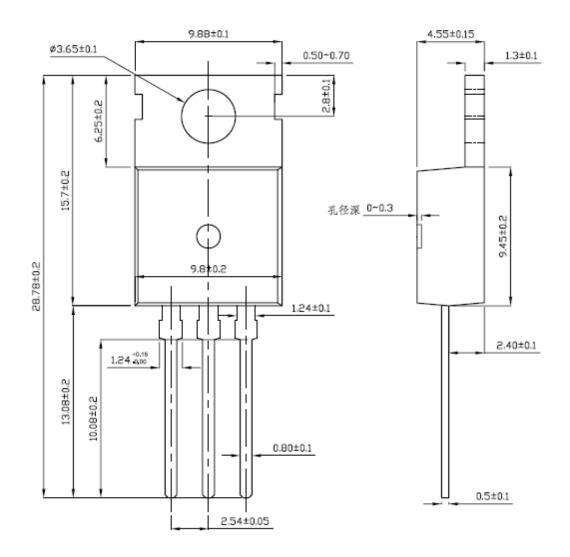
# **TO-220F**







# **TO-220**





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