### **650V N-Channel MOSFET**

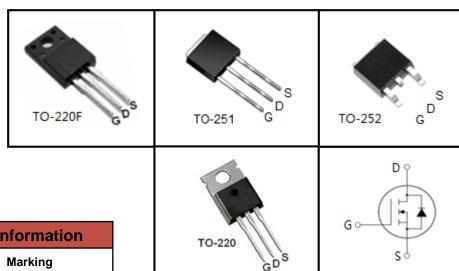
#### **FEATURES**

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information					
Device	Package	Marking			
CS5N65F	TO-220F	CS5N65F			
CS5N65P	TO-220	CS5N65P			
CS5N65U	TO-251	CS5N65U			
CS5N65D	TO-252	CS5N65D			



Parameter		Symbol	Value				
			TO-220F	TO-252	TO-251	TO-220	Unit
Drain-Source Voltage (V <sub>GS</sub> = 0V)		V <sub>DSS</sub>	650				V
Continuous Drain Current		I <sub>D</sub>	5				А
Pulsed Drain Current	(note1)	I <sub>DM</sub>	20				А
Gate-Source Voltage		V <sub>GSS</sub>	±30				V
Single Pulse Avalanche Energy	(note2)	E <sub>AS</sub>	115				mJ
Avalanche Current	(note1)	I <sub>AS</sub>	4.8			А	
Repetitive Avalanche Energy	(note1)	E <sub>AR</sub>	69				mJ
Power Dissipation (T <sub>C</sub> = 25°C)		P <sub>D</sub>	83 54			W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55~+150				°C

Thermal Resistance							
Barrandar	Symbol	Value				11	
Parameter		TO-220F	TO-252	TO-251	TO-220	Unit	
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	2.3		1.5		0004	
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62.5		60		°C/W	



<b>Specifications</b> $T_J = 25^{\circ}$ C, unless otherwise noted									
Parameter	Symbol	Test Conditions	Value			Unit			
rarameter	Syllibol	Test Conditions	Min.	Тур.	Max.	Offic			
Static									
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250\mu A$	650			٧			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ			
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 30V$			±100	nA			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V			
Drain-Source On-Resistance (Note3)	R <sub>DS(on)</sub>	$V_{GS} = 10V, I_D = 2.5A$		1.8	2.1	Ω			
Dynamic									
Input Capacitance	C <sub>iss</sub>	$V_{GS} = 0V$ ,		588		pF			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 25V$ ,		61					
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		7					
Total Gate Charge	$Q_g$			13		nC			
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 520V, I_{D} = 5A,$ $V_{GS} = 10V$		2					
Gate-Drain Charge	$Q_{gd}$	93 -		7					
Turn-on Delay Time	t <sub>d(on)</sub>			36		ns			
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 325V, I_{D} = 5A,$		16					
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25 \Omega$		93					
Turn-off Fall Time	t <sub>f</sub>			27					
Drain-Source Body Diode Character	istics								
Continuous Body Diode Current	Is	T 0500			5	A			
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25 °C			20				
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}\text{C}, I_{SD} = 2.5\text{A}, V_{GS} = 0\text{V}$			1.4	V			
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0V, I_{S} = 5A,$		677		ns			
Reverse Recovery Charge	Q <sub>rr</sub>	di <sub>F</sub> /dt =100A /µs		1.9		μC			

#### **Notes**

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25  $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

ls, Source Current (A)

### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

Figure 1. Output Characteristics (T<sub>J</sub> = 25°C)

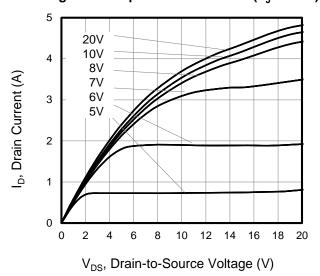


Figure 3. Drain Current vs. Temperature

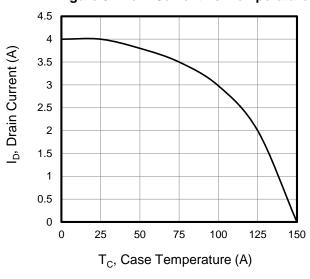


Figure 5. Transfer Characteristics

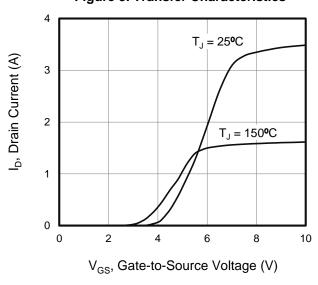


Figure 2. Body Diode Forward Voltage

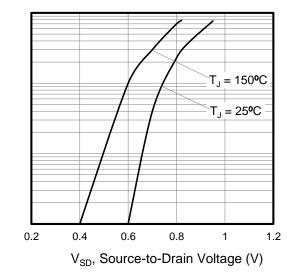


Figure 4. BV<sub>DSS</sub> Variation vs. Temperature

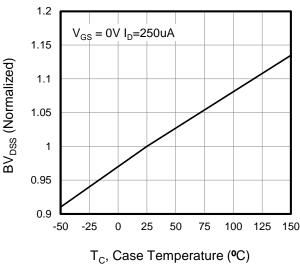
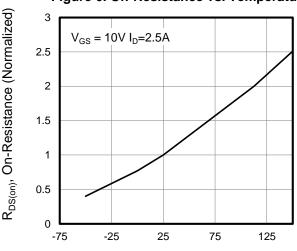


Figure 6. On-Resistance vs. Temperature



### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

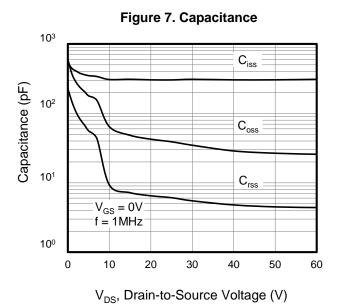


Figure 9. Transient Thermal Impedance TO-220,TO-251,TO-252

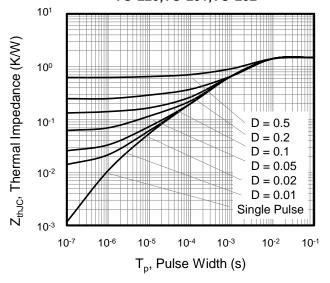
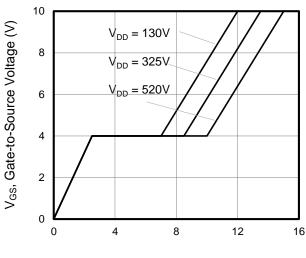


Figure 8. Gate Charge



Q<sub>q</sub>, Total Gate Charge (nC)

Figure 10. Transient Thermal Impedance
TO-220F

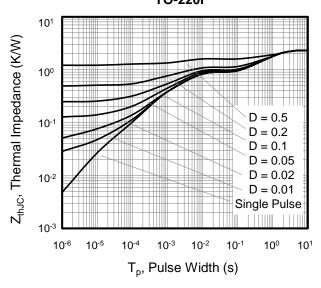


Figure A: Gate Charge Test Circuit and Waveform

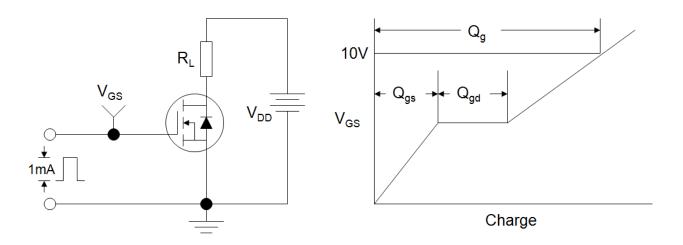


Figure B: Resistive Switching Test Circuit and Waveform

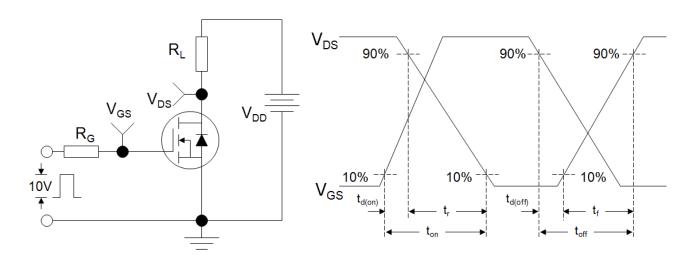
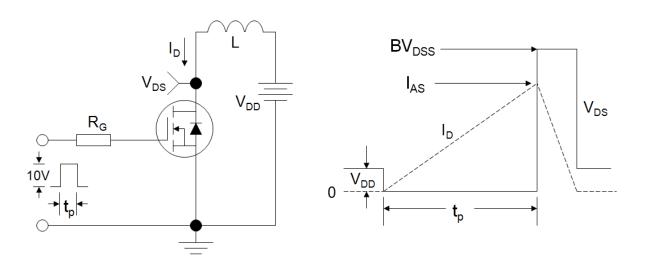
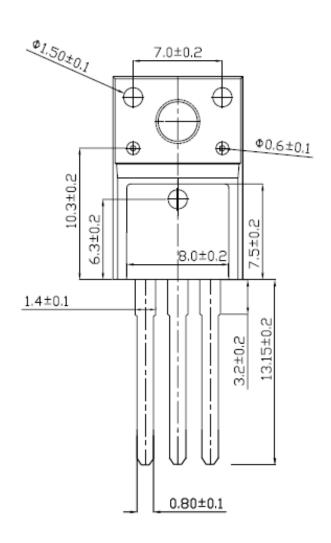
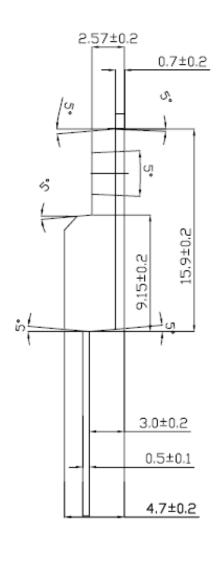


Figure C: Unclamped Inductive Switching Test Circuit and Waveform

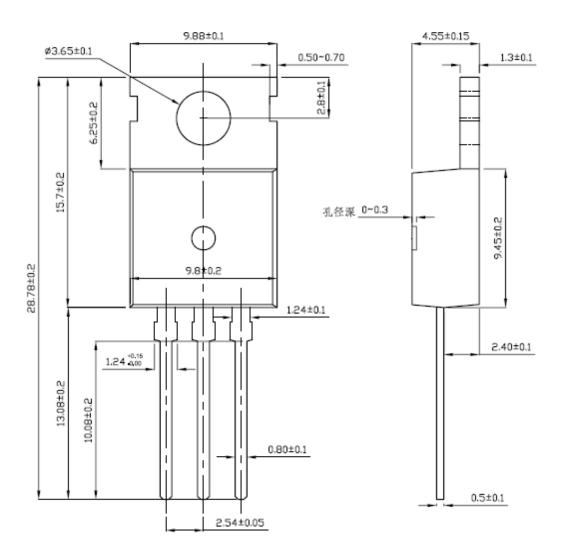


**TO-220F** 

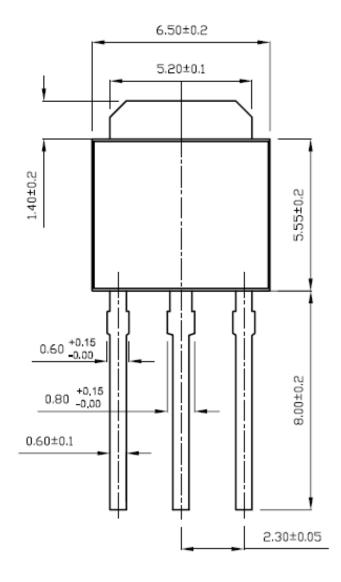


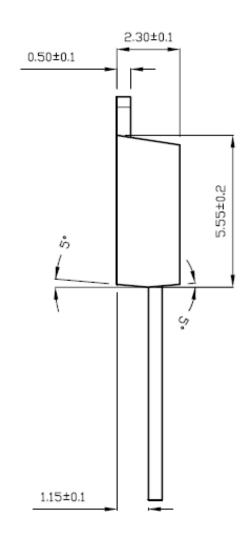


**TO-220** 

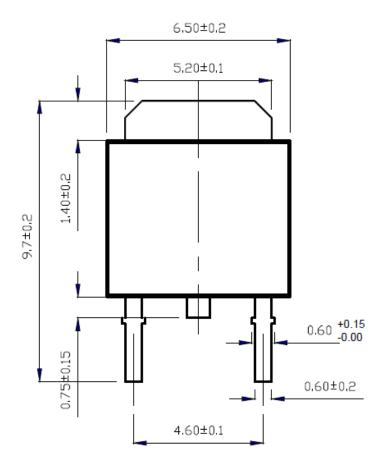


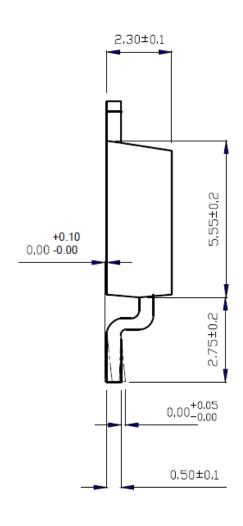
TO-251





**TO-252** 







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